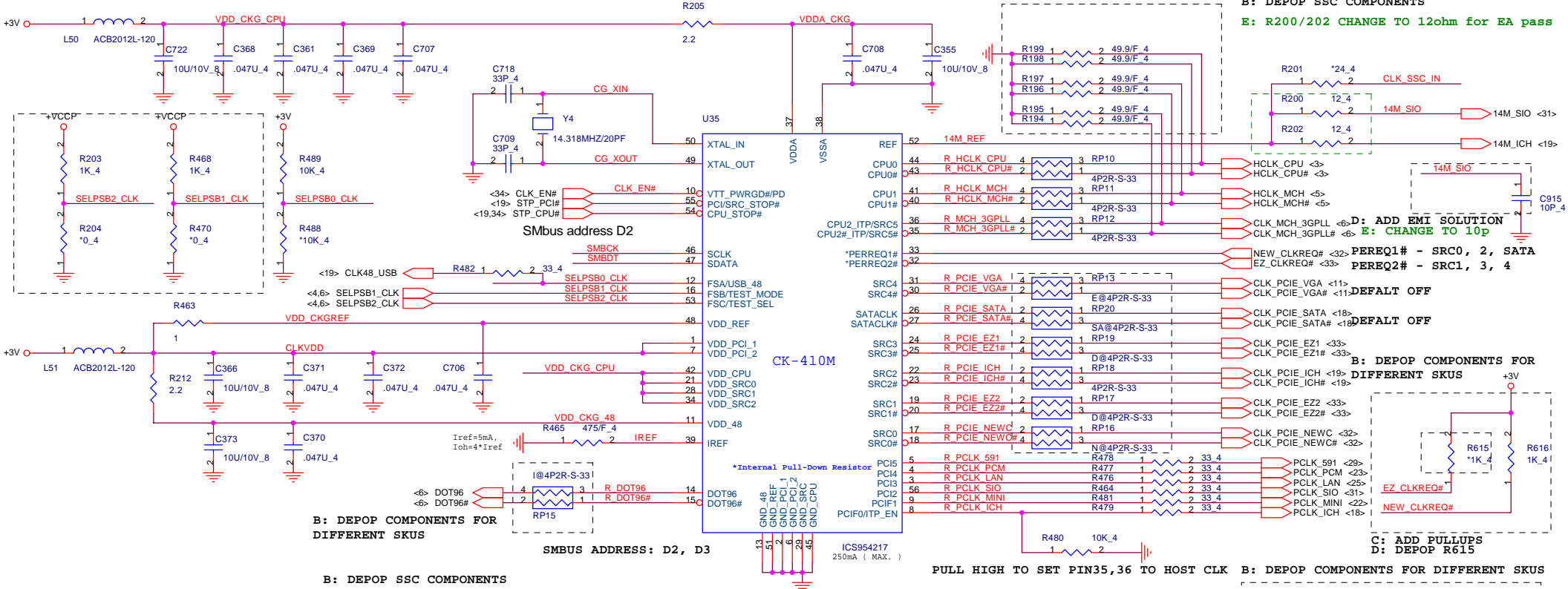


REVB: POP R203 R468 AND DEPOP R204, R470 FOR DOTHAN B

Place these termination to close CK410M.

B: DEPOP SSC COMPONENTS
E: R200/202 CHANGE TO 12ohm for EA pass



B: DEPOP COMPONENTS FOR DIFFERENT SKUS

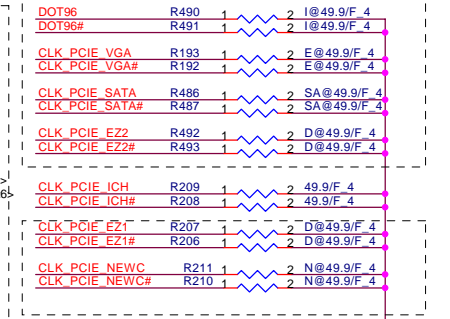
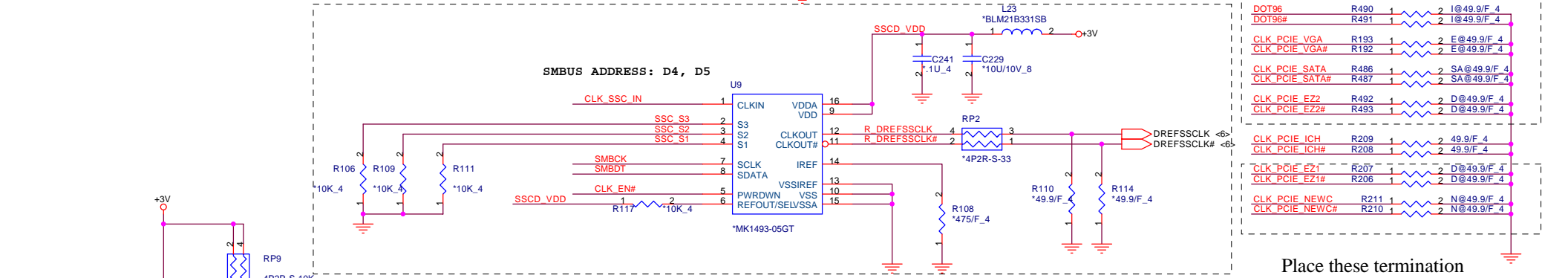
B: DEPOP SSC COMPONENTS

B: DEPOP COMPONENTS FOR DIFFERENT SKUS

C: ADD PULLUPS
D: DEPOP R615

PULL HIGH TO SET PIN35,36 TO HOST CLK

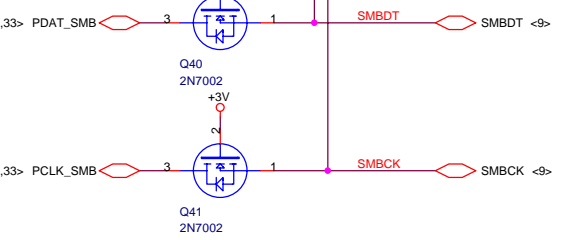
B: DEPOP COMPONENTS FOR DIFFERENT SKUS

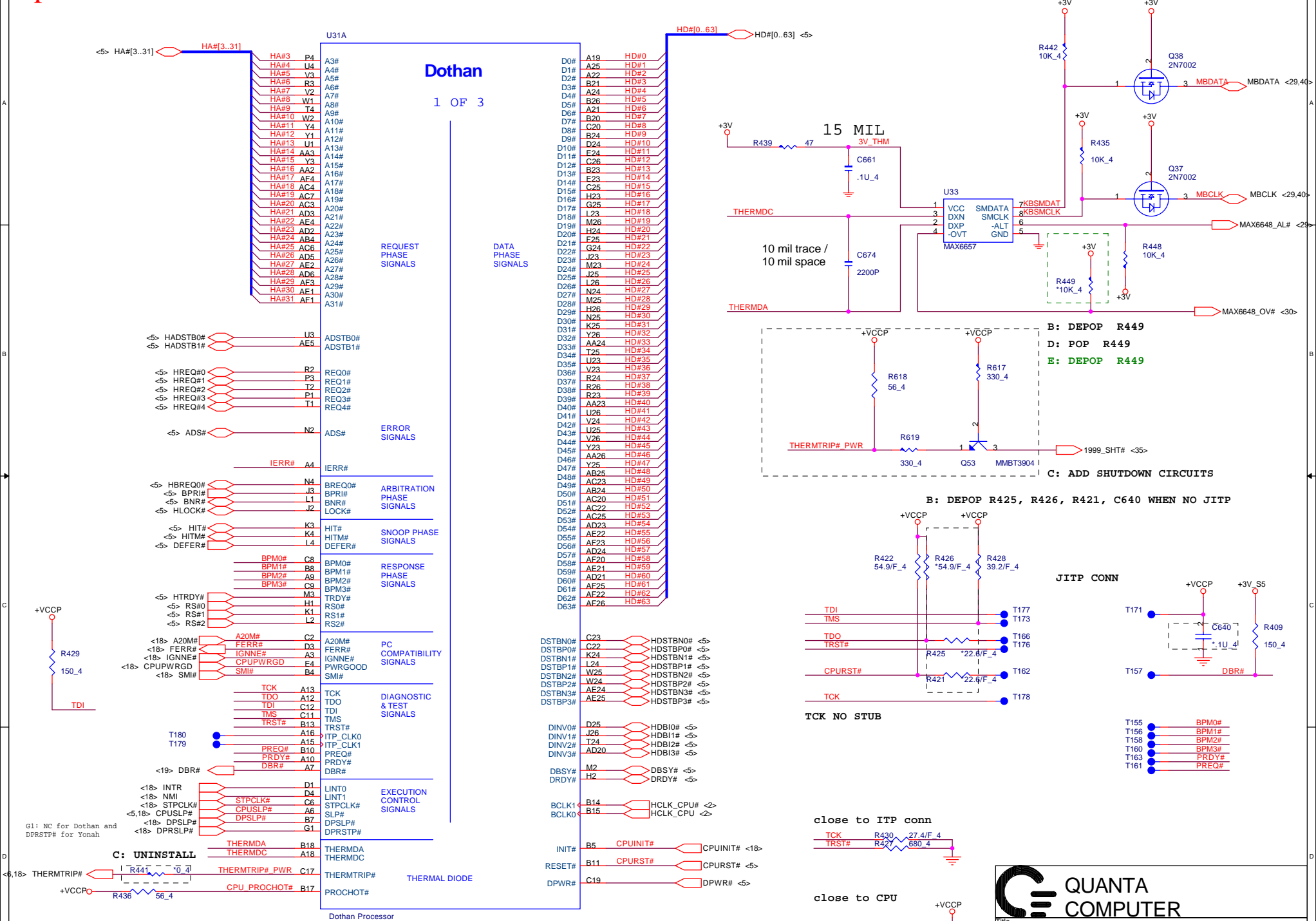


Place these termination to close CK410M.

DOTHAN-A 400
DOTHAN-A 533

FSC	FSB	FSA	CPU	SRC	PCI
1	0	1	100	100	33
0	0	1	133	100	33
0	1	1	166	100	33
0	1	0	200	100	33
0	0	0	266	100	33
1	0	0	333	100	33
1	1	0	400	100	33
1	1	1	RSVD	100	33





QUANTA COMPUTER

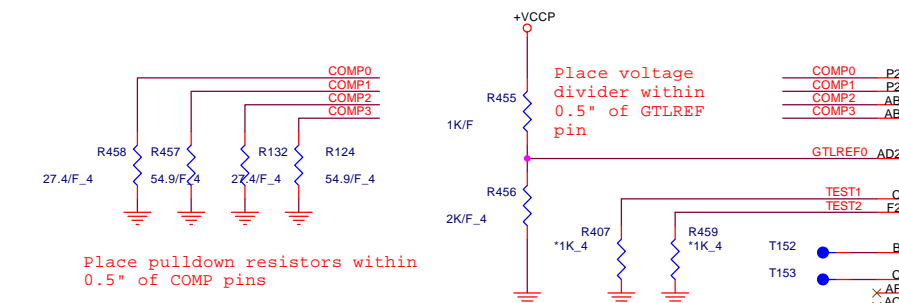
Title: Dothan Processor (HOST)

Size: Document Number ZLZ

Date: Tuesday, December 21, 2004

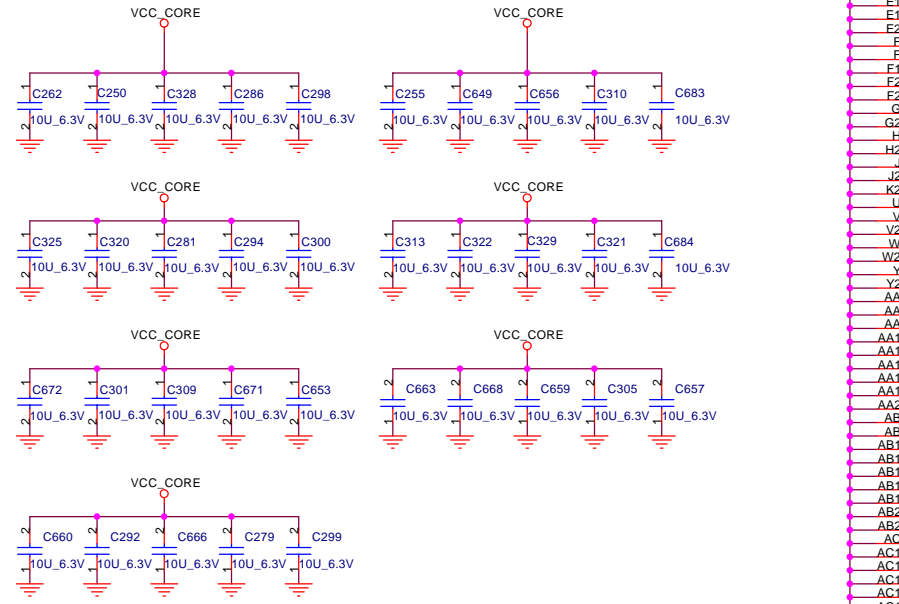
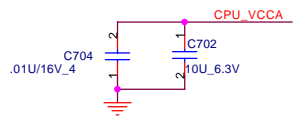
Sheet 3 of 41

Rev F

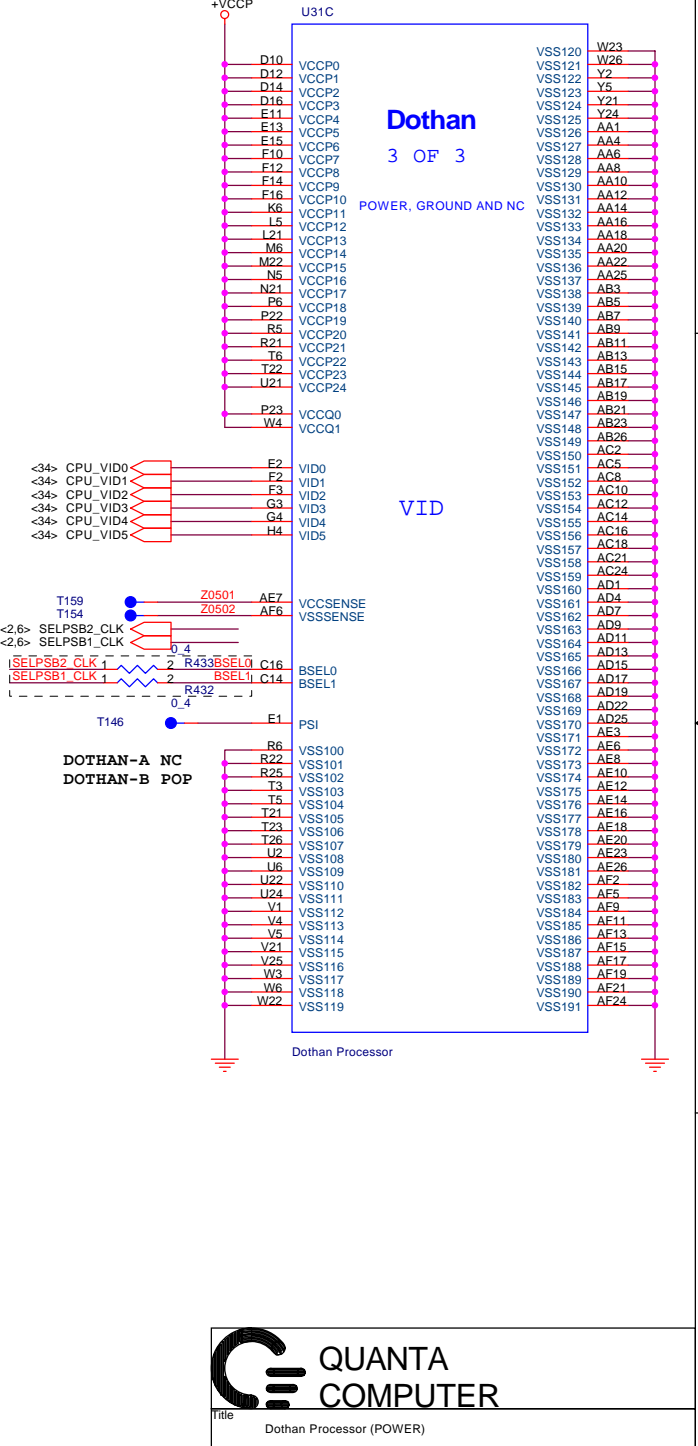
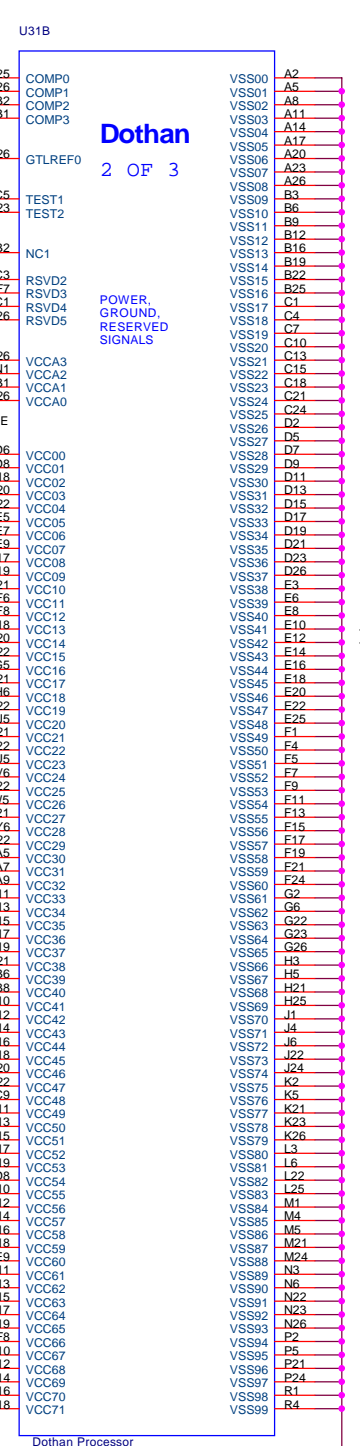
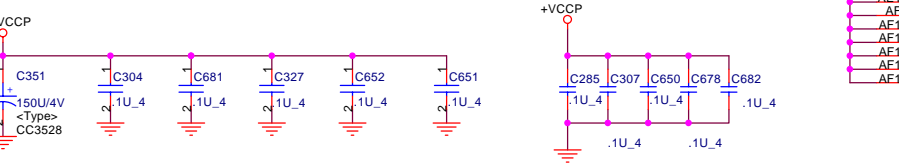


Place pull-down resistors within 0.5" of COMP pins

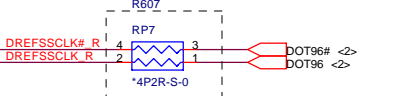
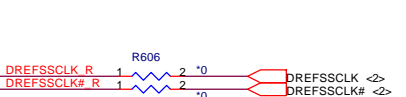
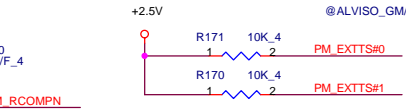
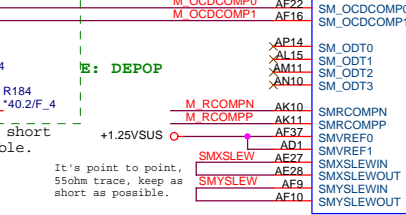
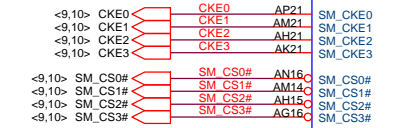
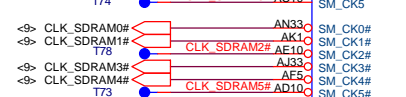
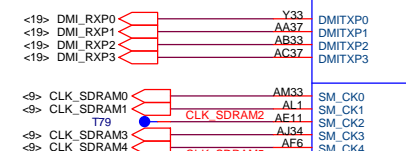
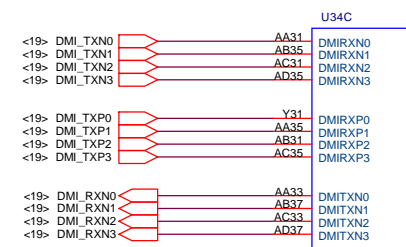
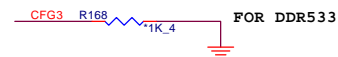
Place voltage divider within 0.5" of GTLREF pin



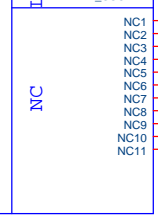
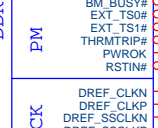
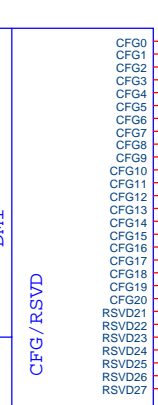
Total caps = 2633 uF
ESR = 15m ohm/5 // 5m ohm/25 // 5m ohm/15



CFG[0:2]=100 FOR FSB 533
CFG[0:2]=101 FOR FSB 400

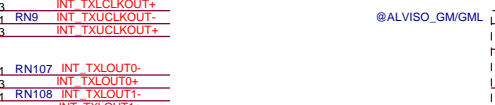
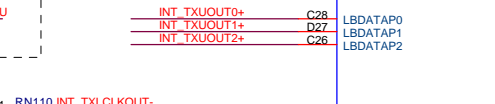
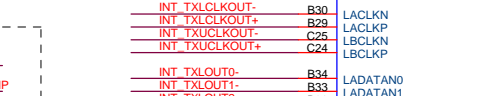
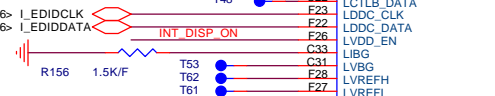
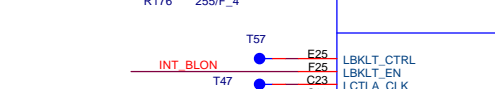
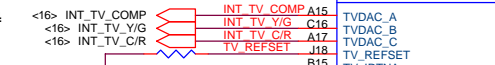
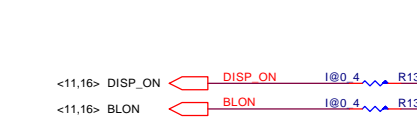
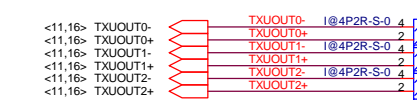
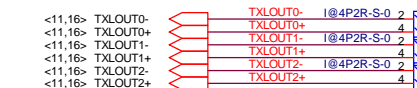
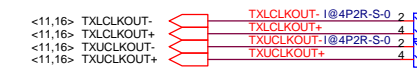
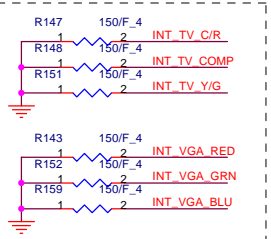


D: DEPOP RP7



CFG[17:3] have internal pullup resistors.
CFG[19:18] have internal pulldown resistors

B: POP ALWAYS



MISC

TV

VGA

SDVO

PCI-EXPRESS GRAPHICS

SDVO

SDVO

SDVO

SDVO

SDVO



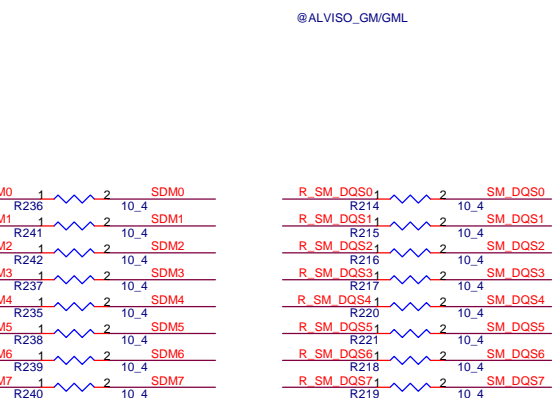
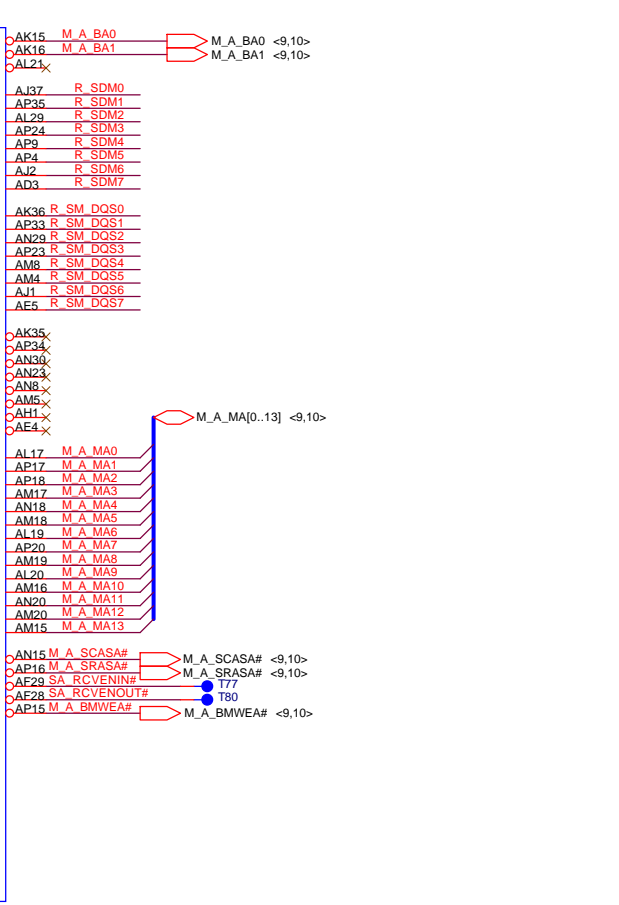
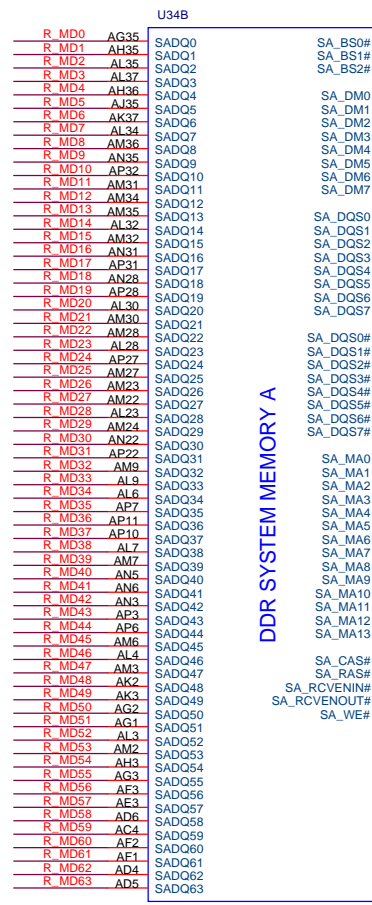
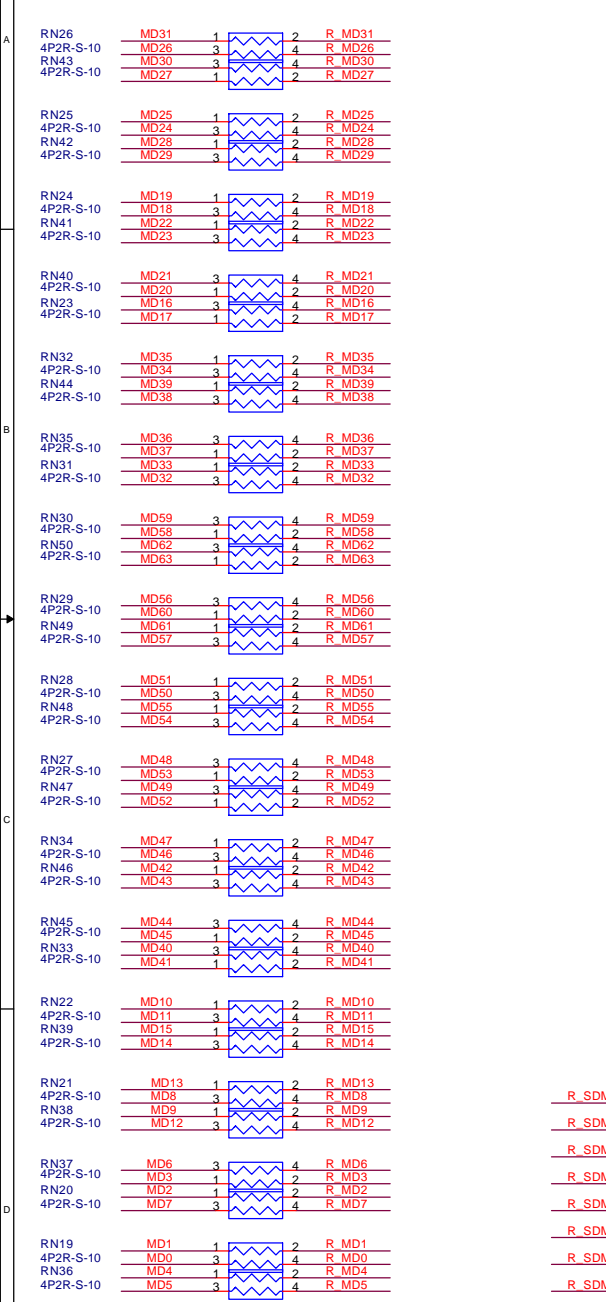
Alviso (VGA, DMI)

Document Number ZL2

Date: Tuesday, December 21, 2004 Sheet 6 of 41

B: NO STUFF WHEN NO DOCKING

MD[0..63] <9,10>
SM_DQS[0..7] <9,10>
SDM[0..7] <9,10>



QUANTA
COMPUTER

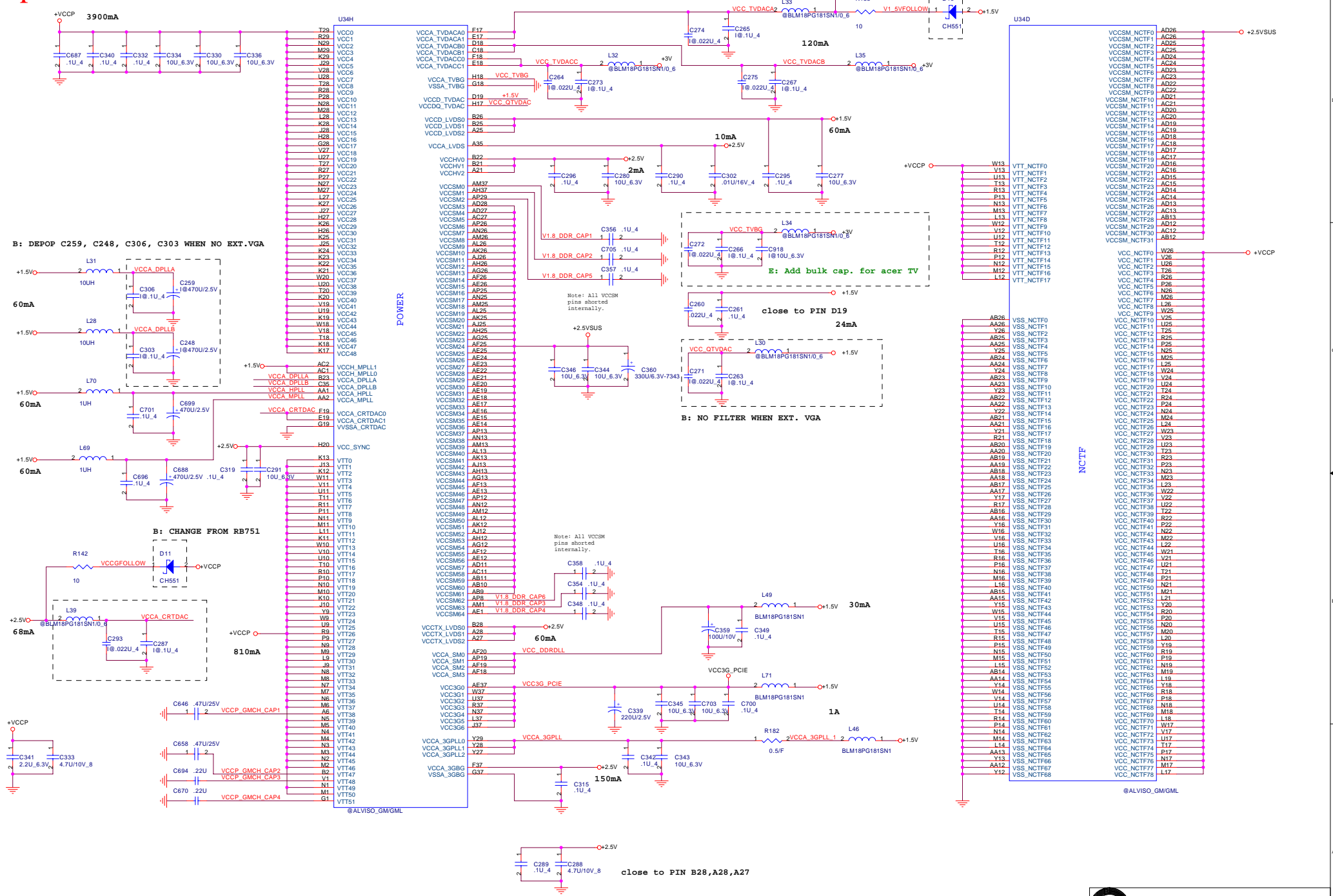
Title: Alviso (DDR)

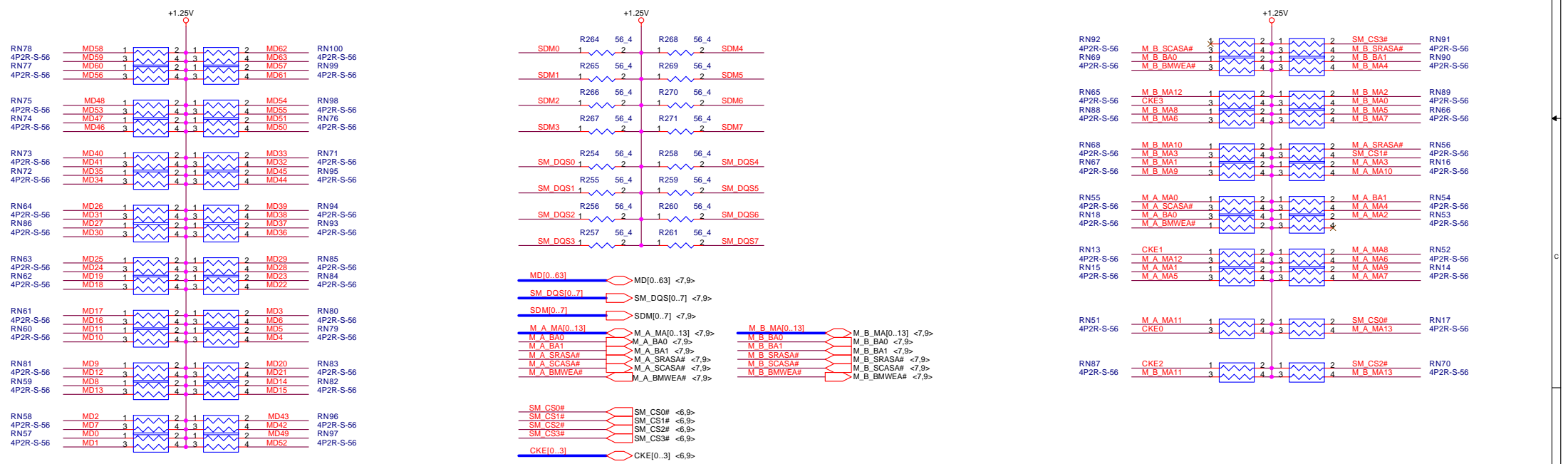
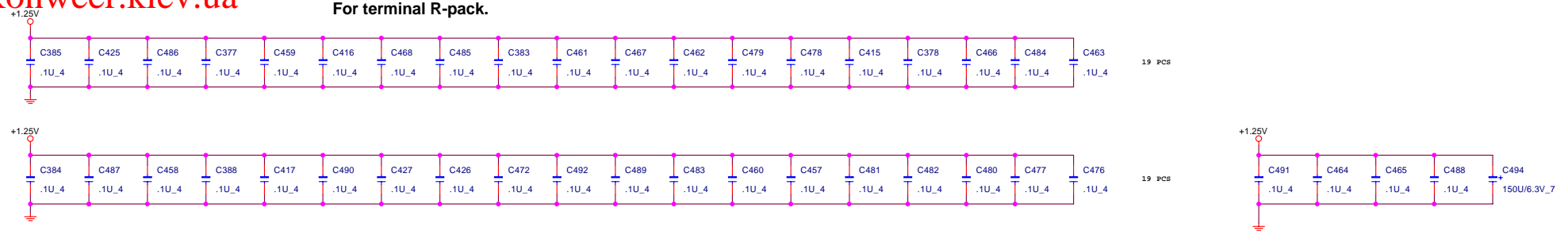
Size: CustomZL2

Document Number: 7 of 41

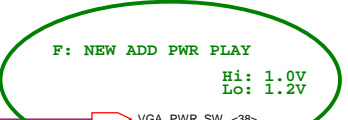
Date: Tuesday, December 21, 2004

Rev: F

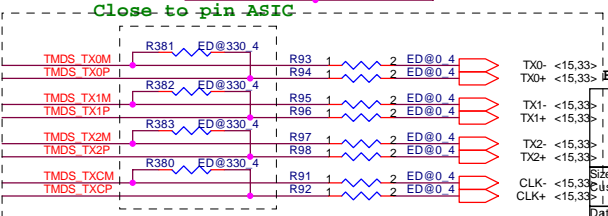
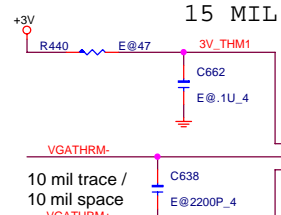
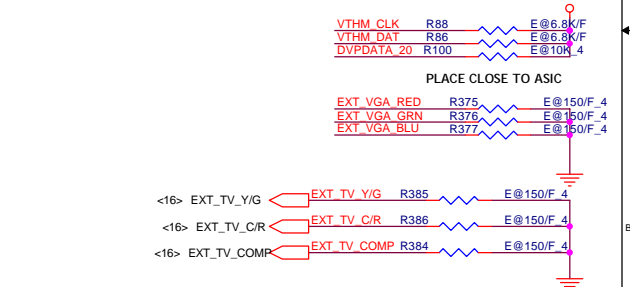
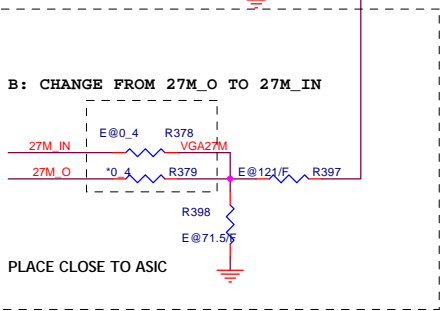
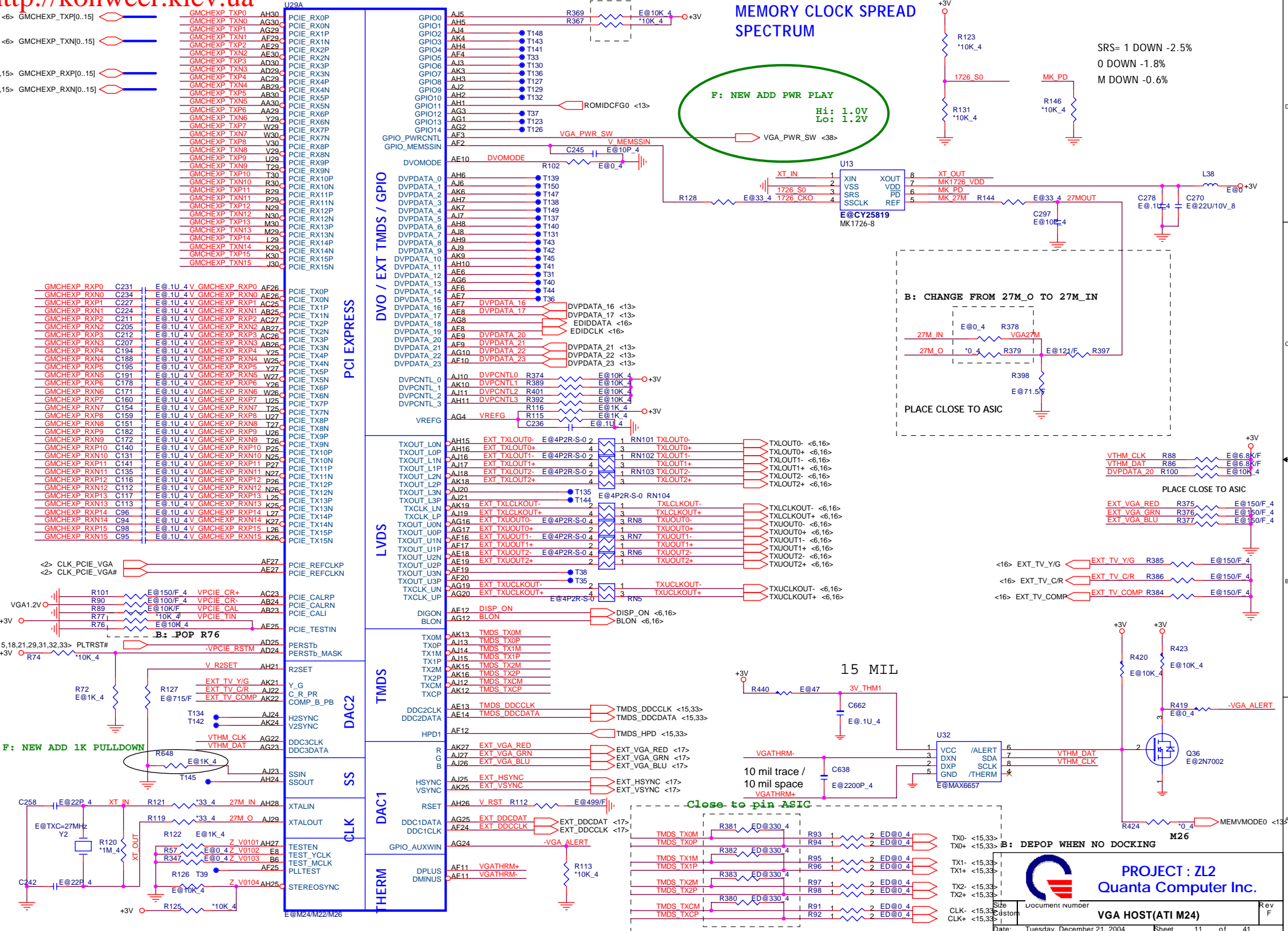




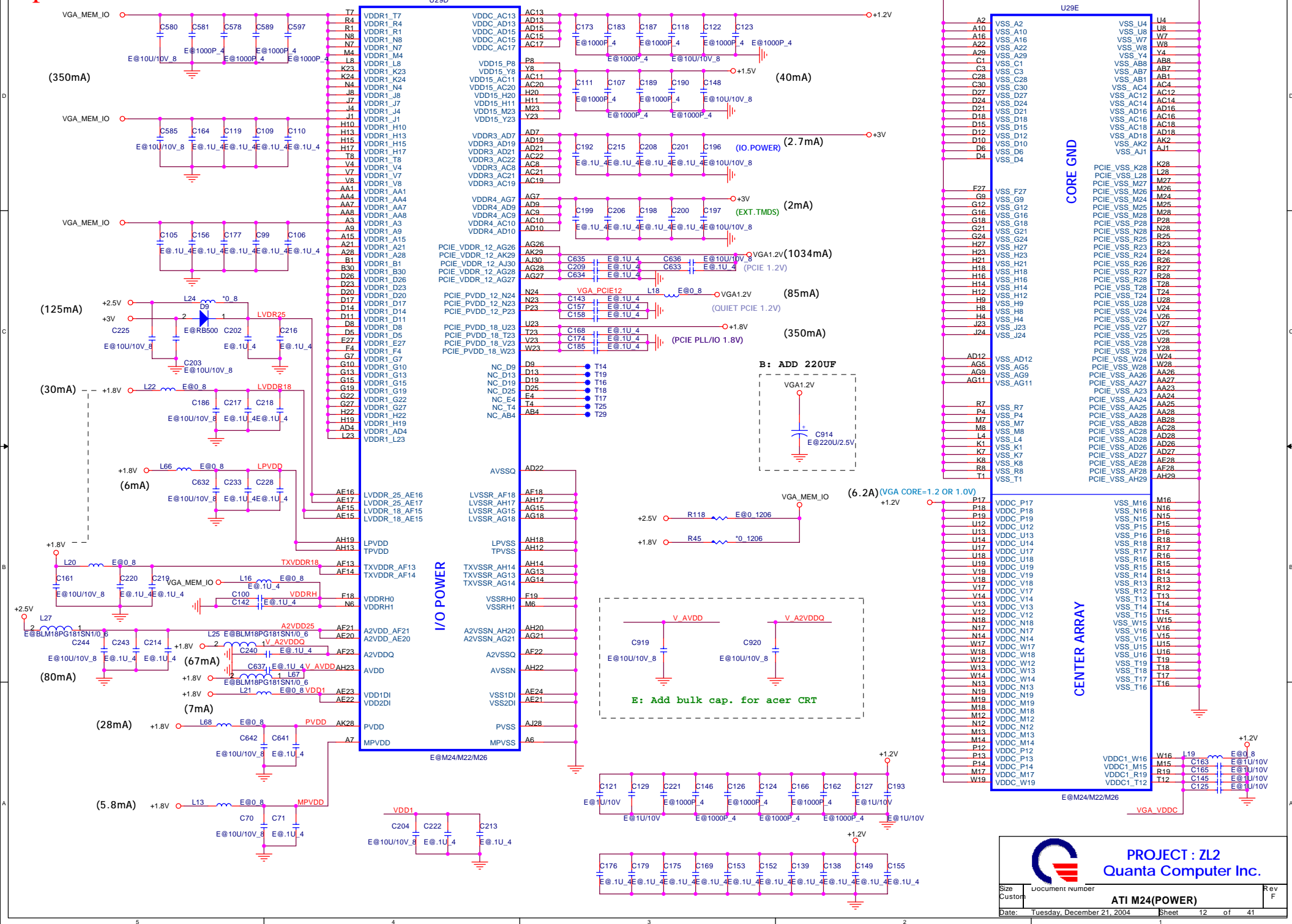
MEMORY CLOCK SPREAD SPECTRUM

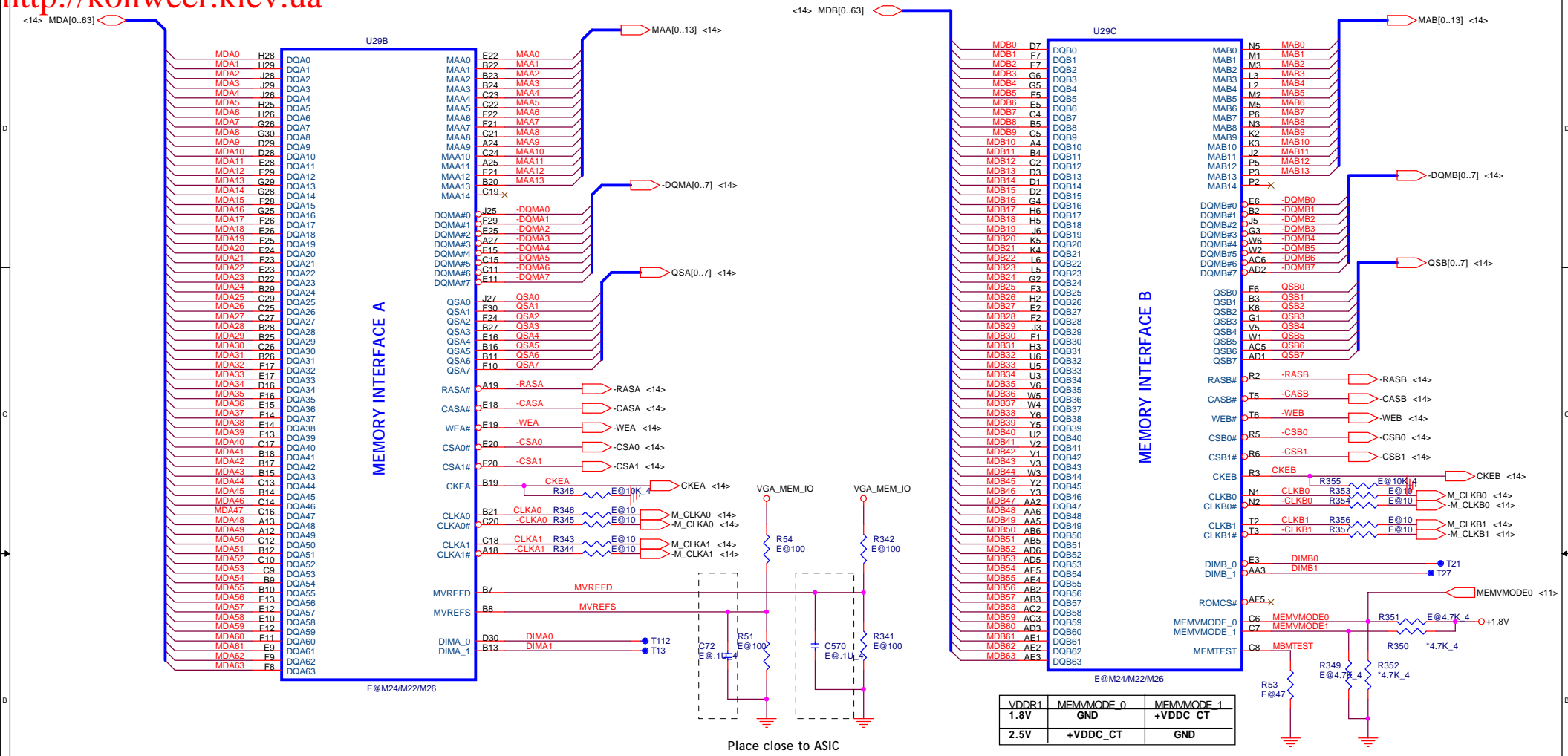


SRS = 1 DOWN -2.5%
 0 DOWN -1.8%
 M DOWN -0.6%



PROJECT : ZL2
Quanta Computer Inc.

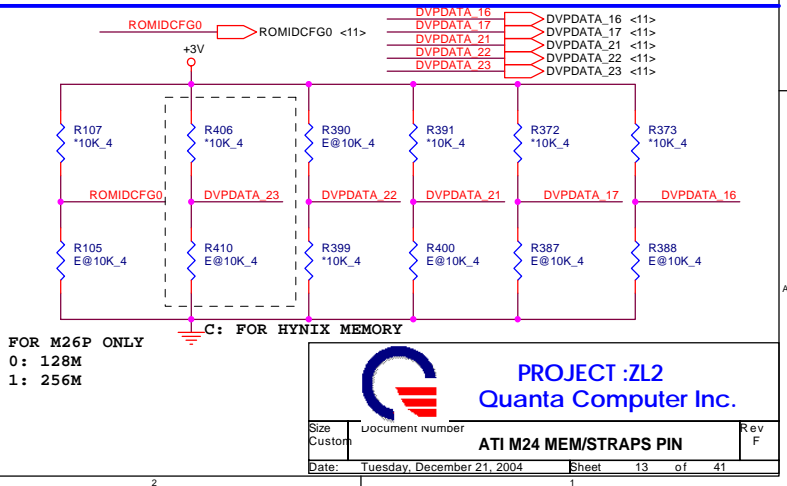




GPIO_0	PCI-Express Current Calibration Bandgap Backup 0: use reference voltage from Bandgap 1: use reference voltage from resistor divider
GPIO_1	PCI-Express PLL Calibration force enable 0: Disable PLL force calibration 1: Enable PLL force calibration
GPIO_(3,2)	00: PCI Express 1.0 mode 01: RESERVED 10: PCI Express 1.0 mode 11: RESERVED
GPIO_4	Turn off PCI-Express impedance / strength calibration 0: enable 1: disable
GPIO_5	Bypass PCI-Express PLL

STRAPS PIN

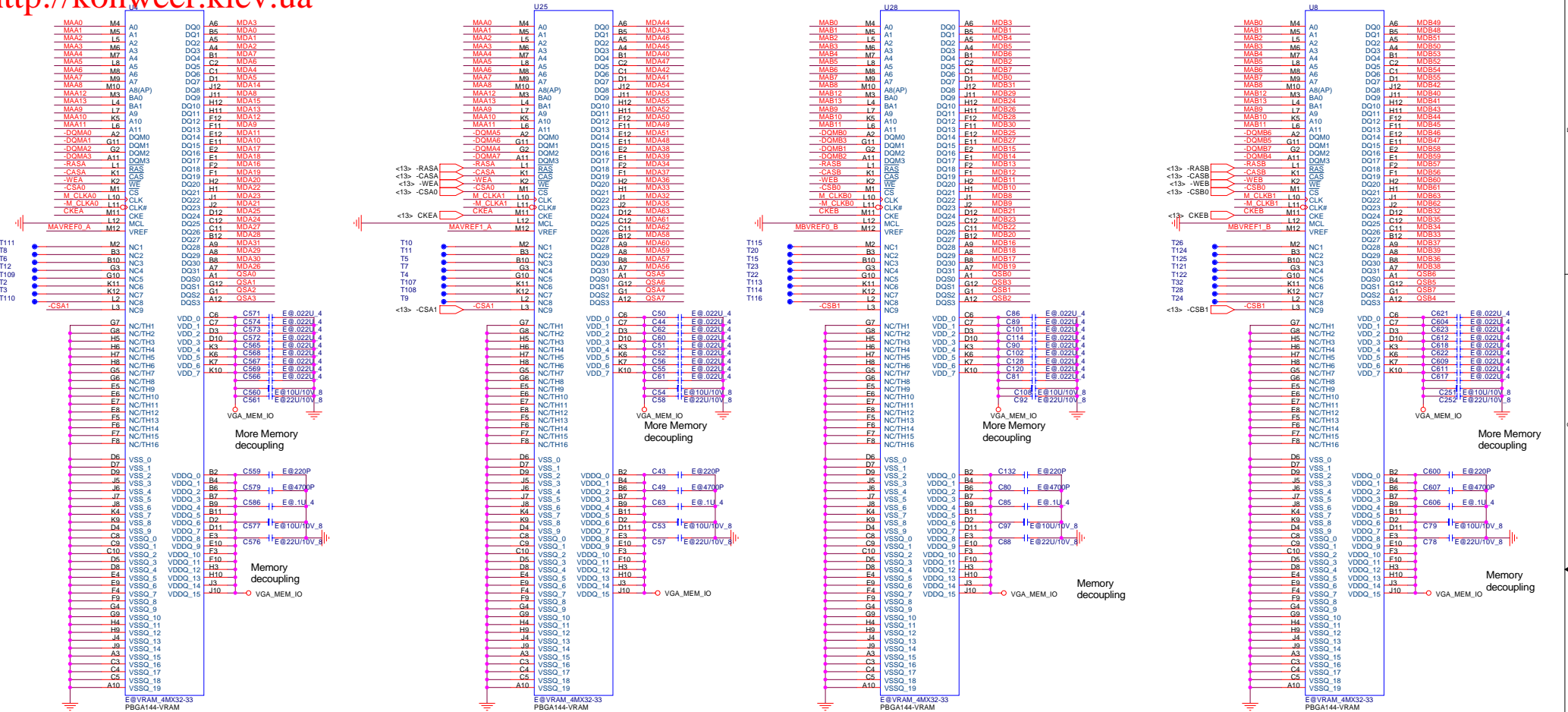
GPIO_6	PCI-Express transmitter current compensation 0: Normal 1: Inject extra current for output buffer switching
GPIO_8	Strap to set the debug muxes to bring out DEBUG signals even if registers are inaccessible
GPIO(9,13:11) INT P/D	ROMIDCFG 0x0x: No ROM, CHG_ID=0 0x1x: No Rom, CHG_ID=1 1000: Parallel ROM, Chip ID'S from ROM 1000: Parallel ROM, Chip ID'S from ROM
DVPDATA_21-23 MEM TYPE	DVPDATA_21: 0=4Mx32 1=8Mx32 DVPDATA_22: 0=128M 1=64M DVPDATA_23: 0=Hynix 1=Samsung



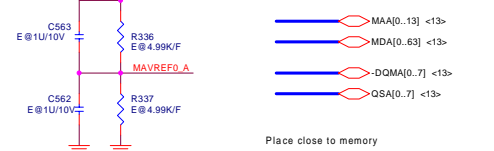
PROJECT :ZL2
Quanta Computer Inc.

Size Custom Document number
ATI M24 MEM/STRAPS PIN Rev F

Date: Tuesday, December 21, 2004 Sheet 13 of 41



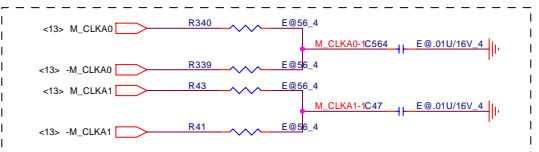
VGA DDR MEMORY A
 @64/128Mbytes DDR 128Mbit 1MX32X4 uBGA



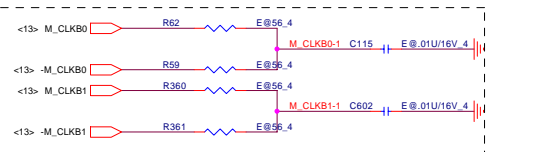
8Mx32 AKD56WCT503 K4D5323QP-GC33 1.8V
4Mx32 AKD35W-T506 K4D263238E-GC33 2.5V



VGA DDR MEMORY B
 @64/128Mbytes DDR 128Mbit 1MX32X4 uBGA

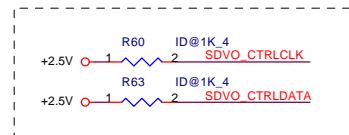


At least a 2.5:1 spacing between the pair
 These resistors and caps must be placed to minimize any stubs. These must also be placed after the memory



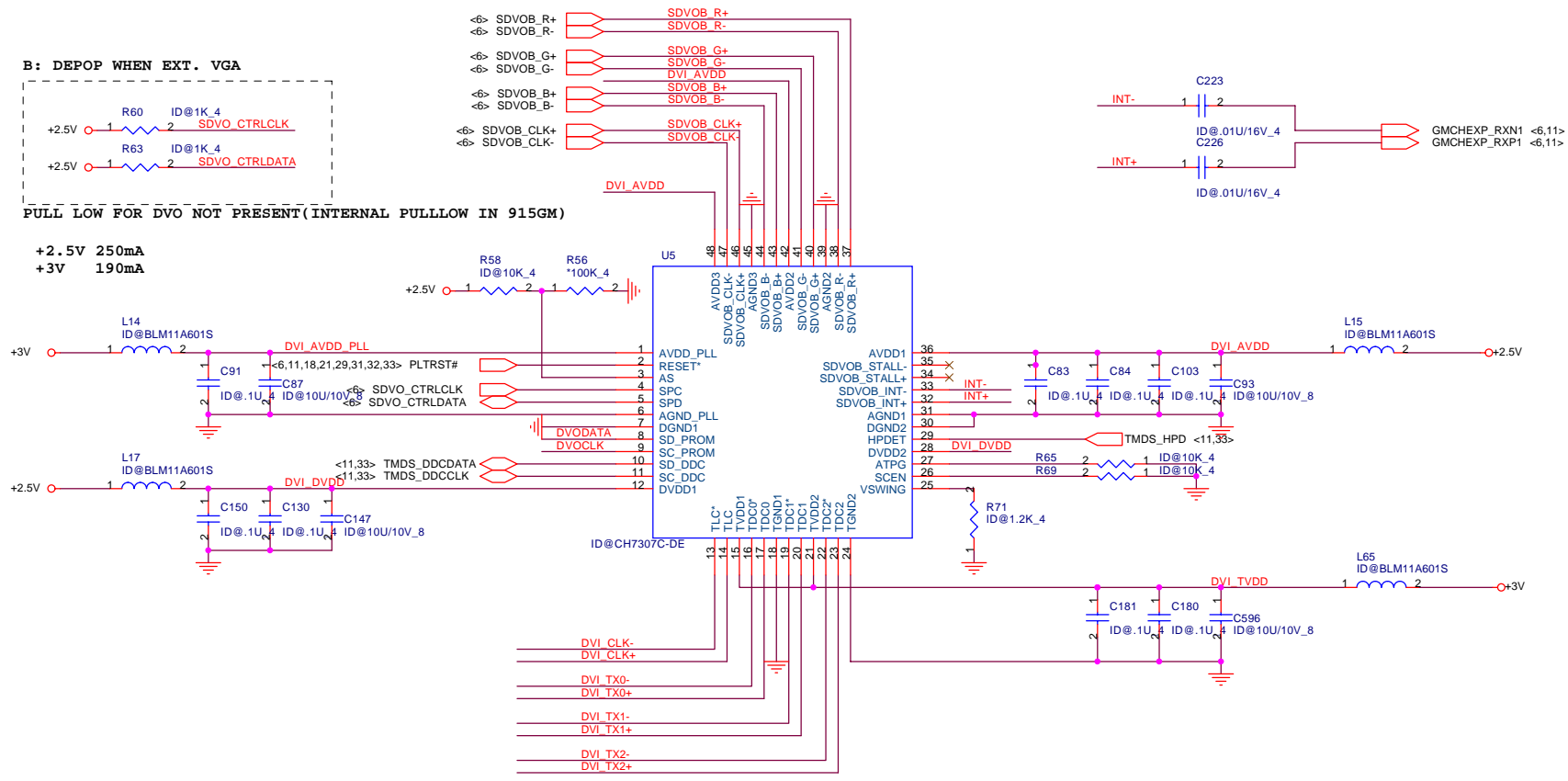
At least a 2.5:1 spacing between the pair
 These resistors and caps must be placed to minimize any stubs. These must also be placed after the memory

B: DEPOP WHEN EXT. VGA

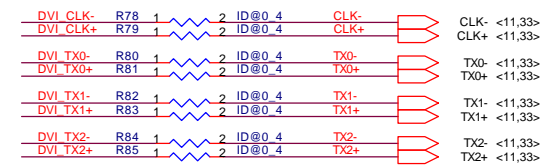
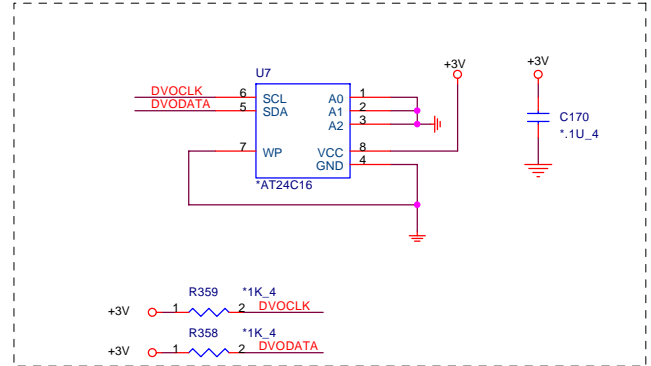


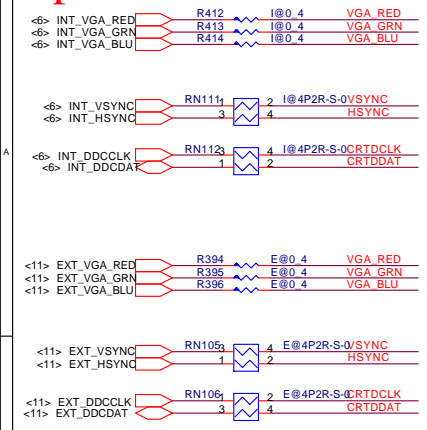
PULL LOW FOR DVO NOT PRESENT (INTERNAL PULLLOW IN 915GM)

+2.5V 250mA
+3V 190mA

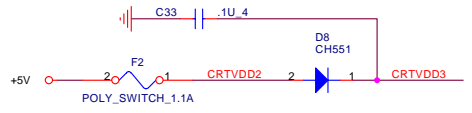
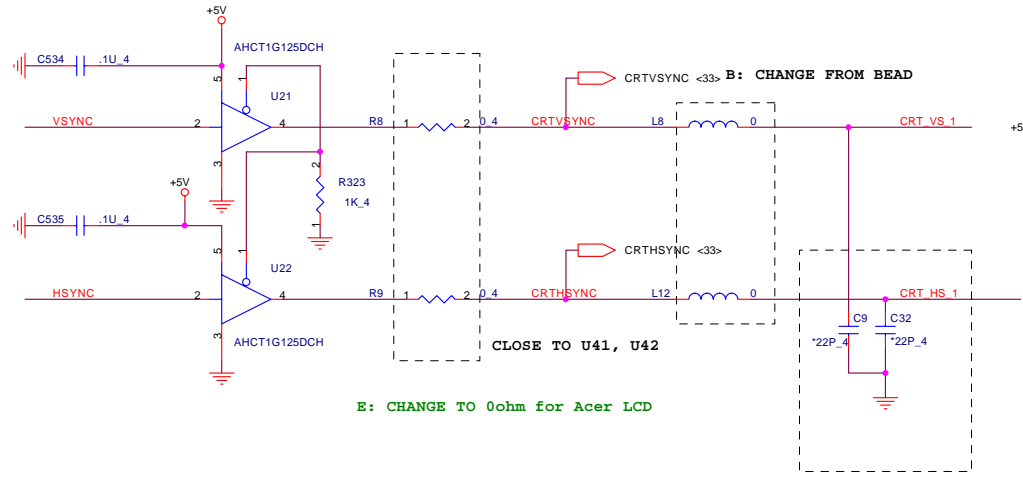
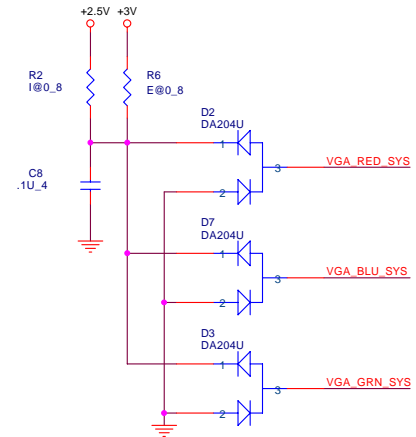
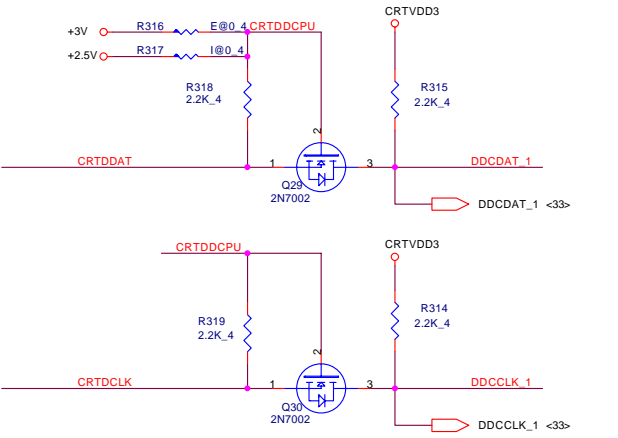
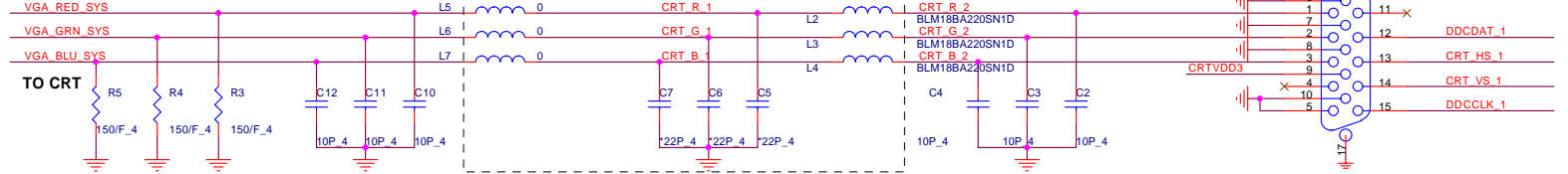
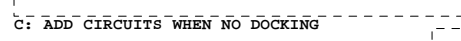
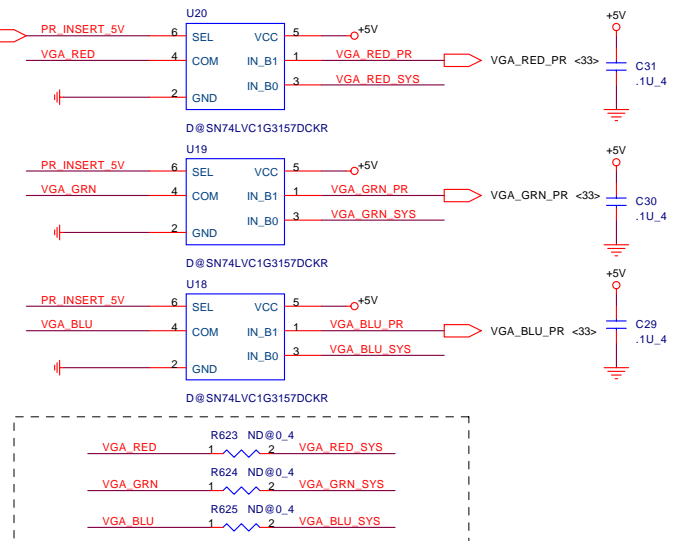


B: ALWAYS NOT ON, TEST ONLY

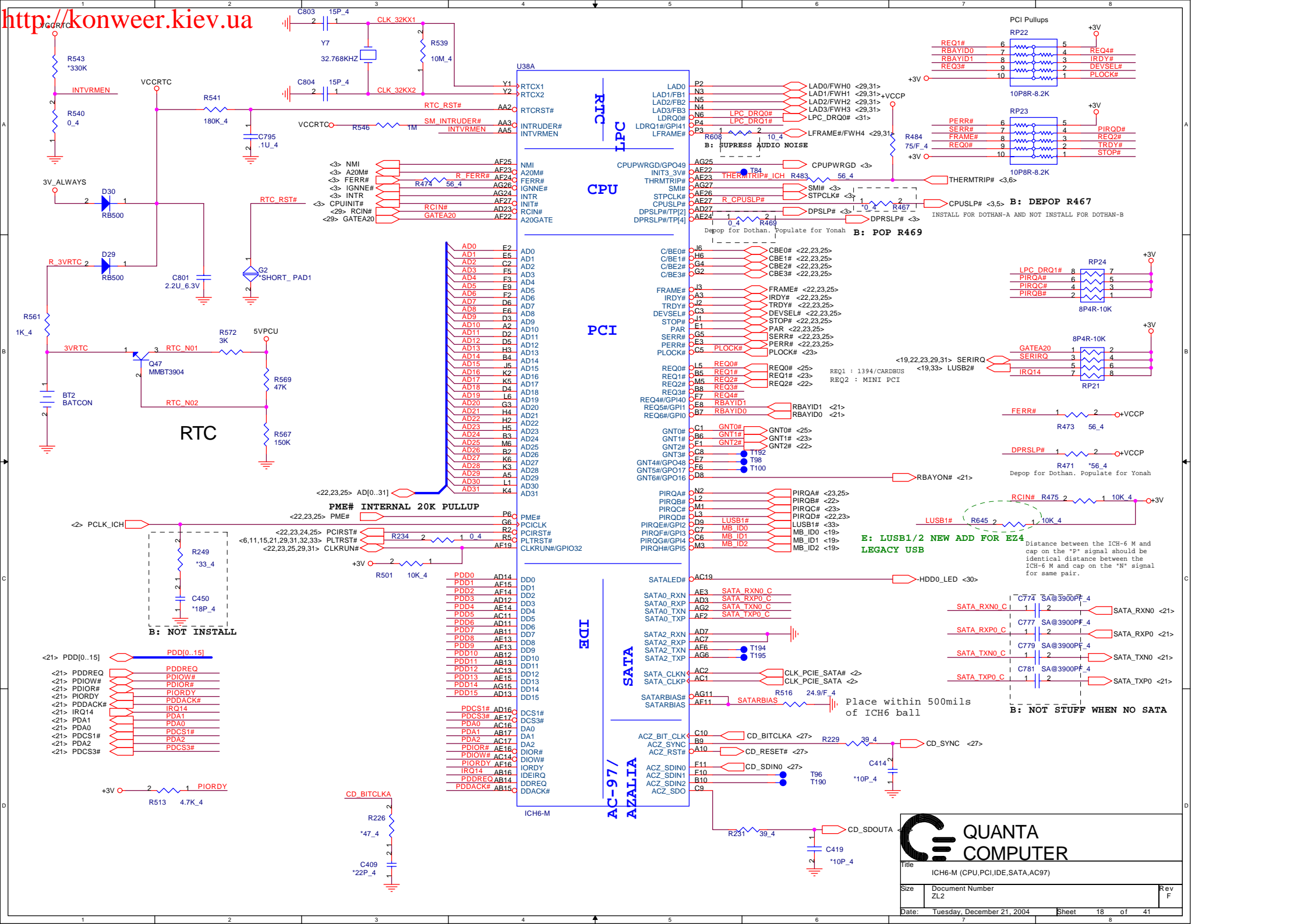




SEL	FUNCTION
LOW	IN_B0
HIGH	IN_B1



E: CHANGE TO 0ohm for Acer LCD



RTC

CPU

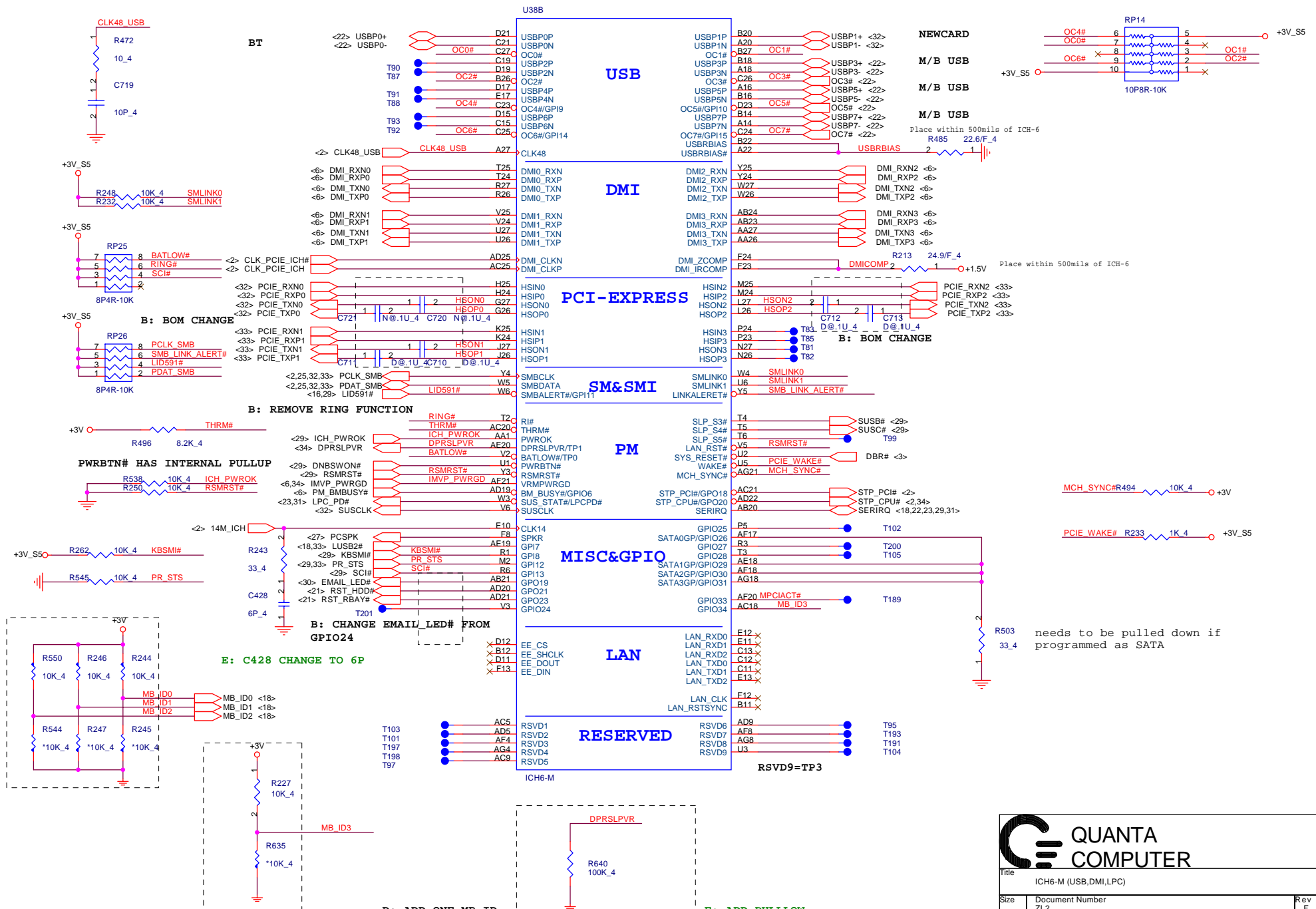
PCI

IDE

SATA

AC-97/
AZALIA





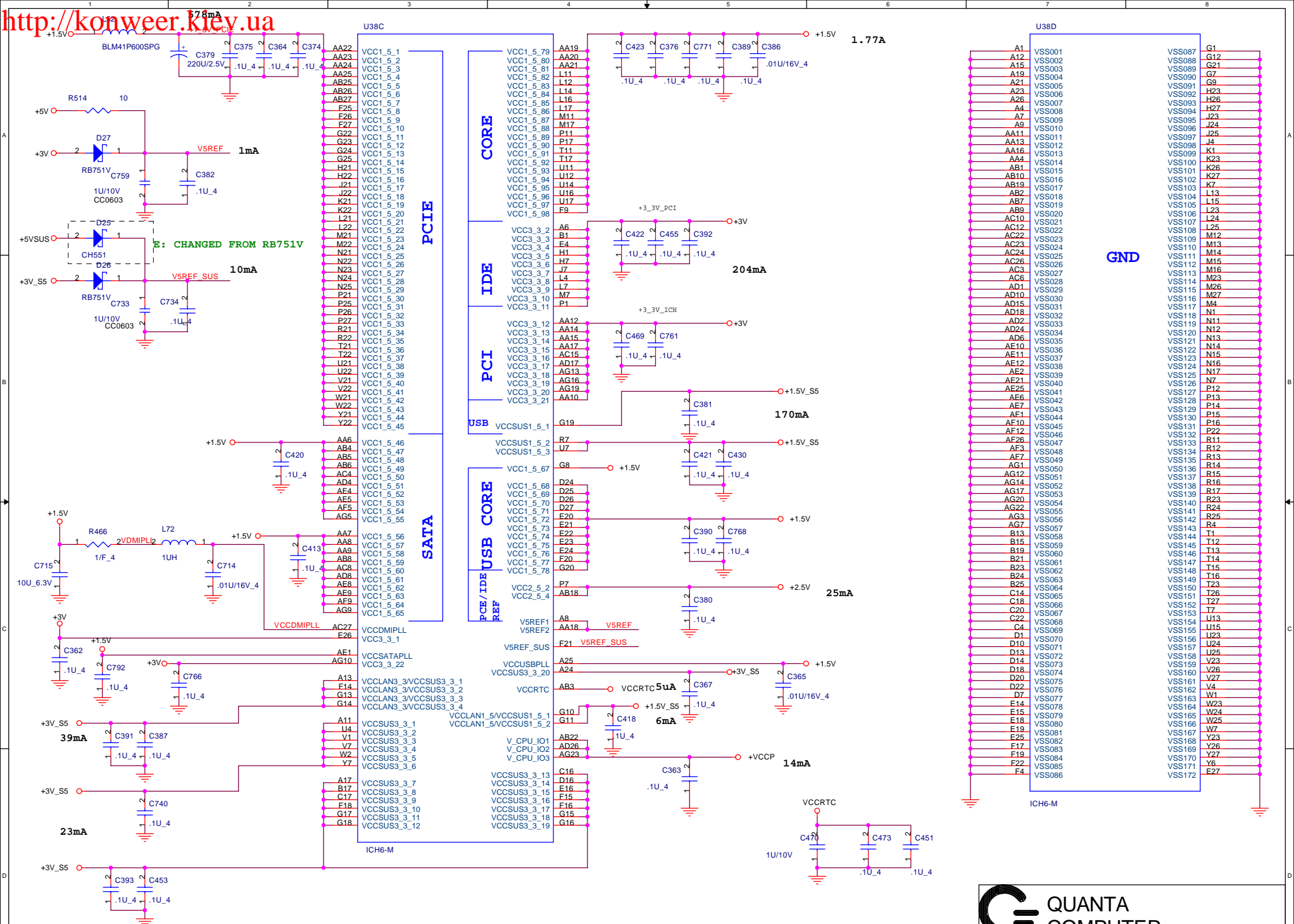
QUANTA COMPUTER

Title: ICH6-M (USB,DMI,LPC)

Size: Document Number ZLZ

Date: Wednesday, December 22, 2004 Sheet 19 of 41

Rev F



QUANTA COMPUTER

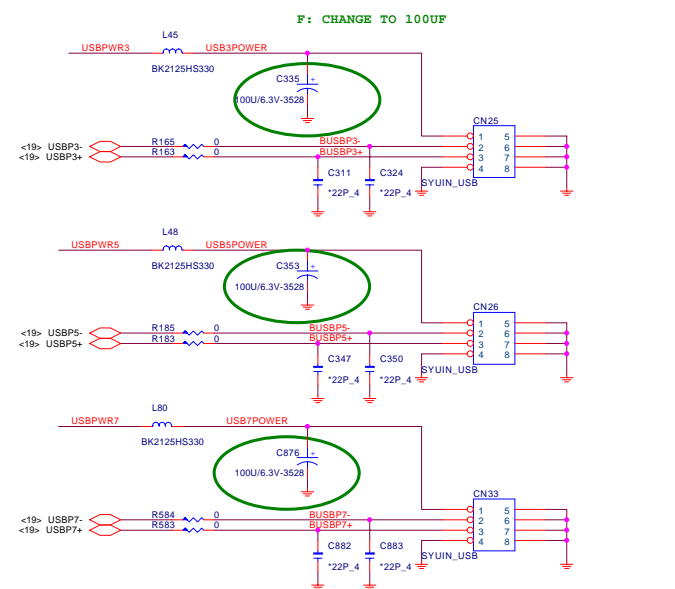
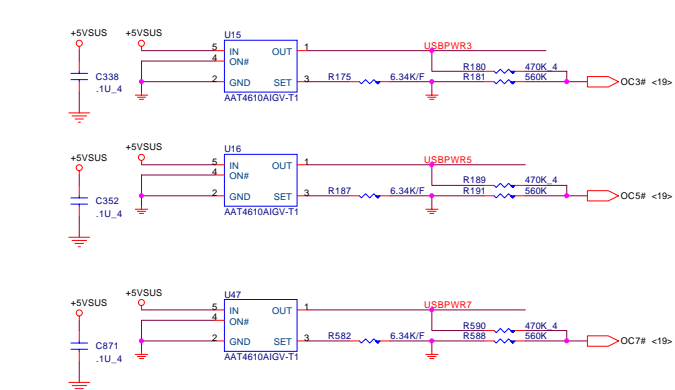
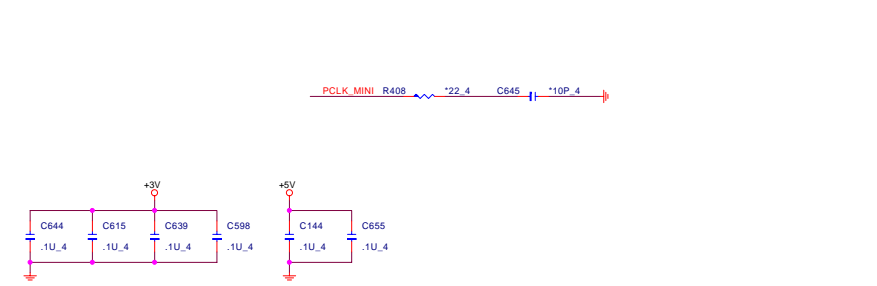
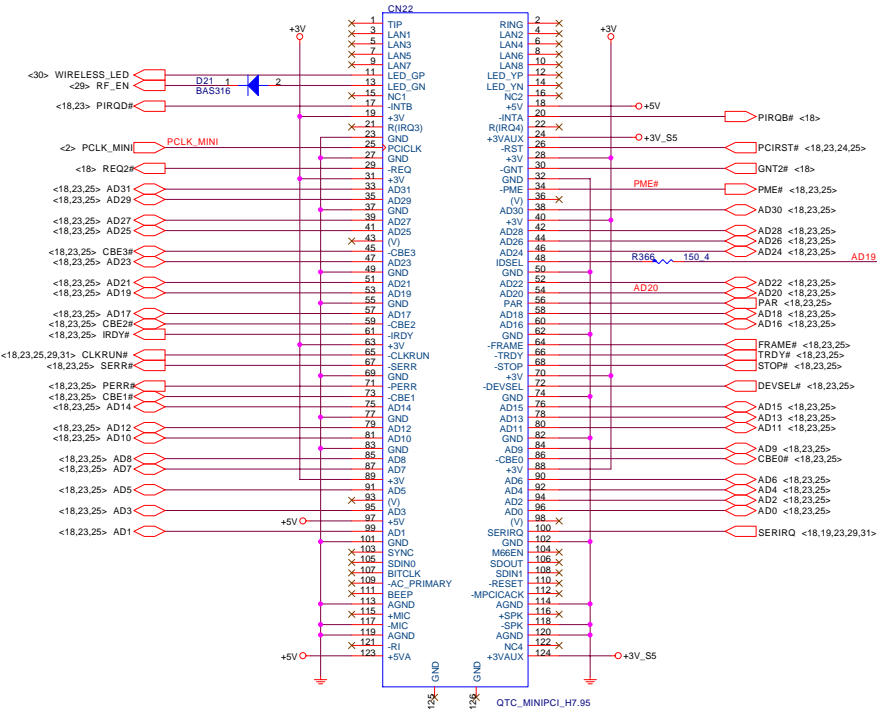
Title: ICH6-M (POWER&GND)

Size	Document Number	Rev
	ZLZ	F

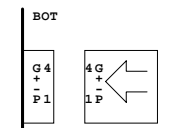
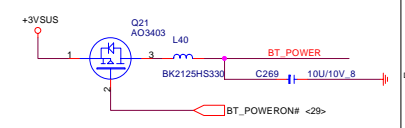
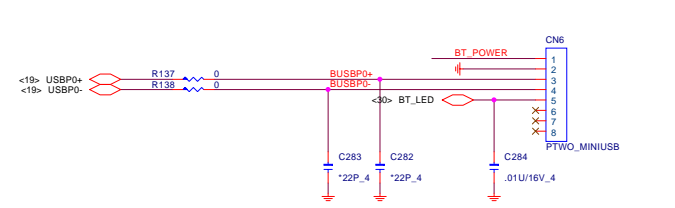
Date: Tuesday, December 21, 2004 Sheet 20 of 41

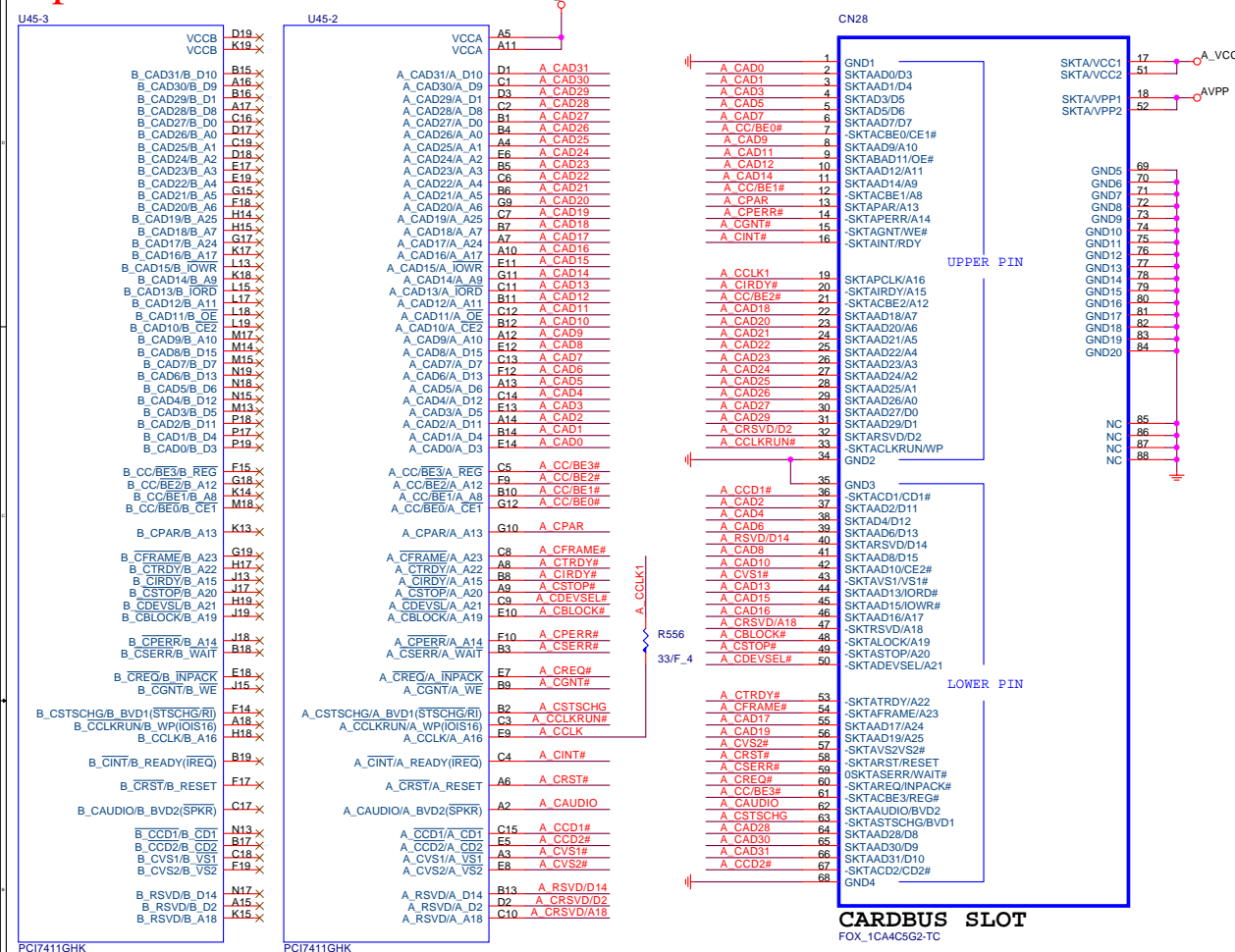
Interrupt Pin : INTB# , INTC#
Request Indicate : REQ1#
Grant Indicate : GNT1#

MINI-PCI

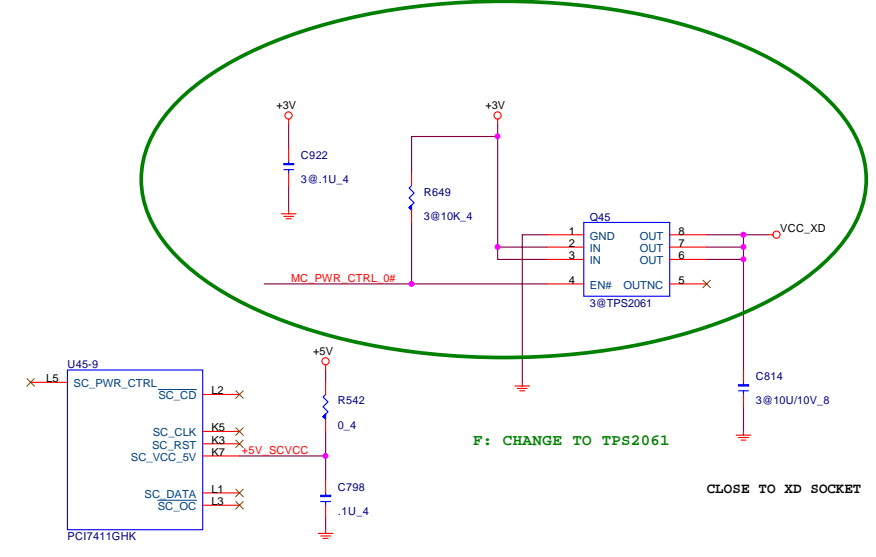
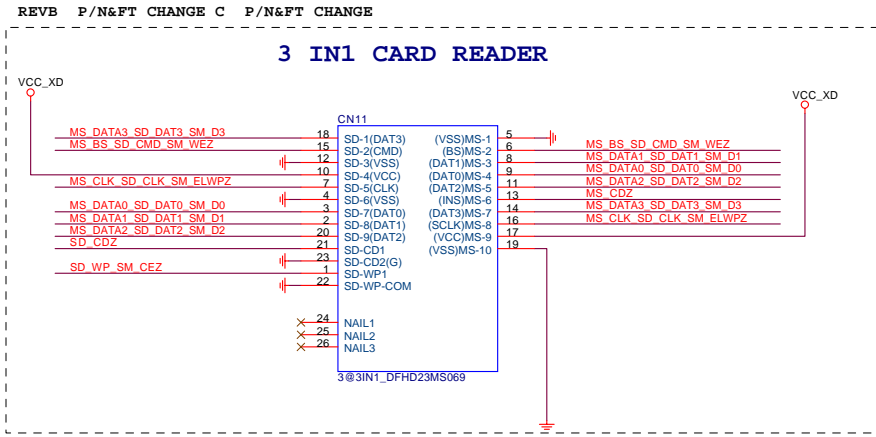
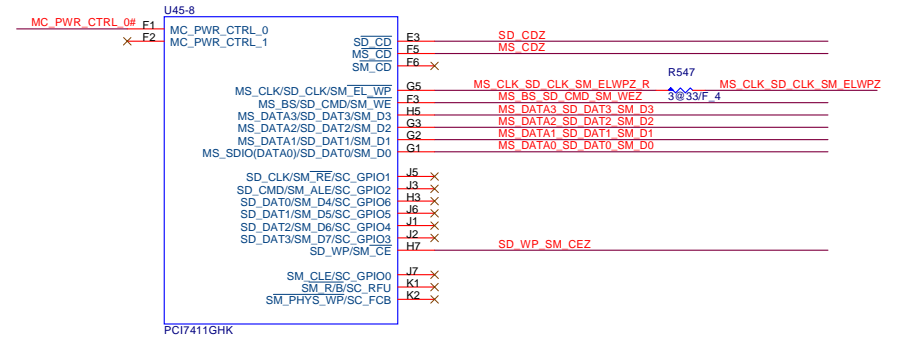
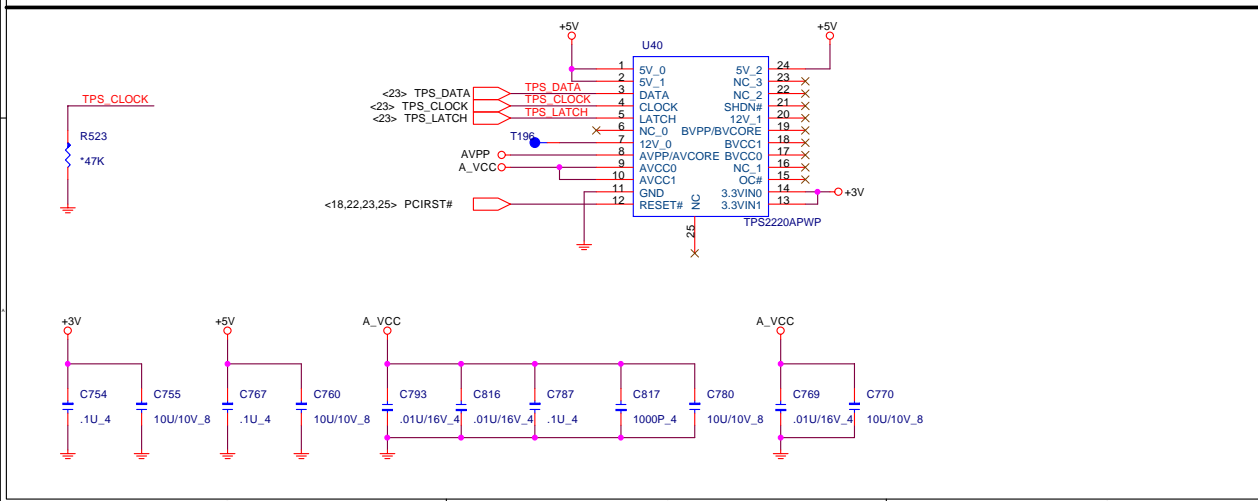


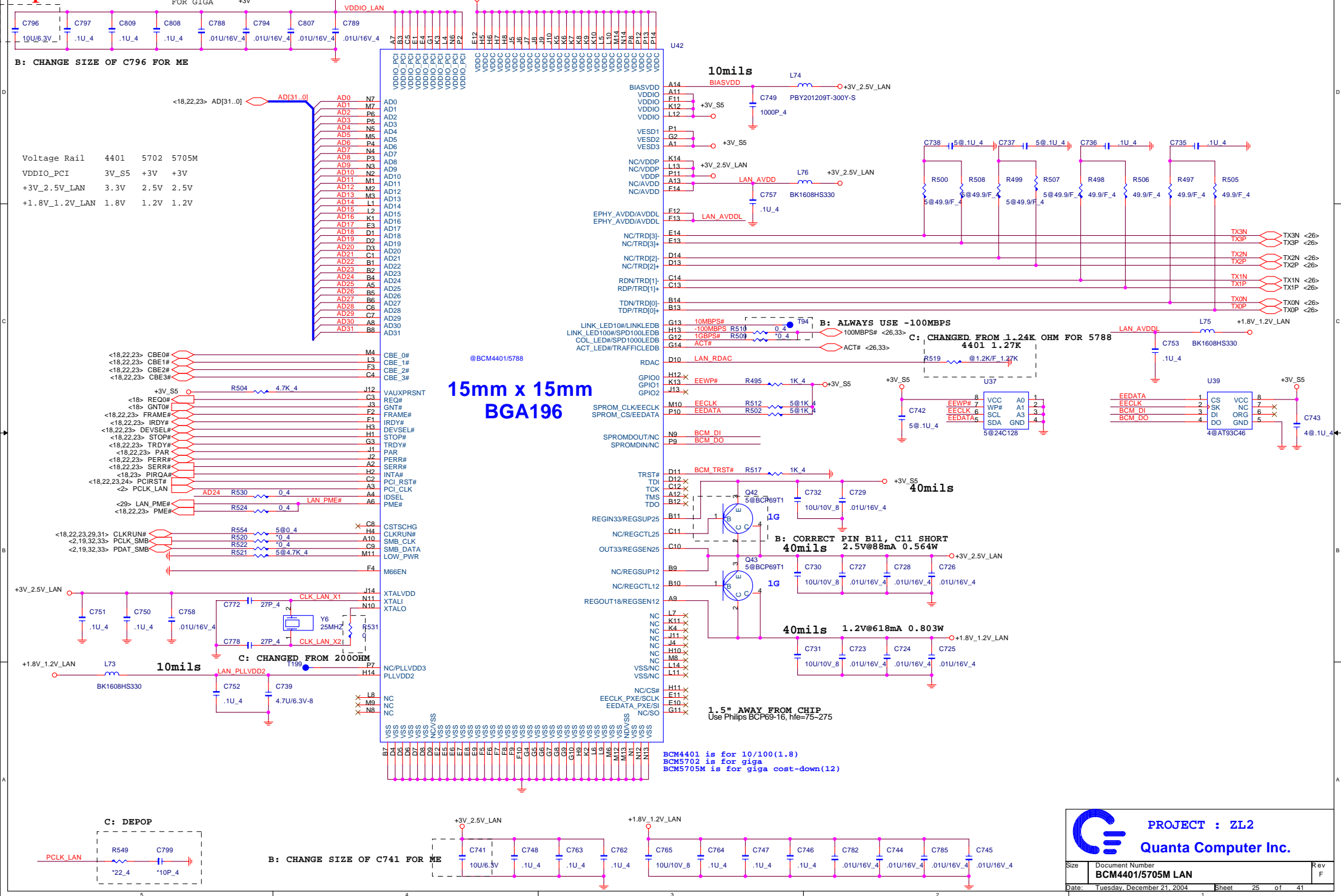
B: REMOVE CHOKE PADS





CARBUS SLOT
FOX_1C4C5G2-TC

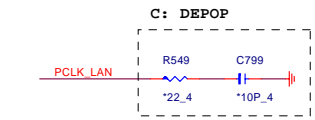




B: CHANGE SIZE OF C796 FOR ME

Voltage Rail	4401	5702	5705M
VDDIO_PCI	3V_S5	+3V	+3V
+3V_2.5V_LAN	3.3V	2.5V	2.5V
+1.8V_1.2V_LAN	1.8V	1.2V	1.2V

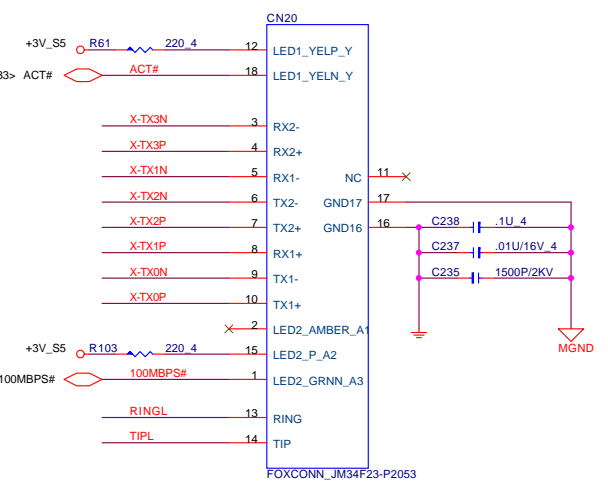
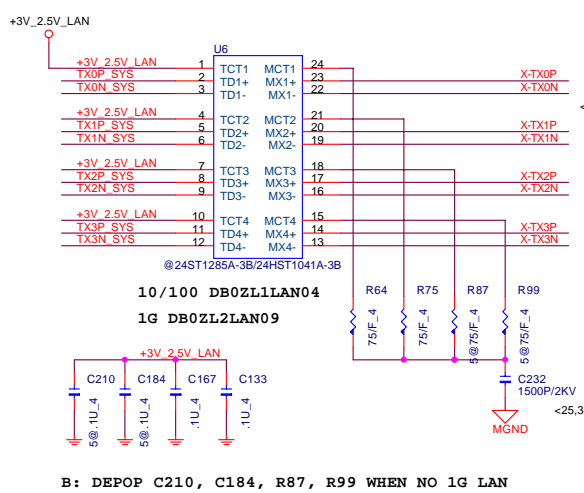
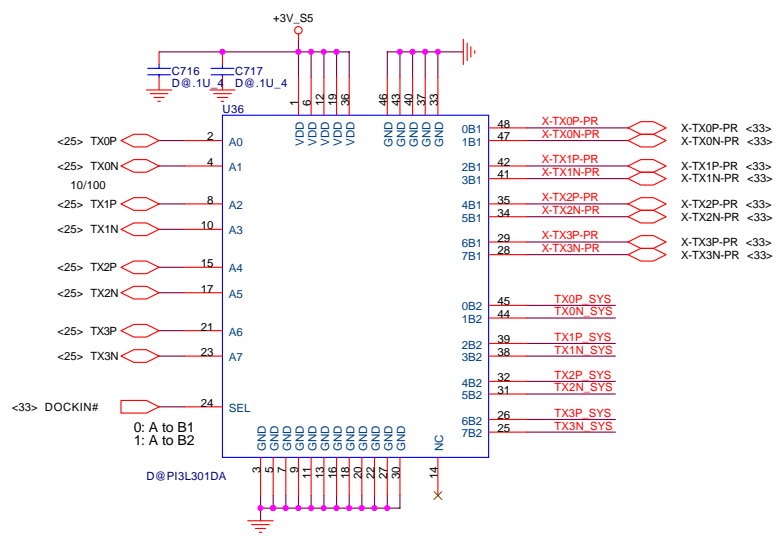
15mm x 15mm
BGA196



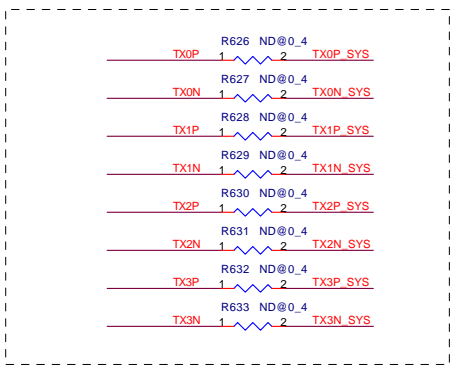
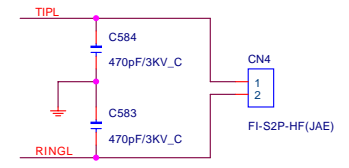
B: CHANGE SIZE OF C741 FOR ME

PROJECT : ZL2
Quanta Computer Inc.

Size	Document Number	Rev
	BCM4401/5705M LAN	F
Date:	Tuesday, December 21, 2004	Sheet 25 of 41



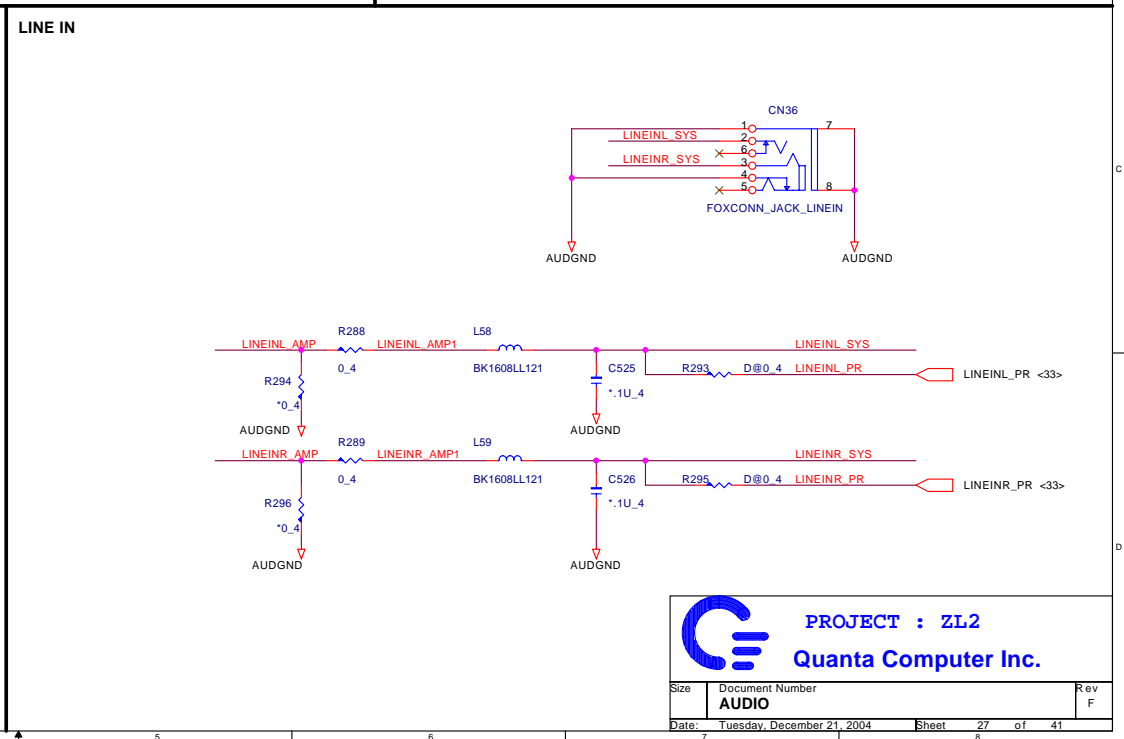
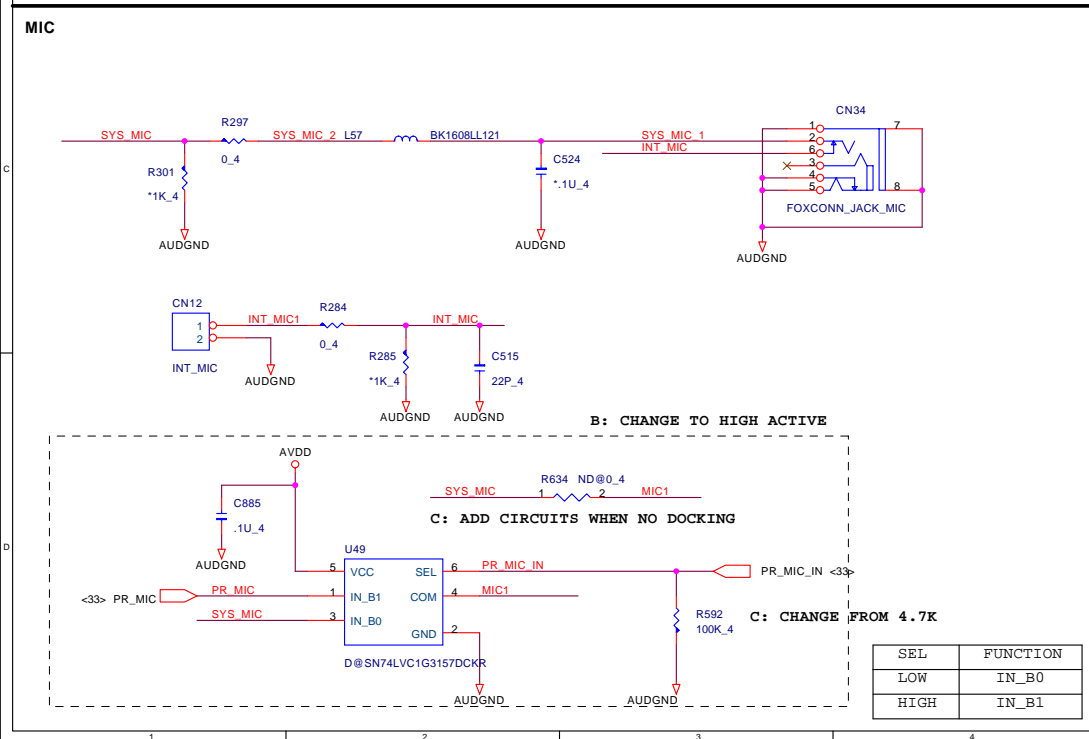
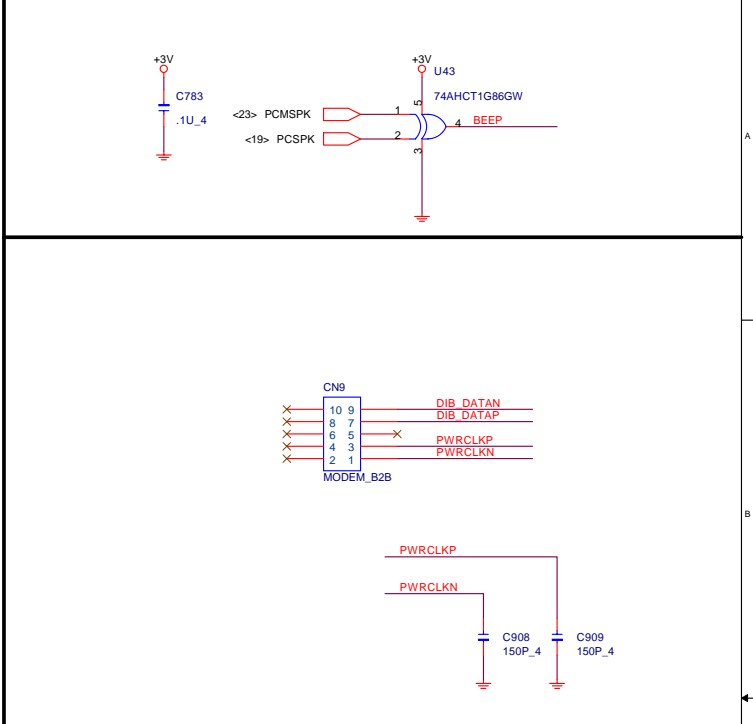
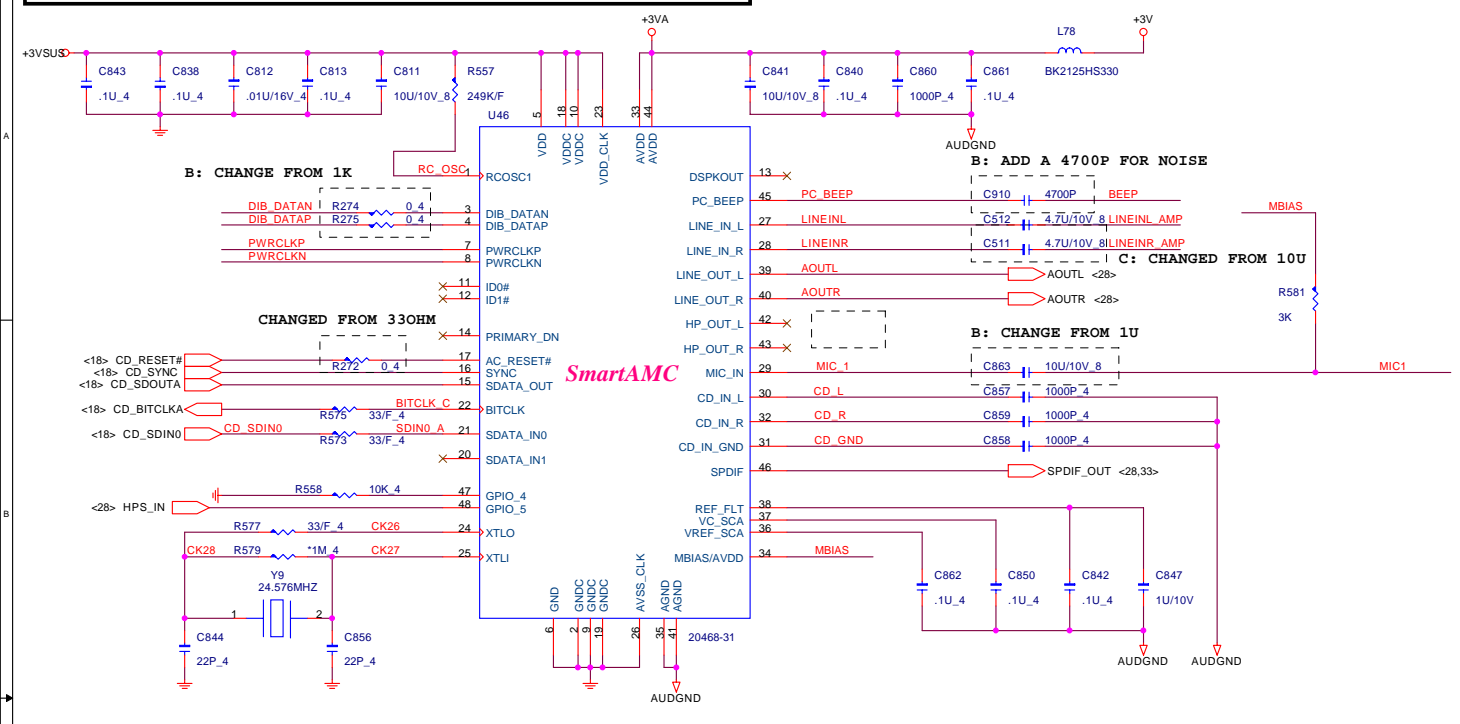
B: DEPOP C210, C184, R87, R99 WHEN NO 1G LAN



C: ADD CIRCUITS WHEN NO DOCKING

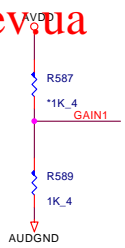


E: ADD FOR EMI REQUEST

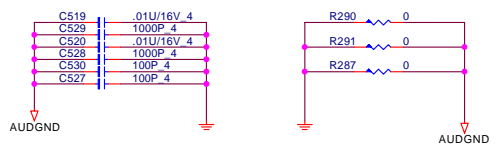
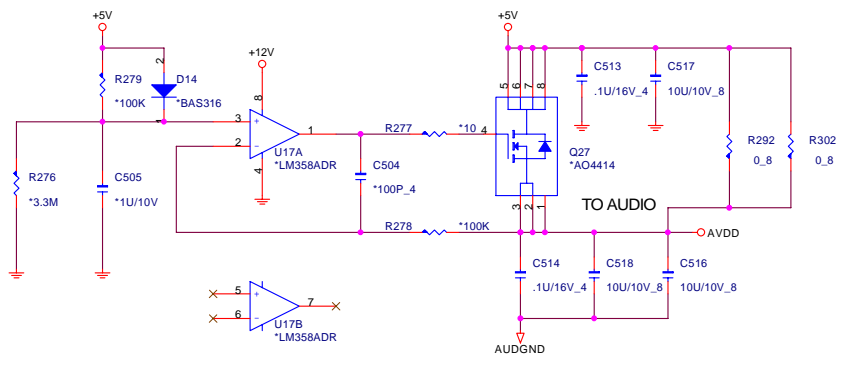


SEL	FUNCTION
LOW	IN_B0
HIGH	IN_B1

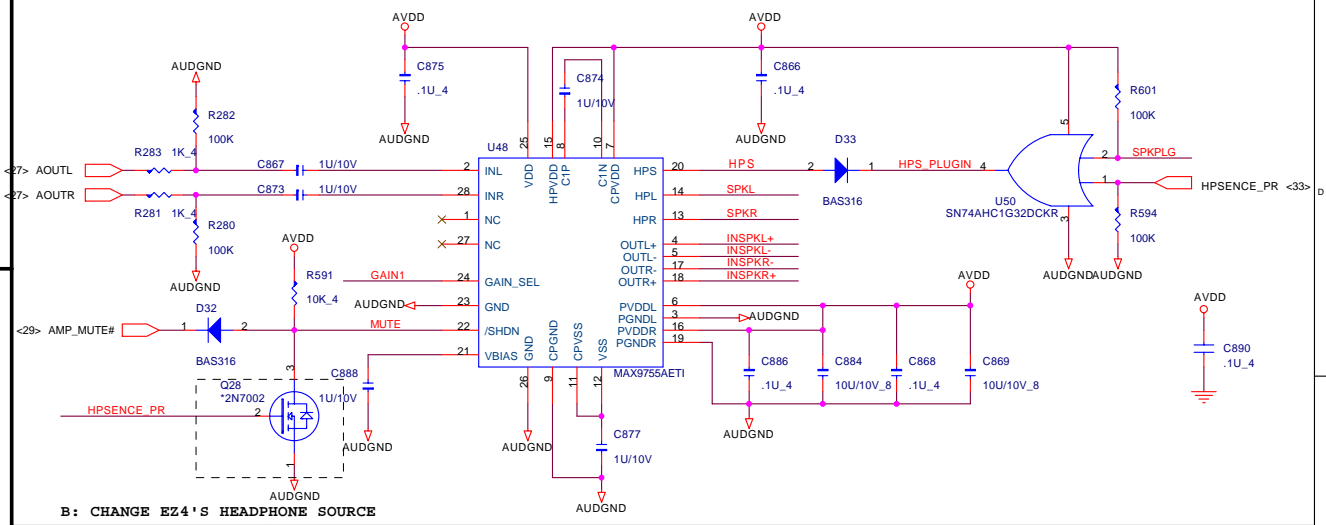
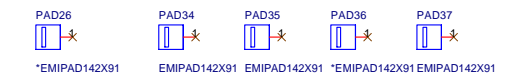
GAIN1	SPKR MODE	HP MODE
0	10.5	3
1	9	0



AMP POWER

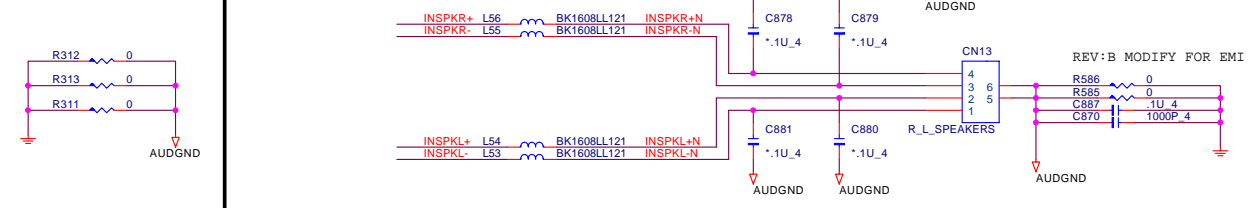


- F: RESTORE PAD26 FOR EMI REQUEST
- E: REMOVE PAD26, AND PAD35 CHANGE LOCATION
- B: ADD SPRING FOR MODEM CABLE



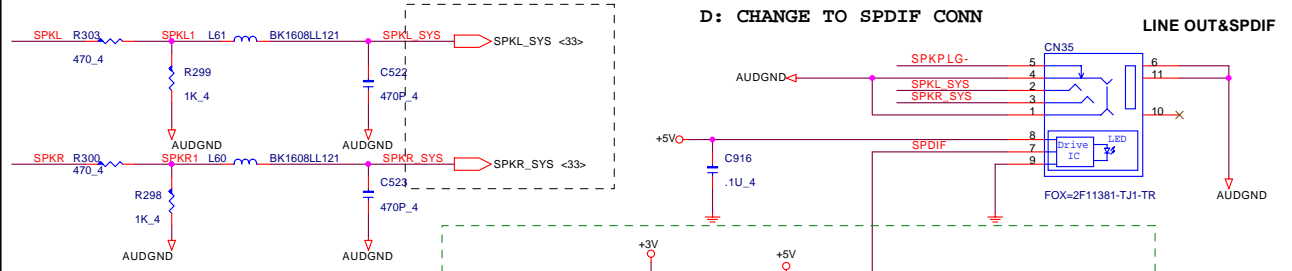
B: CHANGE EZ4'S HEADPHONE SOURCE

SPEAKER CON.

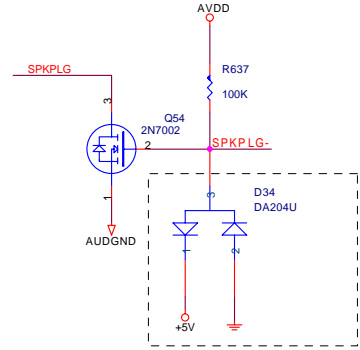


D: CHANGE TO SPDIF CONN

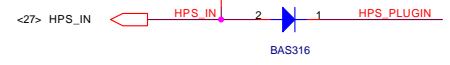
LINE OUT&SPDIF



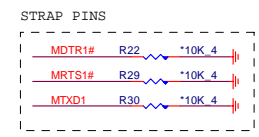
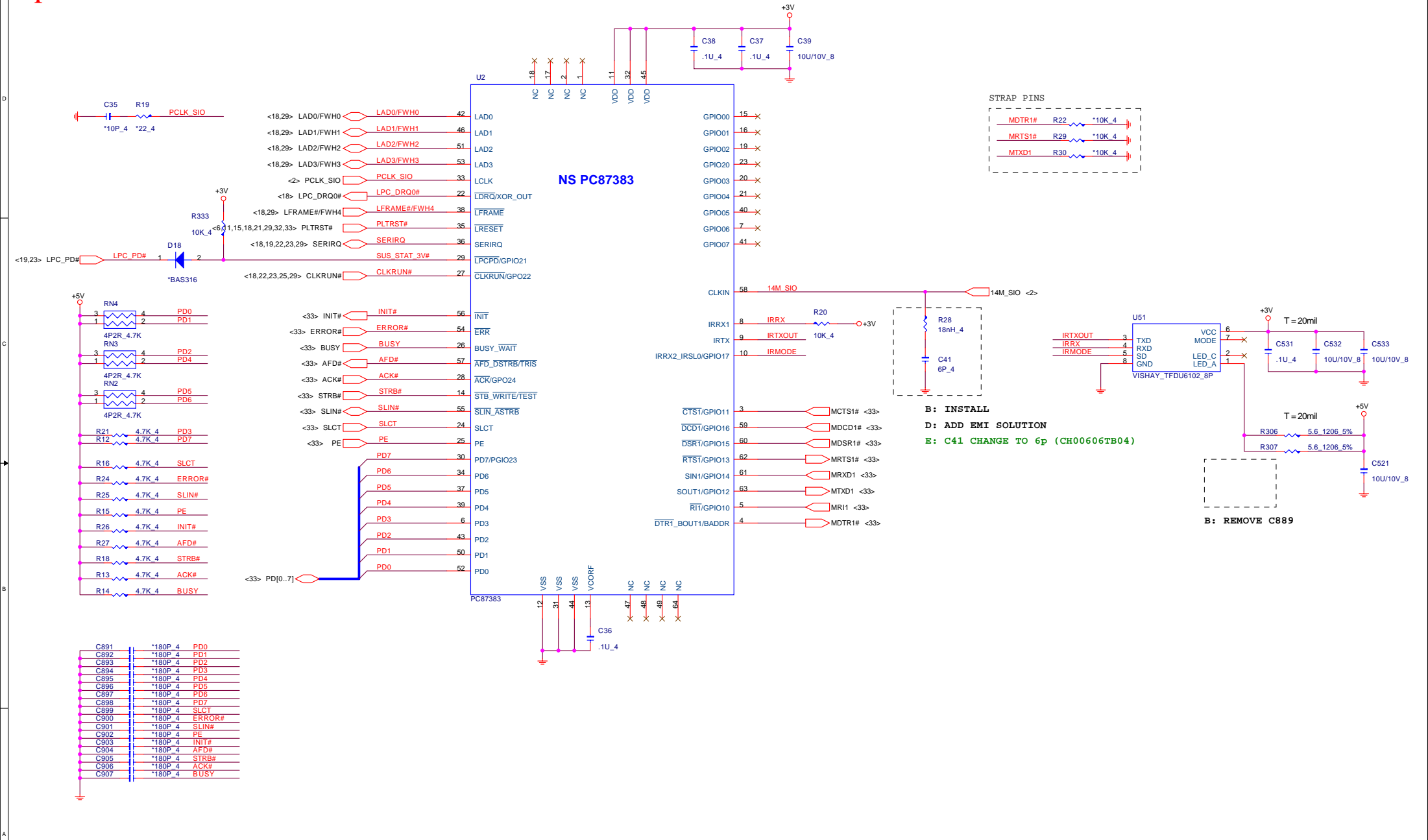
B: CHANGE EZ4'S HEADPHONE SOURCE



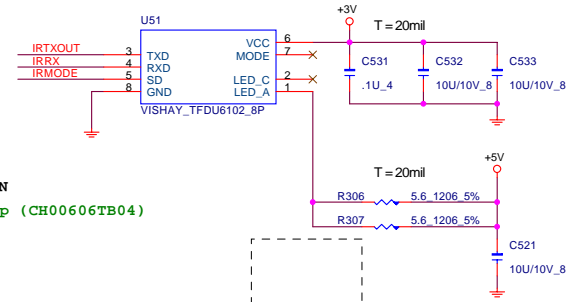
- F: NEW ADD FOR ESD



PROJECT : ZL2
Quanta Computer Inc.

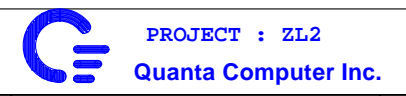


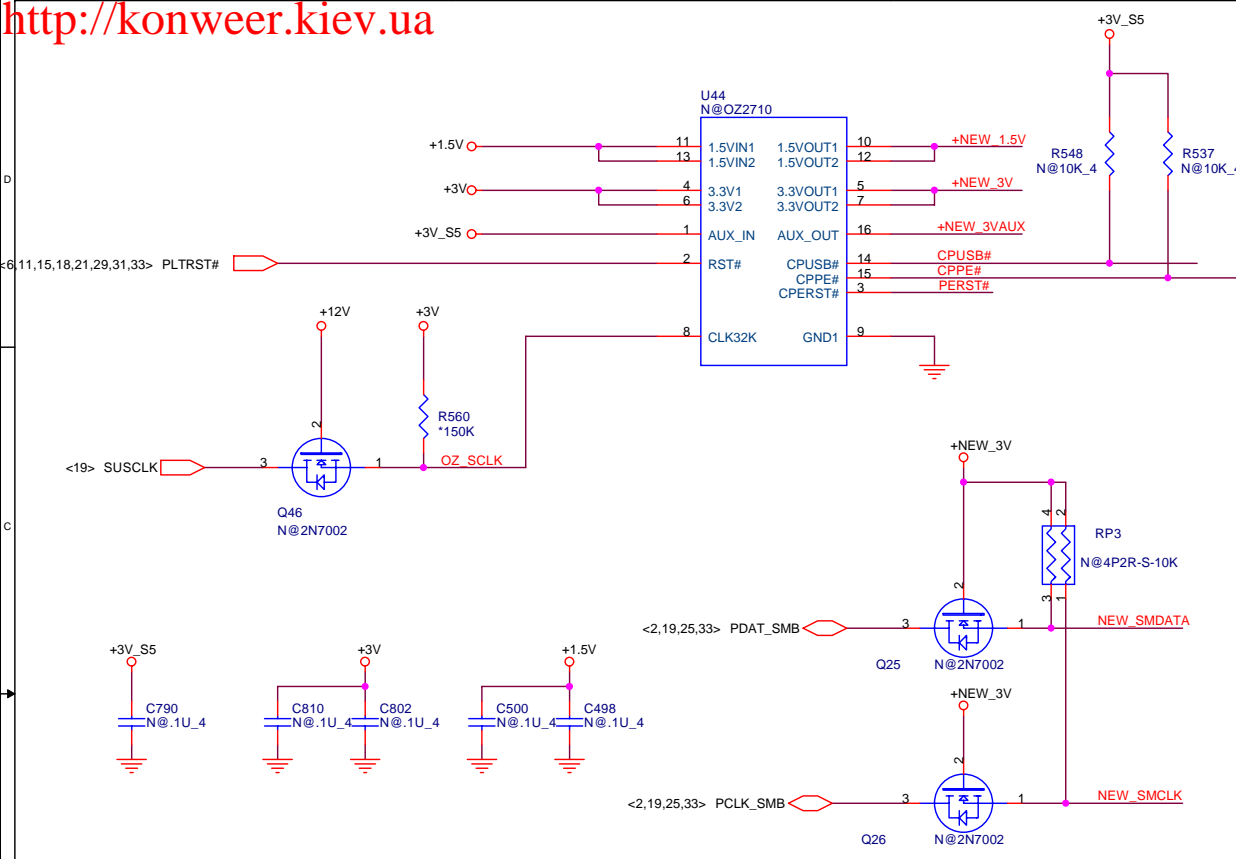
- B: INSTALL**
- D: ADD EMI SOLUTION**
- E: C41 CHANGE TO 6p (CH00606TB04)**



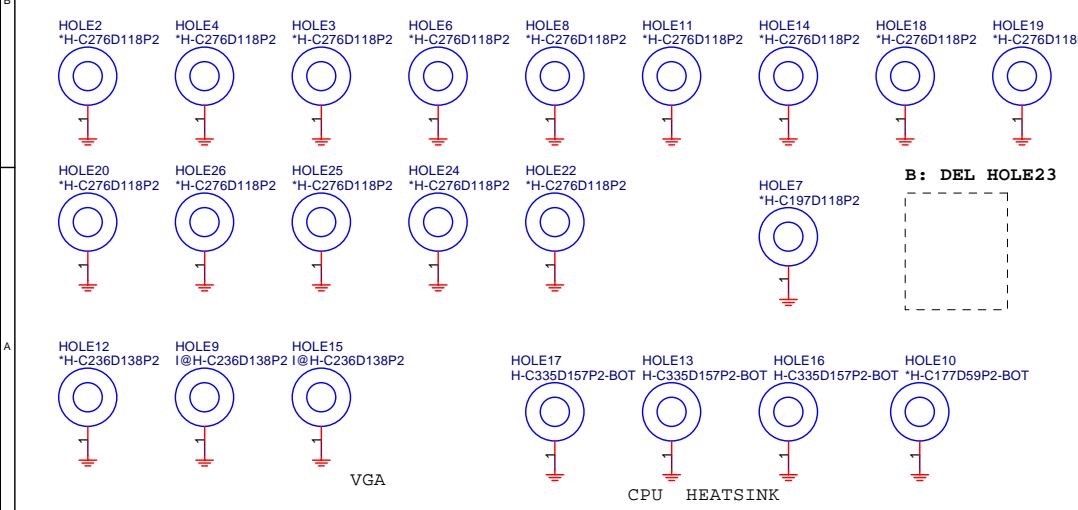
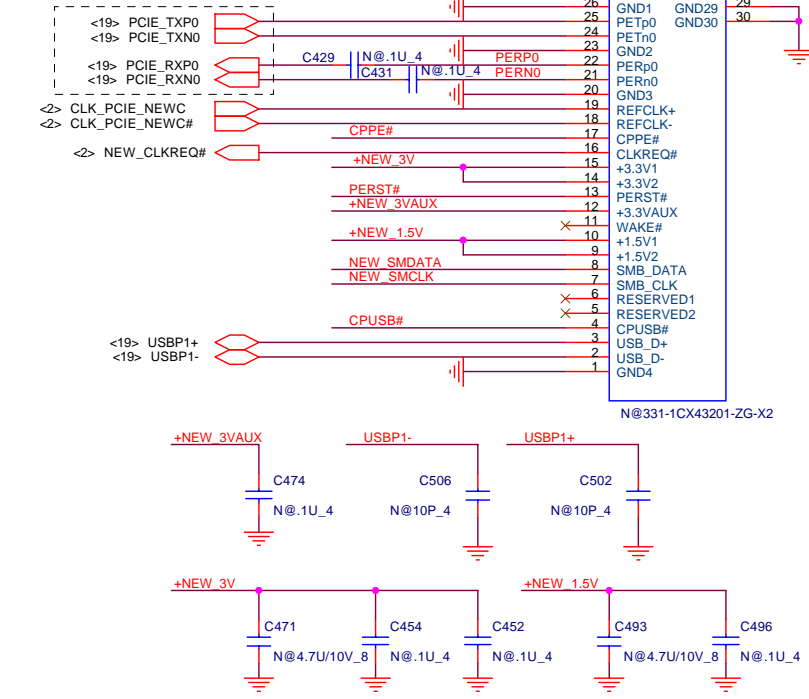
B: REMOVE C889

C891	*180P 4	PD0
C892	*180P 4	PD1
C893	*180P 4	PD2
C894	*180P 4	PD3
C895	*180P 4	PD4
C896	*180P 4	PD5
C897	*180P 4	PD6
C898	*180P 4	PD7
C899	*180P 4	SLCT
C900	*180P 4	ERROR#
C901	*180P 4	SLIN#
C902	*180P 4	PE
C903	*180P 4	INIT#
C904	*180P 4	AFD#
C905	*180P 4	STRB#
C906	*180P 4	ACK#
C907	*180P 4	BUSY

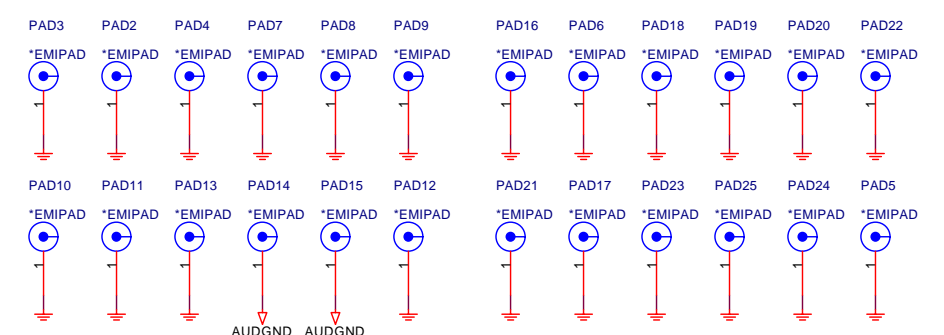




E: REVERSE RX AND TX

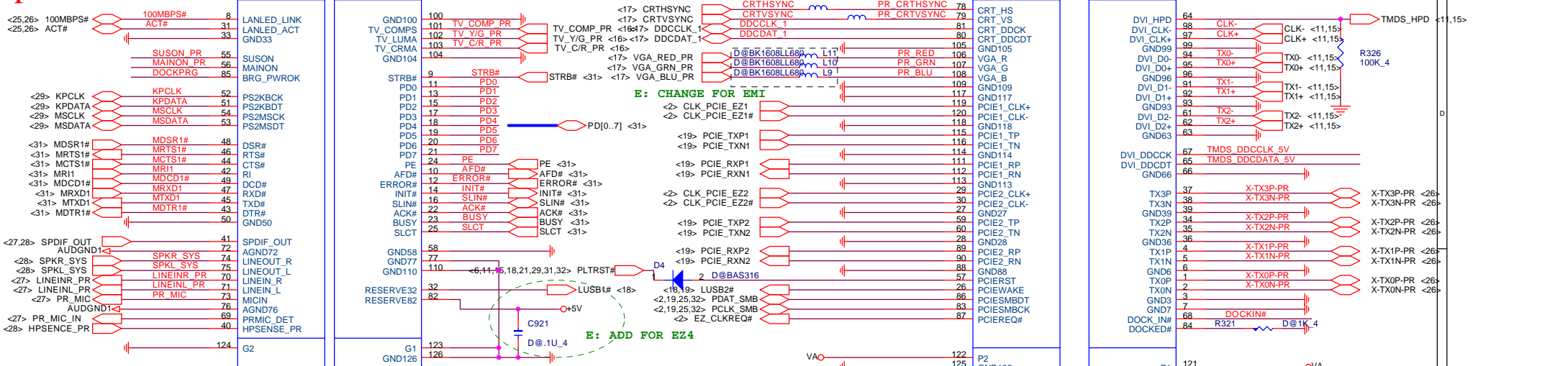


B: DEL HOLE23

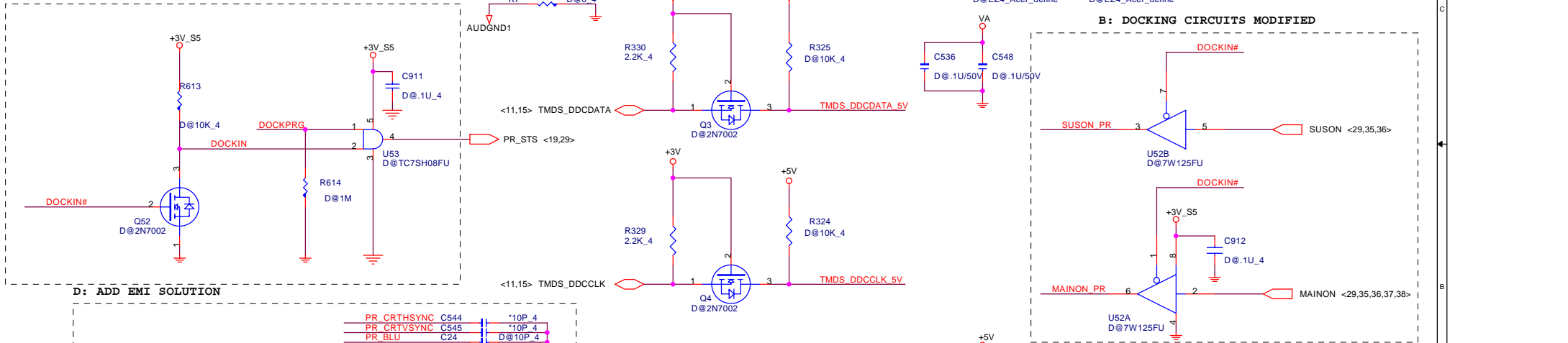


PROJECT : ZL2
Quanta Computer Inc.

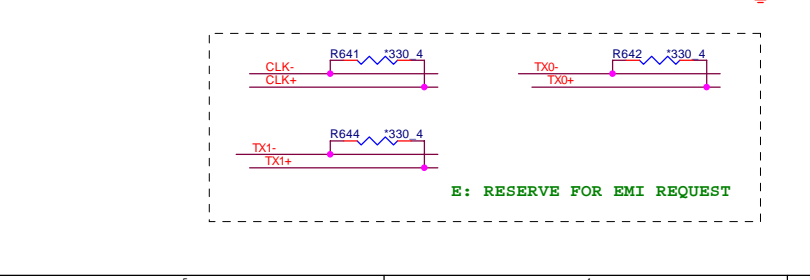
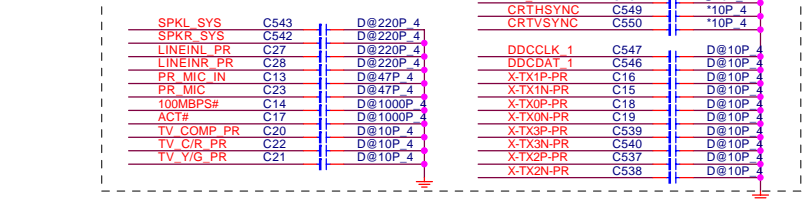
Size	Document Number	Rev
	EZ PORT & SIO (87383)	F
Date:	Tuesday, December 21, 2004	Sheet 32 of 41



B: DOCKING CIRCUITS MODIFIED

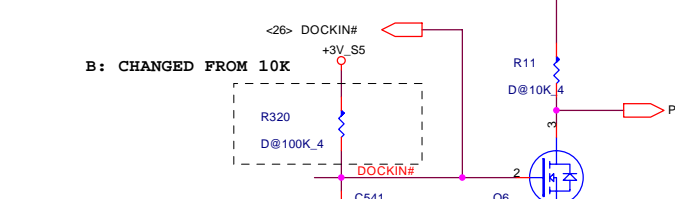


D: ADD EMI SOLUTION

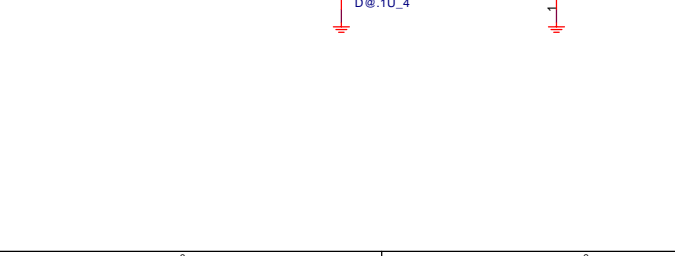


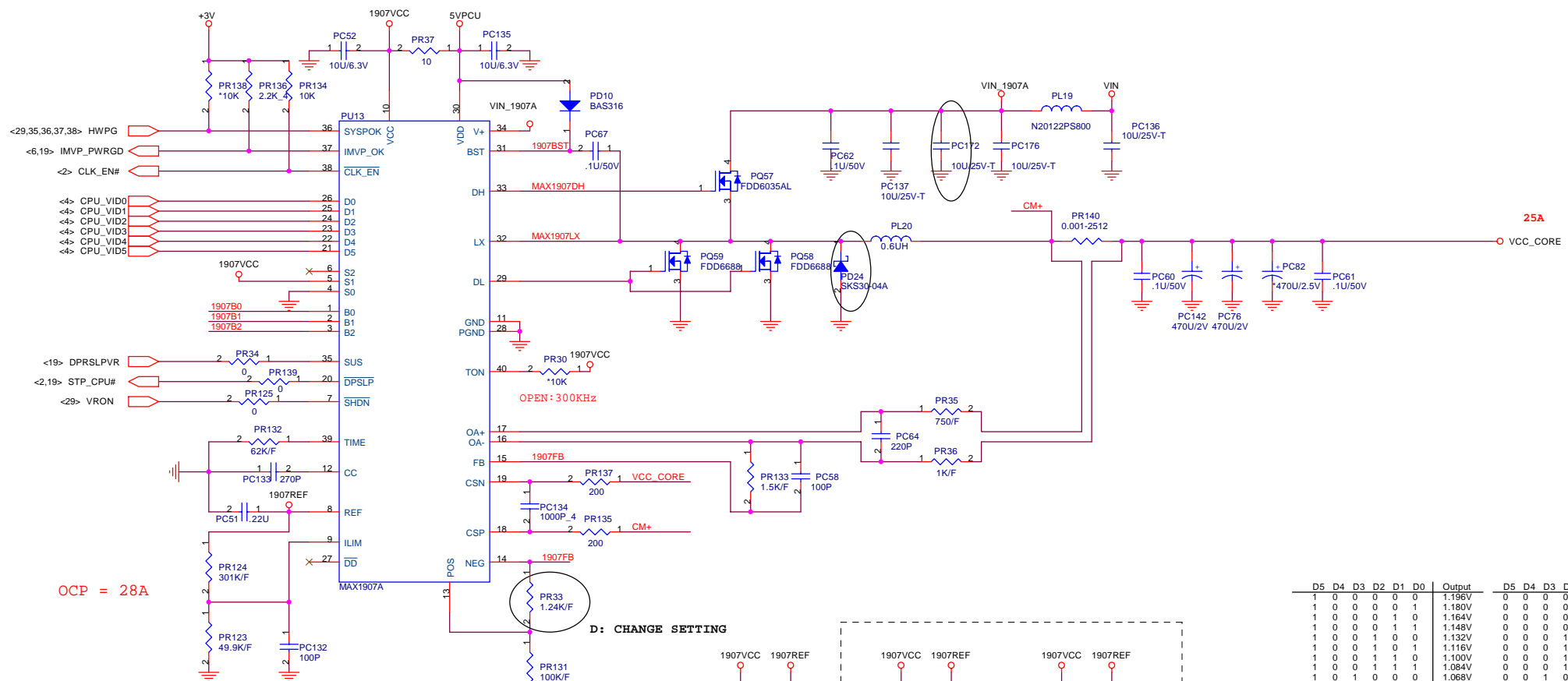
E: RESERVE FOR EMI REQUEST

E: POP C24,25,26 FOR EMI



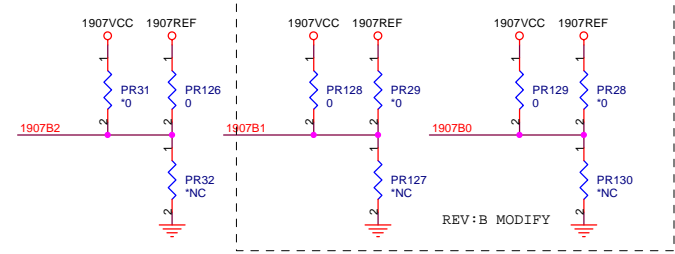
B: CHANGED FROM 10K





OCP = 28A

D: CHANGE SETTING



D: CHANGE SETTING


SUSPEND MODE (SUS=HIGH)

S2	S1	S0	Output
✓ OPEN	VCC	GND	0.748V

VCC_BOOT

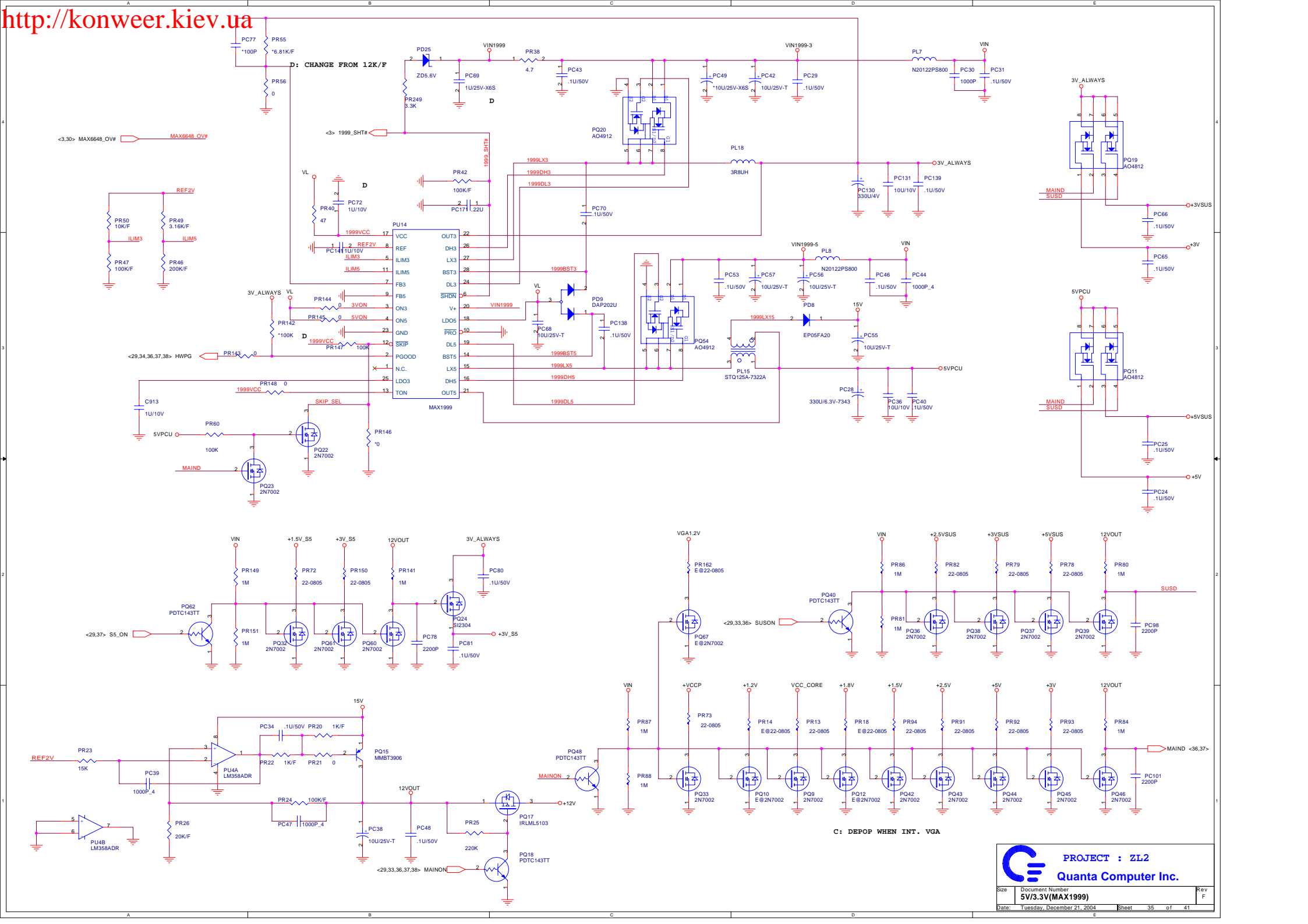
B2	B1	B0	Output
✓ GND	GND	GND	1.708V
REF	REF	REF	1.372V
OPEN	OPEN	OPEN	1.036V
VCC	VCC	VCC	0.700V
✓ REF	VCC	VCC	1.212V

D5	D4	D3	D2	D1	D0	Output	D5	D4	D3	D2	D1	D0	Output
1	0	0	0	0	0	1.196V	0	0	0	0	0	0	1.708V
1	0	0	0	0	1	1.180V	0	0	0	0	0	1	1.692V
1	0	0	0	1	0	1.164V	0	0	0	0	1	0	1.676V
1	0	0	0	1	1	1.148V	0	0	0	0	1	1	1.660V
1	0	0	1	0	0	1.132V	0	0	0	1	0	0	1.644V
1	0	0	1	0	1	1.116V	0	0	0	1	0	1	1.628V
1	0	0	1	1	0	1.100V	0	0	0	1	1	0	1.612V
1	0	0	1	1	1	1.084V	0	0	0	1	1	1	1.596V
1	0	1	0	0	0	1.068V	0	0	1	0	0	0	1.580V
1	0	1	0	0	1	1.052V	0	0	1	0	0	1	1.564V
1	0	1	0	1	0	1.036V	0	0	1	0	1	0	1.548V
1	0	1	0	1	1	1.020V	0	0	1	0	1	1	1.532V
1	0	1	1	0	0	1.004V	0	0	1	1	0	0	1.516V
1	0	1	1	0	1	0.988V	0	0	1	1	0	1	1.500V
1	0	1	1	1	0	0.972V	0	0	1	1	1	0	1.484V
1	0	1	1	1	1	0.956V	0	0	1	1	1	1	1.468V
1	1	0	0	0	0	0.940V	0	1	0	0	0	0	1.452V
1	1	0	0	0	1	0.924V	0	1	0	0	1	0	1.436V
1	1	0	0	1	0	0.908V	0	1	0	0	1	1	1.420V
1	1	0	0	1	1	0.892V	0	1	0	0	1	1	1.404V
1	1	0	1	0	0	0.876V	0	1	0	1	0	0	1.388V
1	1	0	1	0	1	0.860V	0	1	0	1	0	1	1.372V
1	1	0	1	1	0	0.844V	0	1	0	1	1	0	1.356V
1	1	0	1	1	1	0.828V	0	1	0	1	1	1	1.340V
1	1	1	0	0	0	0.812V	0	1	1	0	0	0	1.324V
1	1	1	0	0	1	0.796V	0	1	1	0	0	1	1.308V
1	1	1	0	1	0	0.780V	0	1	1	0	1	0	1.292V
1	1	1	0	1	1	0.764V	0	1	1	0	1	1	1.276V
1	1	1	1	0	0	0.748V	0	1	1	1	0	0	1.260V
1	1	1	1	0	1	0.732V	0	1	1	1	0	1	1.244V
1	1	1	1	1	0	0.716V	0	1	1	1	1	0	1.228V
1	1	1	1	1	1	0.700V	0	1	1	1	1	1	1.212V

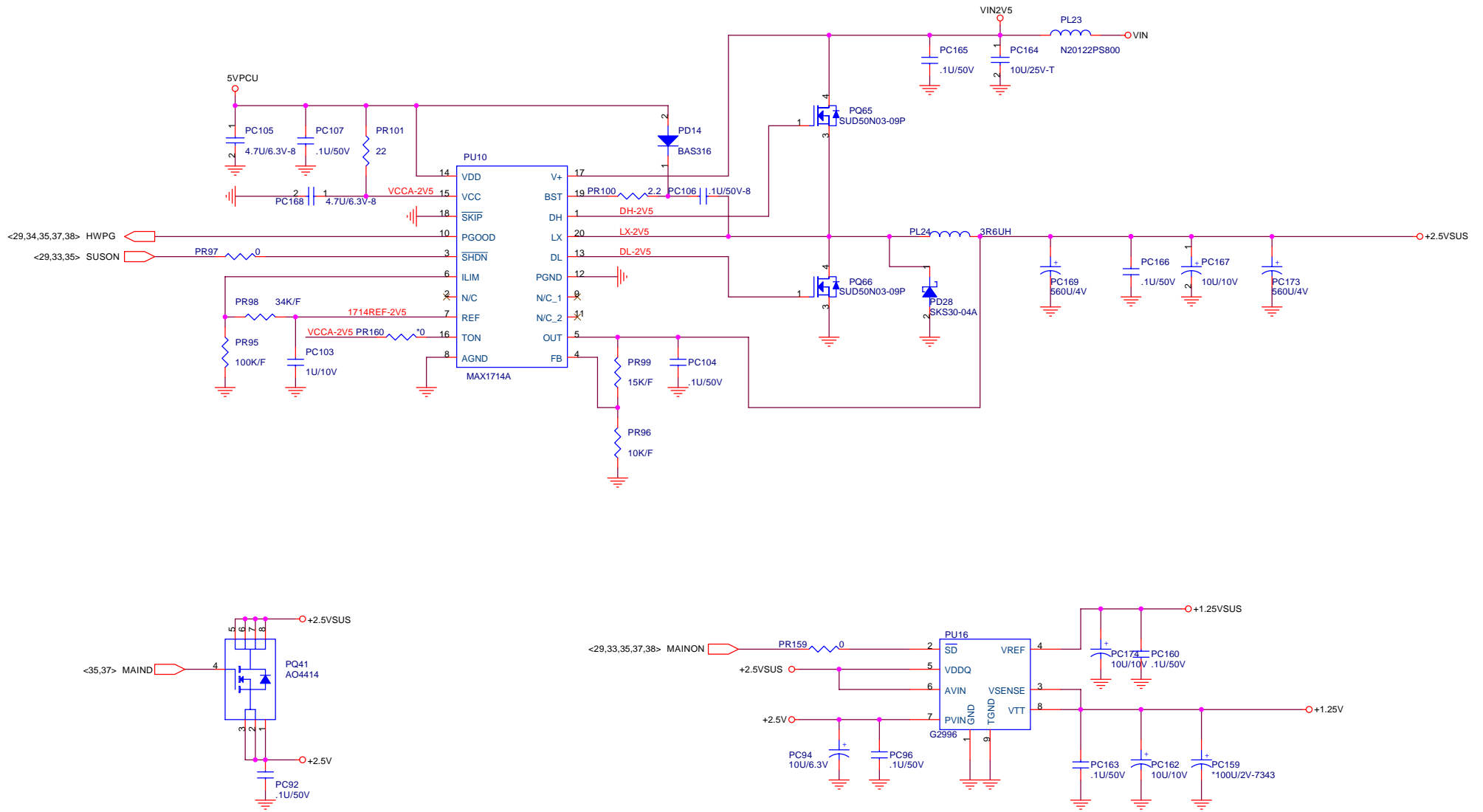


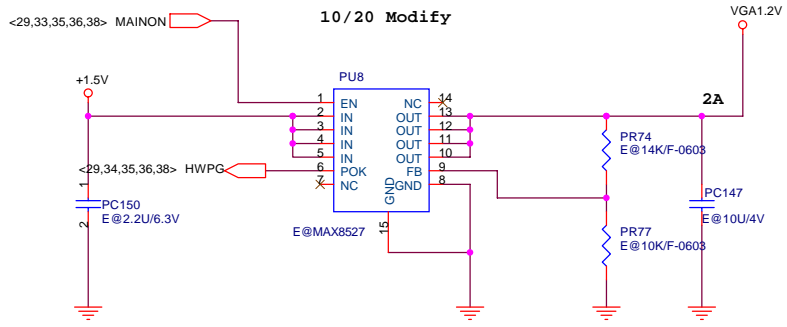
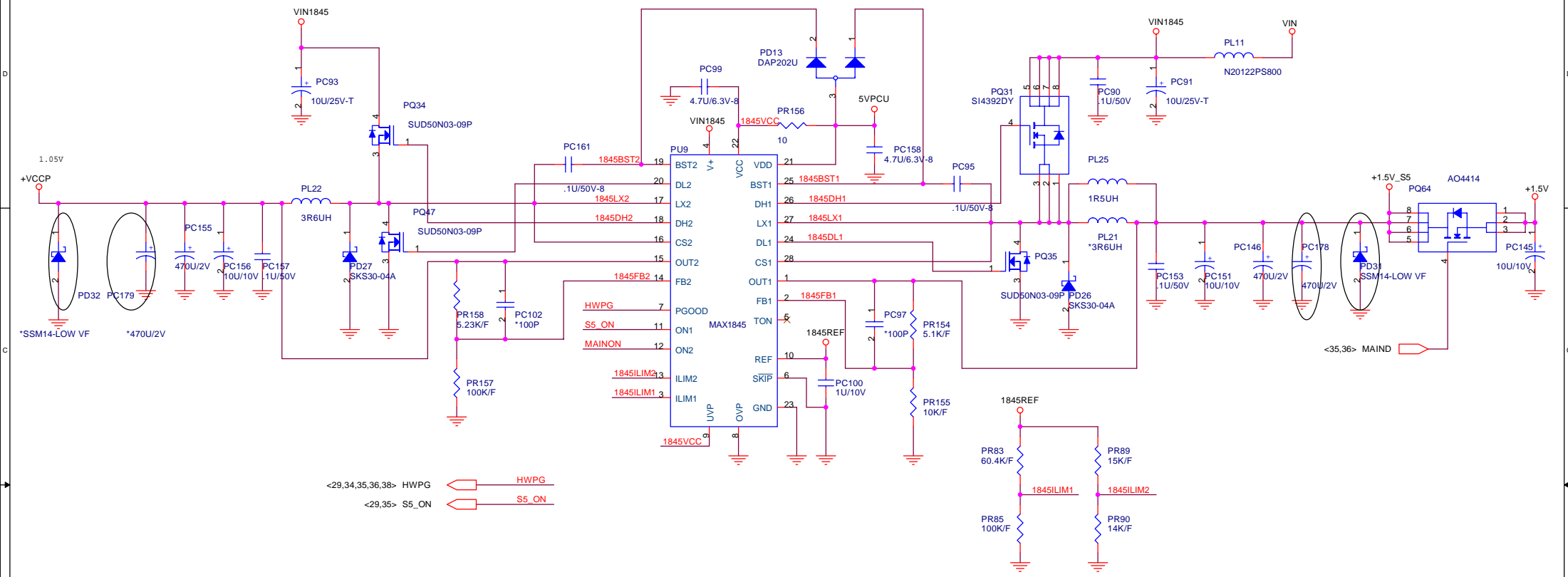
PROJECT : ZL2
Quanta Computer Inc.

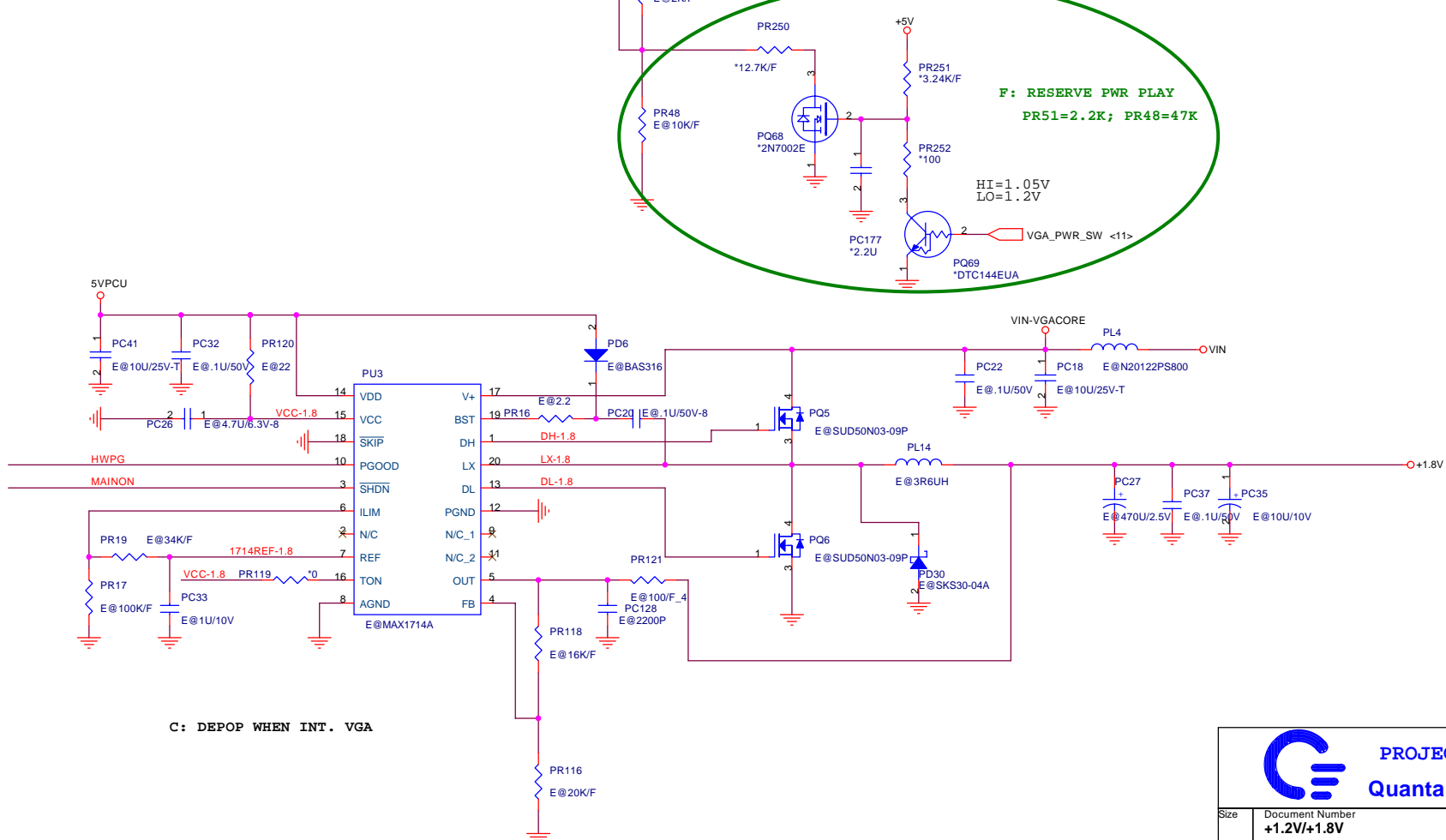
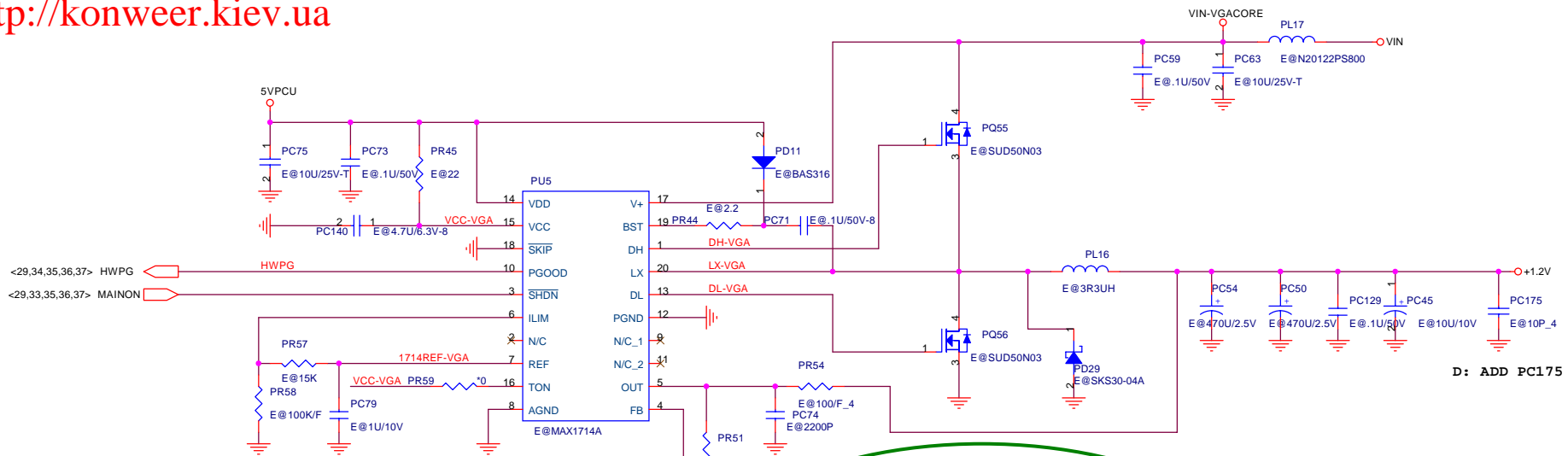
Size	Document Number	Rev
	CPU CORE (MAX1907)	F
Date:	Tuesday, December 21, 2004	Sheet 34 of 41

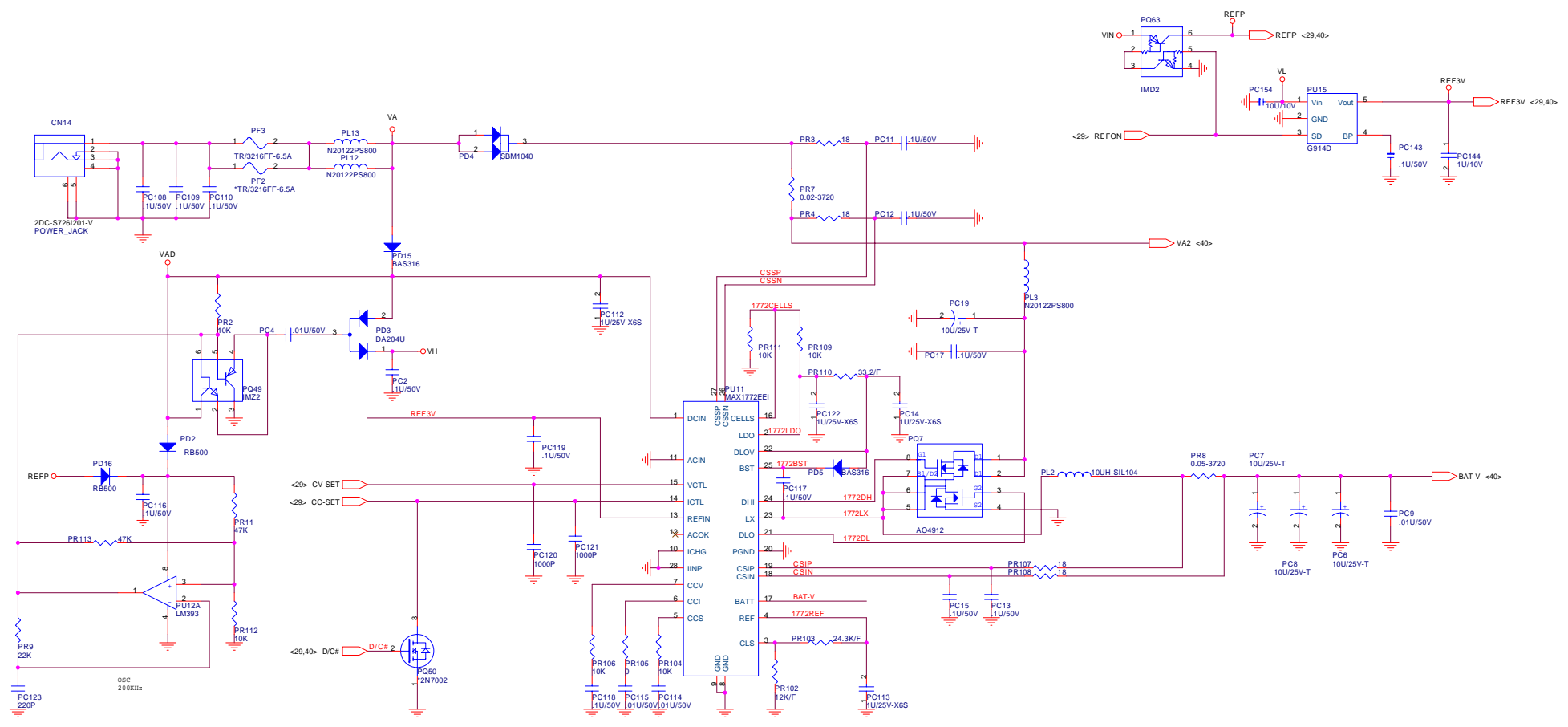


PROJECT : ZL2
Quanta Computer Inc.

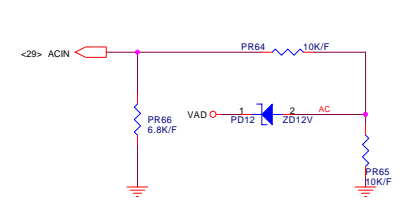




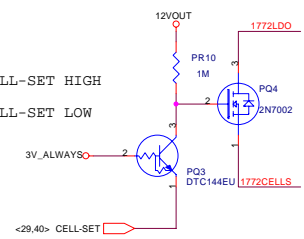




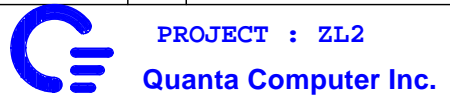
FOR 120W 6.2A



FOR 4S3P CELL-SET HIGH
FOR 3S3P CELL-SET LOW



	REV	CHANGE LIST	PAGE	FROM	TO	
ZL2 MotherBoard	B	PAGE2 1. CHANGE FREQ. SETTINGS FOR DOTHANB 2. UNSTUFF SSC COMPONENTS 3. UNSTUFF COMPONENTS FOR DIFFERENT SKUS	1			
		PAGE3 1. REMOVE R449, PULL HIGH AT POWER SIDE 2. UNSTUFF ITP COMPONENTS 3. STUFF R441 FOR THEMTRIP#	2			
		PAGE4 1. STUFF R432, R433 FOR AUTO-SELECT	3			
		PAGE5 1. STUFF R162 FOR DOTHAN-B	4			
		PAGE6 1. STUFF R147, R148, R151, R143, R152, R159 ALWAYS 2. NOT STUFF DVO COMPONENTS WHEN NO DOCKING	5			
		PAGE8 1. NOT STUFF FILTER COMPONENTS WHEN EXT. VGA 2. CHANGE D10, D11 TO CH551				
		PAGE9 1. STUFF R76, PCIE TESTIN PULL LOW 2. STUFF R369, GPIO0 PULL HIGH 3. CHANGE CLK OUTPUT TO XTALIN 4. NOT STUFF DVI COMPONENTS WHEN NO DOCKING				
		PAGE12 1. ADD 220UF IN VGAL.2V				
		PAGE15 1. NOT STUFF R60, R63 WHEN NO DVO DEVICE 2. NOT STUFF DVO COMPONENTS WHEN NO DOCKING				
		PAGE16 1. CHANGE TV-OUT LC VALUES				
		PAGE18 1. ADD DAMPING ON LFRAME# FOR AUDIO NOISE 2. STUFF R469 AND UNSTUFF R467 FOR DOTHAN-B 3. NOT STUFF COMPONENTS FOR SATA WHEN NO SATA 4. NOT STUFF AC TERMINATION FOR PCLK_1CH				
		PAGE19 1. NOT STUFF PCIE COMPONENTS WHEN NO PCIE DEVICES 2. CHANGE EMAIL LED GPIO 3. CHANGE MB_ID SETTING				
		PAGE21 1. REMOVE R50 2. CHANGE Q15 FROM BJT TO 2N7002	7			
		PAGE23 1. REMOVE RING FUNCTION 1. REMOVE 1394 CHOKE PADS	8			
		PAGE24 1. CHANGE 3IN1 CONNECTOR	9			
		PAGE25 1. CHANGE LED CONNECTION FOR 1G LAN 2. CORRECT U42 PIN.B11 PIN.C11 SHORT 3. CHANGE C741 AND C792'S SIZE FOR ME	10			
		PAGE26 1. UNINSTALL C210, C184, R87, R99 WHEN 10-100	11			
		PAGE27 1. INSERT 4700P IN BEEP SIGNALS 2. REMOVE SPK PR FROM CODEC 3. CHANGE C863 TO 10U 4. REVERSE MIC-SELECT 5. CHANGE R272 TO 0 OHM	12			
		PAGE28 1. CHANGE CONNECTION FOR SPKL-R TO EZ4 2. ADD SPRINGS FOR MODEM CABLE	13			
		PAGE29 1. CHANGE PR INSERT# TO PR_ST5	14			
		PAGE30 1. CHANGE KB AND TP'S CONNECTOR 2. CHANGE LED CIRCUITS	15			
		PAGE31 1. REMOVE C8892. STUFF AC TERMINATIONS FOR 14M_SIO	16			
		PAGE33 1. MODIFY EZ4 INTERFACE	17			
		PAGE 35 1. INCREASE CAPACITOR PC171 NEAR PR422. CHANGE COMPONENT PR38 SERIAL NUMBER FROM 0603 TO 1206 3. TAKE OFF PR39 PR43 PQ21 AND CHANGE NET NAME TO MAX6648_OV#	18			
		PAGE 35 3. INCREASE CAPACITOR C913 4. ADD DISCHARGE FOR VGAL.2V	19			
		PAGE 40 1. INCREASE RESISTOR PR161 NEAR PU7	20			
		PAGE 22 1. REMOVE CHOKE PADS	21			
		PAGE 39 1. TAKE OFF PQ50	22			
		PAGE 37 1. CHANGE PUS NET NAME TO +2.5V	23			
		PAGE 17 1. CHANGE HSYNC& VSYNC'S BEADS TO 0 OHM	24			
		C				
		DA0ZL2MB8C3				
		PAGE2 1. ADD PULLUPS ON CLKREQ PINS	16			
		PAGE3 1. ADD THERMAL SHUTDOWN CIRCUITS	17			
		PAGE13 1. CHANGE OPTIONS TO HYNIX MEMORY	18			
PAGE16 1. DEPOP C558	19					
PAGE17 1. CHANGE LC VALUES FOR RGB	20					
PAGE16, 17, 26 ADD 0 OHM RESISTORS TO SUBSTITUTE SWITCHES WHEN NO DOCKING	21					
PAGE25 1. CHANGE R531 TO 0 OHM 1. CHANGE R519 TO 1.2K/F	22					
PAGE27 1. CHANGE C512, C511 TO .7UF 1. CHANGE R592 TO 100K	23					
PAGE28 1. DEPOP Q28	24					
PAGE29 1. CHANGE BATLED0_1# PINS TO IOPJ6,7	25					
PAGE24 1. CHANGE 3-IN-1 CONNECTOR	26					
PAGE 34 1. INCREASE CAPACITOR PC172 10U/25V IN VIN 1907A 2. INCREASE SCHOTTKY DIODE PD24 SKS30-04A IN MAX1907LX .	27					
PAGE 35 1. INCREASE ZENER DIODE PD25 ZD5.6V SERIES WITH VIN1999 AND PR249 2. CHANGE NET NAME TO 1999_CHT#	28					
PAGE 36 1. CHANGE MOSFET SUD50N03-09P TO PQ65 PQ66 2. INCREASE CAPACITOR PC173 560U/4V IN +2.5VSUS 3. INCREASE CAPACITOR PC174 10U/10V IN +1.25VSUS 4. INSERT PR250 BETWEEN PU16 PIN2 AND PIN 5 .	29					
PAGE 37 1. CHANGE MOSFET SUD50N03-09P TO PQ34 PQ47 AND PQ35 2. TAKE OFF JUMP 3.CHANGE CAPACITOR PC155 PC146 TO 560U/4V	30					
PAGE 38 1. CHANGE MOSFET SUD50N03-09P TO PQ5 PQ6 PQ55 AND PQ56 2. TAKE OFF JUMP 3. CHANGE CHOKE PL16 TO 3R3UH 4.CHANGE CAPACITOR PC27 TO E@470U/2.5V	31					
PAGE 39 1. EXCHANGE NET NAME 3V_ALWAYS AND CELL-SET	32					
PAGE 23,25,29,31 1. DEPOP RC FILTERS ON PCI CLOCKS	33					
D						
PAGE2 1. DEPOP R615 2. ADD 47P ON 14M_SIO	28					
PAGE3 1. POP R449	29					
PAGE6 1. DEPOP RP7	30					
PAGE16 1. LID SW FOOTPRINT CHANGED	31					
PAGE19 1. ADD MB_ID3	32					
PAGE21 1. CHANGE C82'S RATING TO 25V	33					
PAGE28 1. PHONE JACK CHANGED TO SPDIF	34					
PAGE30 1. CHANGE R338 TO 100K	35					
PAGE31 1. CHANGE R28 AND C41'S VALUE						
PAGE33 1. ADD EMI CAPS						
PAGE34 1. ADD CAP PC176						
PAGE35 1. CHANGE CAP PC69 COMPONENT 2. CHANGE CAP PC72 CONNECT POINT 3. CHANGE RESISTOR PR147 COMPONENT						
E						
PAGE2 Change R200, R202, C915 value to pass EA	PAGE18 Change GPIO pin define, add R645					
PAGE3,30 DEPOP COMPONENTS FOR MAX6648_OV#	PAGE20 D25 change to CH551					
PAGE6 1. DEPOP R186, R184	PAGE31 Change C41 to 6pF					
PAGE8 Add C918 to solve TV issue	PAGE33 Modify EZ4 pin define, add R641, 642, 644 for EMI					
PAGE12 Add C919,920, change L25,27,67						
PAGE16 1. ADD LEVEL SHIFT FOR EDID						
PAGE16 Change R8, R9, remove C9, C32 for pass Acer LCD						
PAGE19 ADD 100K PULLLOW ON DPRSLPVR, change C428						
PAGE21 1. CHANGE SWAP-ODD RESET						
PAGE23 1. ADD PULL-LOW ON PCMSPK	PAGE 35 1. CHANGE PC68 COMPONENT.					
PAGE24 1. ADD 30OHM CURRENT LIMIT ON VCC_XD	PAGE 36 1. CHANGE PR99 PD28 COMPONENT.					
PAGE32 1. REVERSE RX AND TX	PAGE 37 1. CHANGE PD26 PD27 COMPONENT.					
	PAGE 38 1. CHANGE PR51 COMPONENT.					
	PAGE 39 1. CHANGE PQ7 COMPONENT.					
	2. CHANGE PR74 PR77 PUS PC147 PC150 COMPONENT.					
	2. CHANGE PD29 PD30 COMPONENT.					



PROJECT : ZL2

APPROVE BY: SELMON LIU

DRAWING BY:JOE LIN

REV

COVER SHEET 1 OF 1

Document Number
CHANGE LIST

MB ASSY'S P/N : 31ZL1MB0004

PROJECT LEADER: SELMON LIU

DOCUMENT NO:

DATE :2004/06/01