

# Compal Confidential

## HCL51 Schematics Document

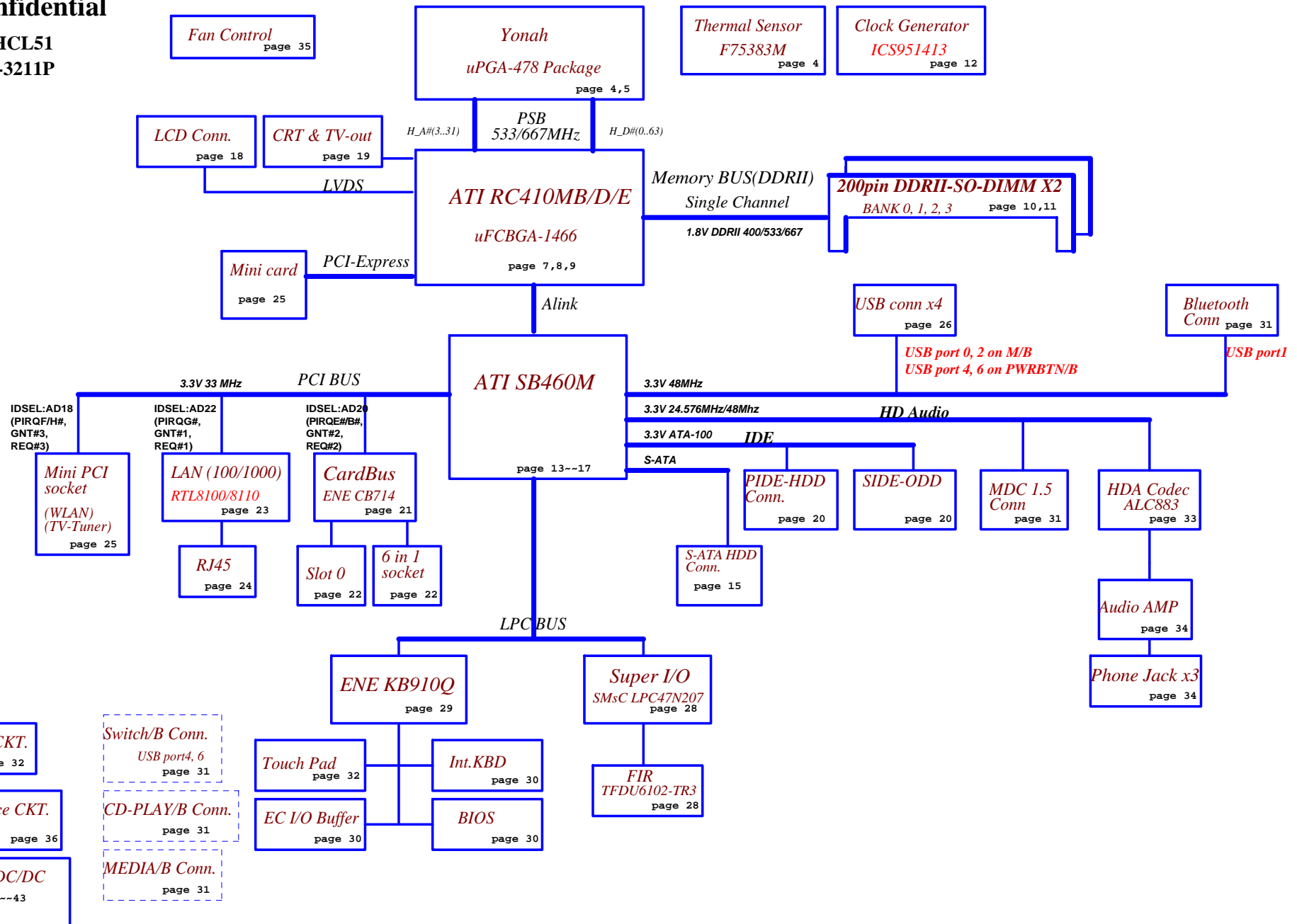
Intel Yonah Processor with ATIRC410MD/E + DDRII + SB460M

2006-04-05

REV: 1.0

Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2005/06/20	Deciphered Date	2006/06/20	Title	Cover Page
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number	Rev
				HCL51 LA-3211P	0.4
				Date: 星期二, 四月 11, 2006	Sheet 1 of 43

Model Name : HCL51  
 File Name : LA-3211P



Security Classification	Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2005/06/20	Deciphered Date	2006/06/20	Title
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number
				HCL51 LA-3211P
				Rev 0.4
				Date: 星期二, 四月 11, 2006
				Sheet 2 of 43

### Voltage Rails

Power Plane	Description	S1	S3	S5
VIN	Adapter power supply (19V)	N/A	N/A	N/A
B+	AC or battery power rail for power circuit.	N/A	N/A	N/A
+CPU_CORE	Core voltage for CPU	ON	OFF	OFF
+0.9VS	0.9V switched power rail for DDR terminator	ON	OFF	OFF
+1.05VS	1.05V switched power rail	ON	OFF	OFF
+1.5VS	1.5V switched power rail	ON	OFF	OFF
+1.8V	1.8V power rail for DDR	ON	ON	OFF
+1.8VS	1.8V switched power rail	ON	OFF	OFF
+2.5VS	2.5V switched power rail	ON	OFF	OFF
+3VALW	3.3V always on power rail	ON	ON	ON*
+3VS	3.3V switched power rail	ON	OFF	OFF
+5VALW	5V always on power rail	ON	ON	ON*
+5VS	5V switched power rail	ON	OFF	OFF
+VSB	VSB always on power rail	ON	ON	ON*
+RTCVCC	RTC power	ON	ON	ON

Note : ON\* means that this power plane is ON only with AC power available, otherwise it is OFF.

### External PCI Devices

Device	IDSEL#	REQ#/GNT#	Interrupts
CardBus(SD)	AD20	2	PIRQE/PIRQH
1394	AD16	0	PIRQA
LAN(10/100)	AD22	1	PIRQG
Mini-PCI(WLAN/TV-Tuner)	AD18	3	PIRQF/PORQH

### EC SM Bus1 address

### EC SM Bus2 address

Device	Address	Device	Address
Smart Battery	0001 011X b	Fintek F75383M	1001 100X b
EEPROM(24C16/02)	1010 000X b		
GMT G781-1	1001 101X b		

### SB460M SM Bus address

Device	Address
Clock Generator (ICS951413)	1101 001Xb
DDR DIMM0	1001 000Xb
DDR DIMM2	1001 010Xb

STATE	SIGNAL	SLP_S1#	SLP_S3#	SLP_S4#	SLP_S5#	+VALW	+V	+VS	Clock
Full ON		HIGH	HIGH	HIGH	HIGH	ON	ON	ON	ON
S1 (Power On Suspend)		LOW	HIGH	HIGH	HIGH	ON	ON	ON	LOW
S3 (Suspend to RAM)		LOW	LOW	HIGH	HIGH	ON	ON	OFF	OFF
S4 (Suspend to Disk)		LOW	LOW	LOW	HIGH	ON	OFF	OFF	OFF
S5 (Soft OFF)		LOW	LOW	LOW	LOW	ON	OFF	OFF	OFF

### Board ID / SKU ID Table for AD channel

Vcc	3.3V +/- 5%			
Ra/Rc/Re	100K +/- 5%			
Board ID	Rb / Rd / Rf	VAD_BID min	VAD_BID typ	VAD_BID max
0	0	0 V	0 V	0 V
1	8.2K +/- 5%	0.216 V	0.250 V	0.289 V
2	18K +/- 5%	0.436 V	0.503 V	0.538 V
3	33K +/- 5%	0.712 V	0.819 V	0.875 V
4	56K +/- 5%	1.036 V	1.185 V	1.264 V
5	100K +/- 5%	1.453 V	1.650 V	1.759 V
6	200K +/- 5%	1.935 V	2.200 V	2.341 V
7	NC	2.500 V	3.300 V	3.300 V

### BOARD ID Table

Board ID	PCB Revision
0	
1	0.2
2	0.2
3	
4	
5	
6	
7	

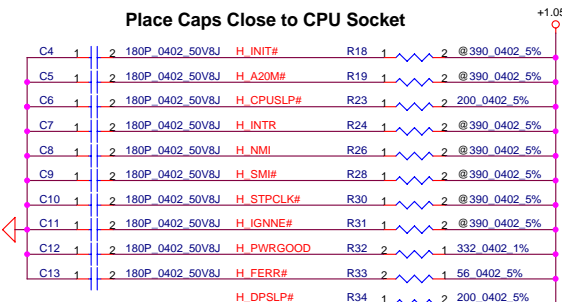
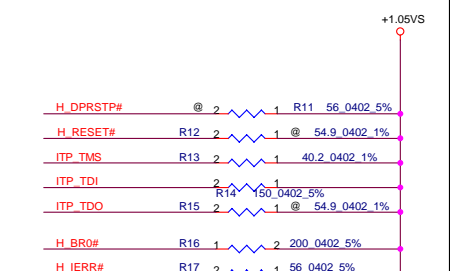
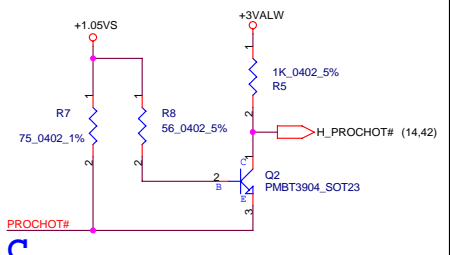
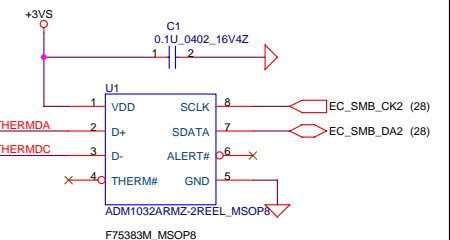
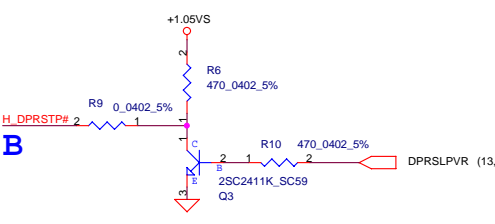
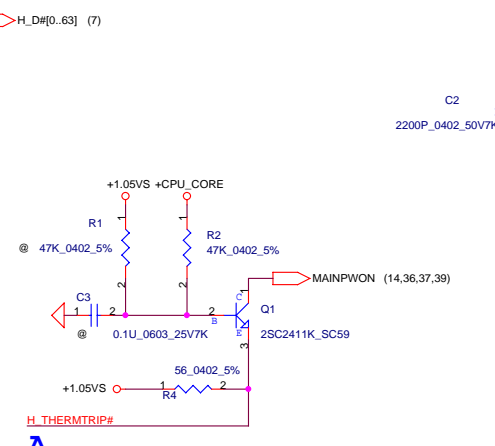
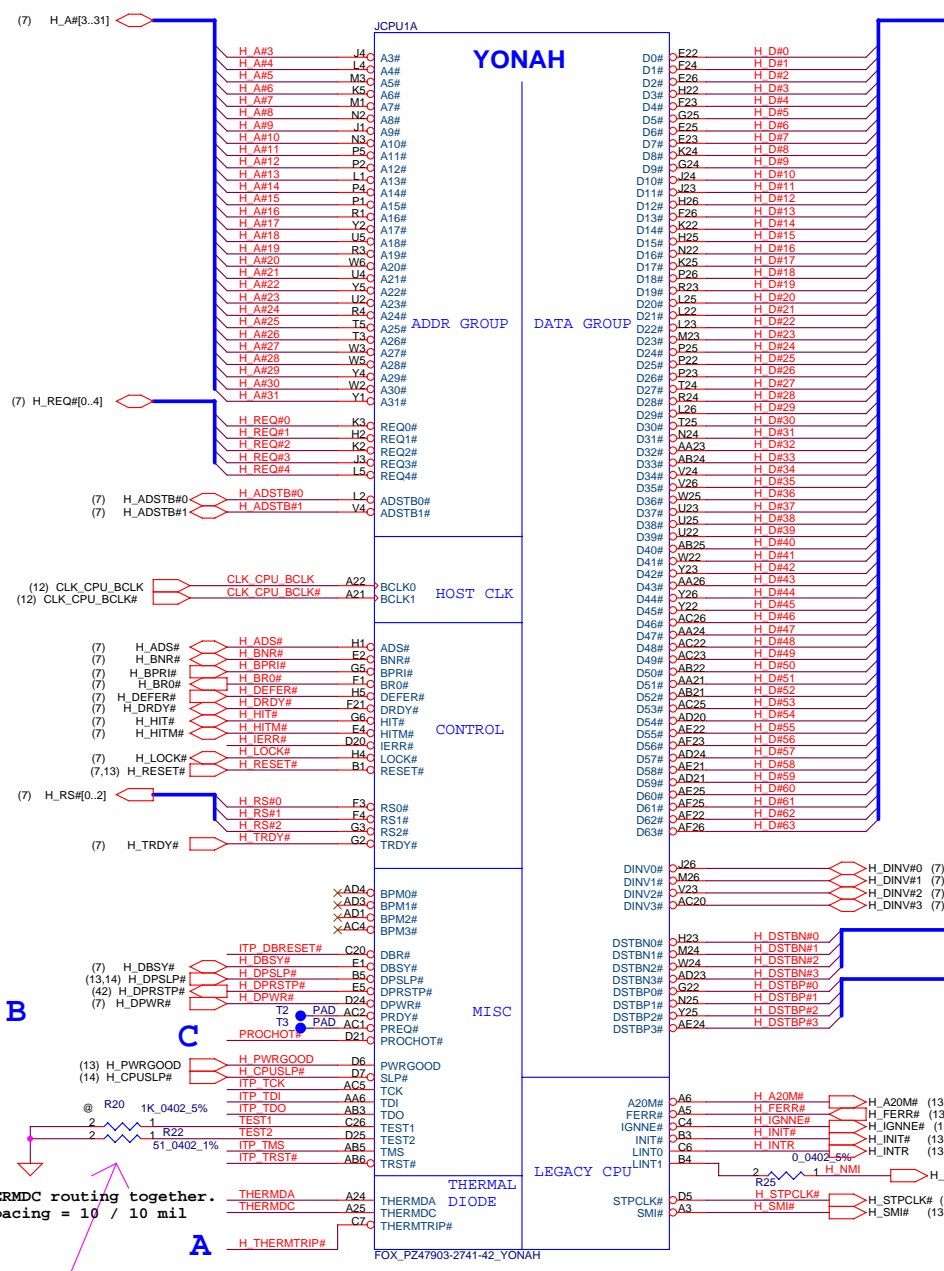
### BTO Option Table

BTO Item	BOM Structure
LAN(10/100)	8100C@
LAN(GIGA)	8110S@
FIR	FIR@
MINI CARD1	MINI1@
SATA HDD	SATA@
CardReader	4IN1@

### SKU ID Table

SKU ID	SKU
0	
1	
2	
3	
4	
5	
6	
7	

Security Classification	Compal Secret Data		Title		Compal Electronics, Inc.		
Issued Date	2005/06/20	Deciphered Date	2006/06/20	Notes List			
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number	HCL51 LA-3211P	Rev	0.4
				Date:	星期二, 四月 11, 2006	Sheet	3 of 43

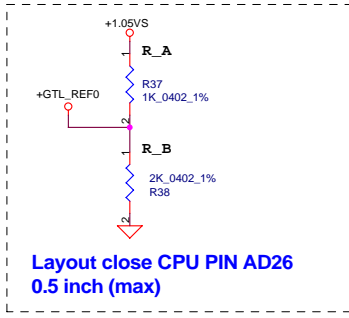
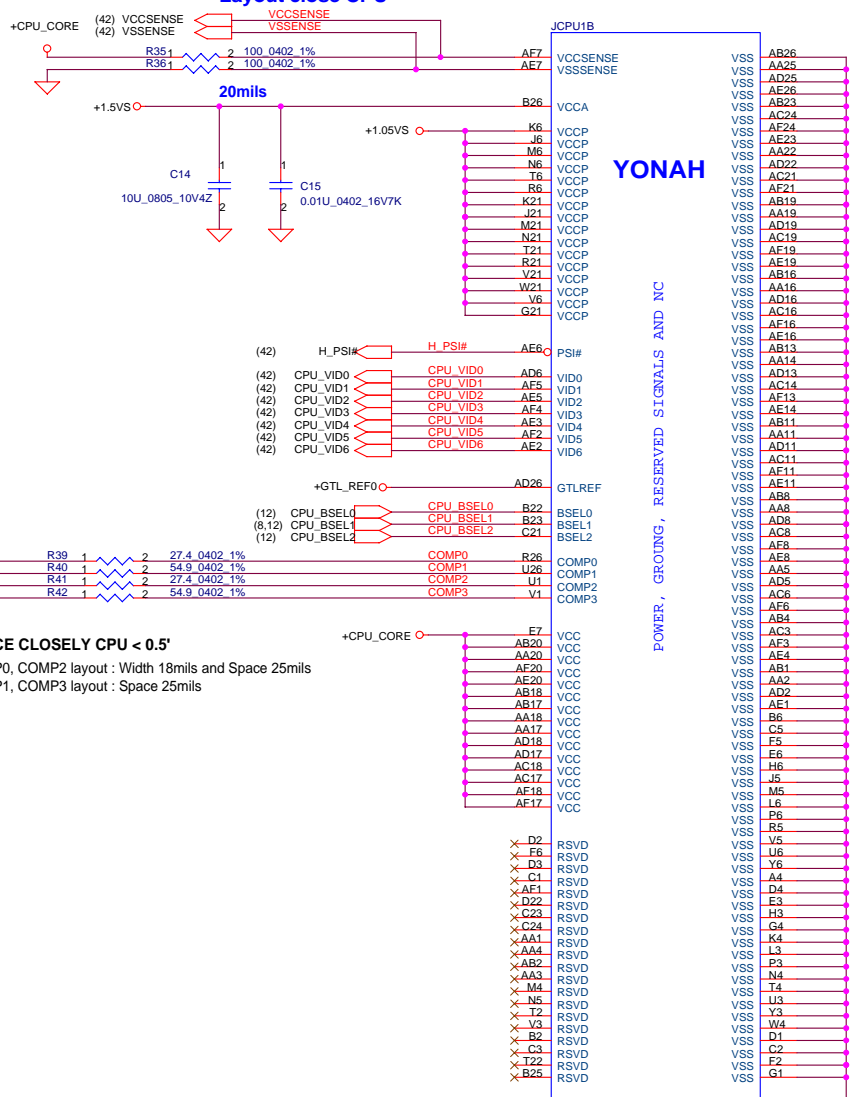


H\_THERMDA, H\_THERMDC routing together. Trace width / Spacing = 10 / 10 mil

For B-0 stepping engineering samples (ES) of Celeron M processor need to pop this 51 ohm resistor.

Length match within 25 mils

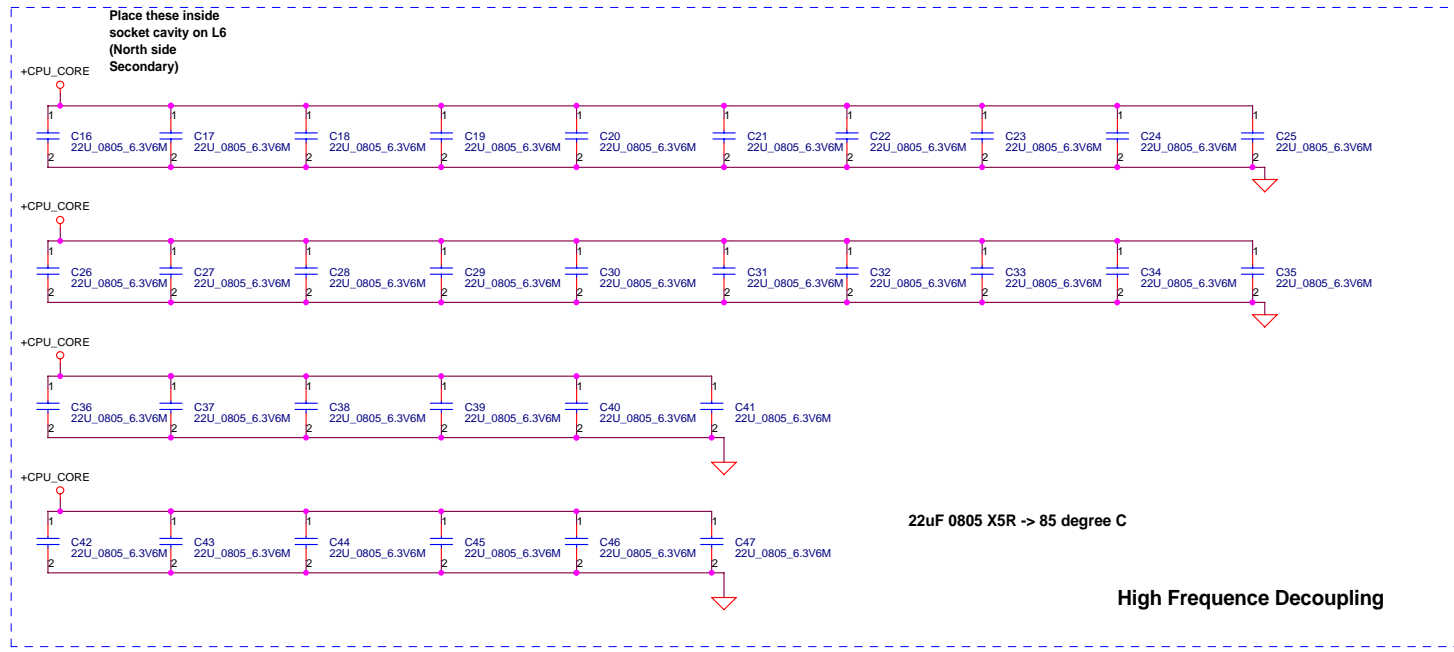
Layout close CPU



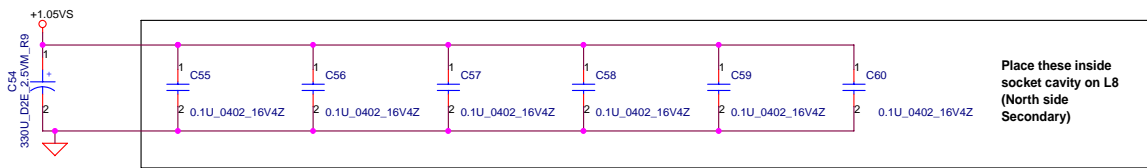
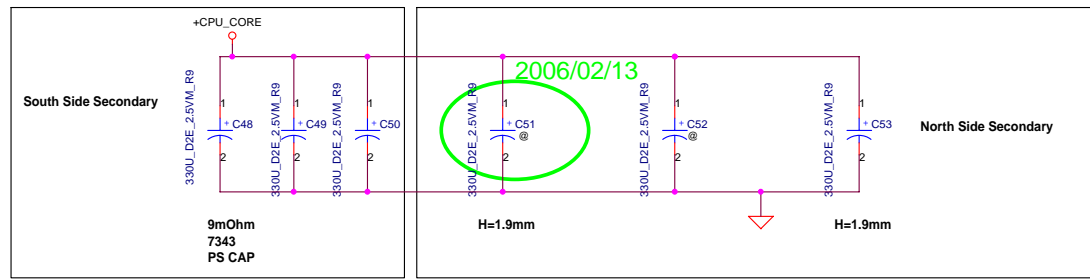
CPU_BSEL	CPU_BSEL0	CPU_BSEL1	CPU_BSEL2
133	0	0	1
166	0	1	1

TRACE CLOSELY CPU < 0.5'

COMP0, COMP2 layout : Width 18mils and Space 25mils  
 COMP1, COMP3 layout : Space 25mils

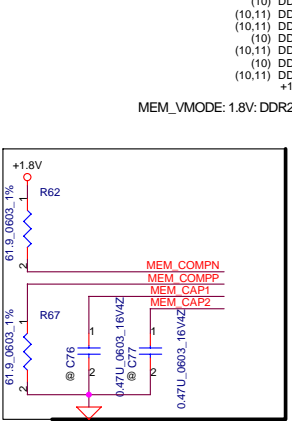
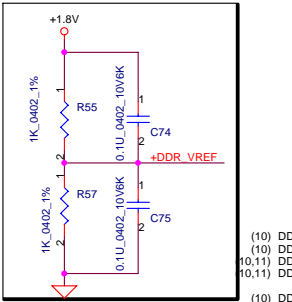
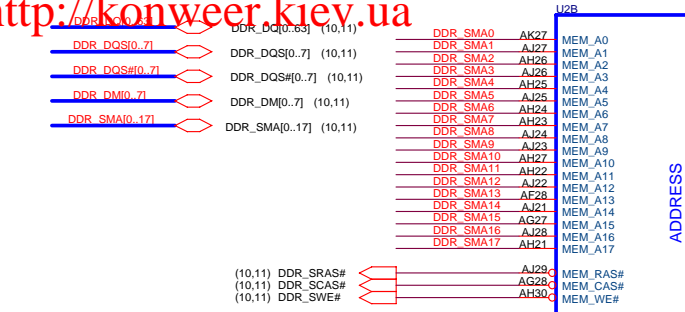


Near VCORE regulator.



Security Classification	Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2005/11/01	Deciphered Date	2006/11/30	Title
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF THE CUSTOMER DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number
				Yonah Bypass
				Rev
				0.4
				Date: 星期二, 四月 11, 2006
				Sheet 6 of 43





MEM\_VMODE: 1.8V; DDR2

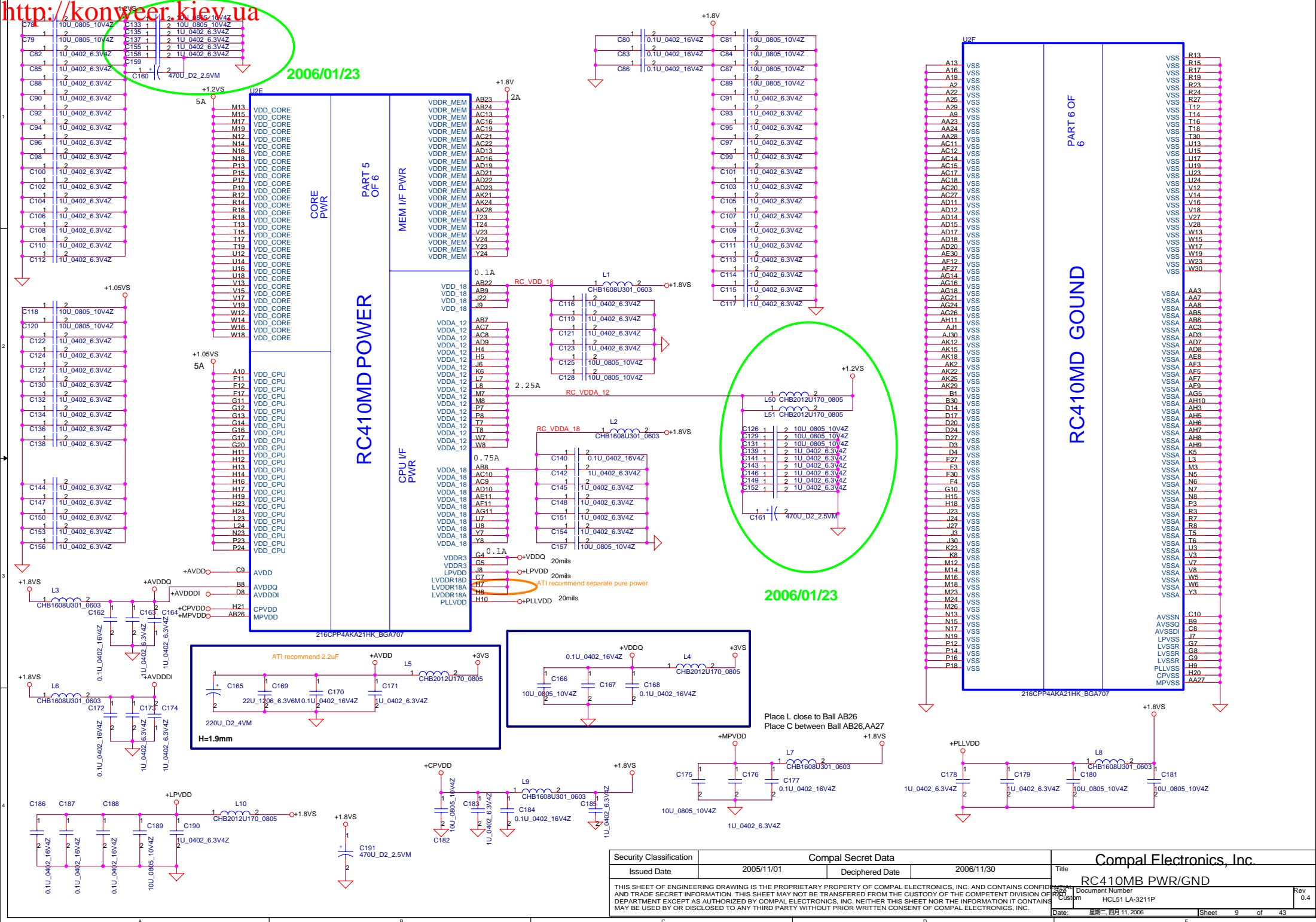
ADDRESS	CLK	MISC	DATA
MEM_A0	MEM_CK0N	MEM_CS#0	MEM_DM0
MEM_A1	MEM_CK0P	MEM_CS#1	MEM_DM1
MEM_A2	MEM_CK1N	MEM_CS#2	MEM_DM2
MEM_A3	MEM_CK1P	MEM_CS#3	MEM_DM3
MEM_A4	MEM_CK2N	MEM_CS#4	MEM_DM4
MEM_A5	MEM_CK2P	MEM_CS#5	MEM_DM5
MEM_A6	MEM_CK3N	MEM_CS#6	MEM_DM6
MEM_A7	MEM_CK3P	MEM_CS#7	MEM_DM7
MEM_A8	MEM_CK4N	MEM_CS#8	MEM_DM8
MEM_A9	MEM_CK4P	MEM_CS#9	MEM_DM9
MEM_A10	MEM_CK5N	MEM_CS#10	MEM_DM10
MEM_A11	MEM_CK5P	MEM_CS#11	MEM_DM11
MEM_A12	MEM_CK6N	MEM_CS#12	MEM_DM12
MEM_A13	MEM_CK6P	MEM_CS#13	MEM_DM13
MEM_A14	MEM_CK7N	MEM_CS#14	MEM_DM14
MEM_A15	MEM_CK7P	MEM_CS#15	MEM_DM15
MEM_A16	MEM_CK8N	MEM_CS#16	MEM_DM16
MEM_A17	MEM_CK8P	MEM_CS#17	MEM_DM17
MEM_A18	MEM_CK9N	MEM_CS#18	MEM_DM18
MEM_A19	MEM_CK9P	MEM_CS#19	MEM_DM19
MEM_A20	MEM_CK10N	MEM_CS#20	MEM_DM20
MEM_A21	MEM_CK10P	MEM_CS#21	MEM_DM21
MEM_A22	MEM_CK11N	MEM_CS#22	MEM_DM22
MEM_A23	MEM_CK11P	MEM_CS#23	MEM_DM23
MEM_A24	MEM_CK12N	MEM_CS#24	MEM_DM24
MEM_A25	MEM_CK12P	MEM_CS#25	MEM_DM25
MEM_A26	MEM_CK13N	MEM_CS#26	MEM_DM26
MEM_A27	MEM_CK13P	MEM_CS#27	MEM_DM27
MEM_A28	MEM_CK14N	MEM_CS#28	MEM_DM28
MEM_A29	MEM_CK14P	MEM_CS#29	MEM_DM29
MEM_A30	MEM_CK15N	MEM_CS#30	MEM_DM30
MEM_A31	MEM_CK15P	MEM_CS#31	MEM_DM31
MEM_A32	MEM_CK16N	MEM_CS#32	MEM_DM32
MEM_A33	MEM_CK16P	MEM_CS#33	MEM_DM33
MEM_A34	MEM_CK17N	MEM_CS#34	MEM_DM34
MEM_A35	MEM_CK17P	MEM_CS#35	MEM_DM35
MEM_A36	MEM_CK18N	MEM_CS#36	MEM_DM36
MEM_A37	MEM_CK18P	MEM_CS#37	MEM_DM37
MEM_A38	MEM_CK19N	MEM_CS#38	MEM_DM38
MEM_A39	MEM_CK19P	MEM_CS#39	MEM_DM39
MEM_A40	MEM_CK20N	MEM_CS#40	MEM_DM40
MEM_A41	MEM_CK20P	MEM_CS#41	MEM_DM41
MEM_A42	MEM_CK21N	MEM_CS#42	MEM_DM42
MEM_A43	MEM_CK21P	MEM_CS#43	MEM_DM43
MEM_A44	MEM_CK22N	MEM_CS#44	MEM_DM44
MEM_A45	MEM_CK22P	MEM_CS#45	MEM_DM45
MEM_A46	MEM_CK23N	MEM_CS#46	MEM_DM46
MEM_A47	MEM_CK23P	MEM_CS#47	MEM_DM47
MEM_A48	MEM_CK24N	MEM_CS#48	MEM_DM48
MEM_A49	MEM_CK24P	MEM_CS#49	MEM_DM49
MEM_A50	MEM_CK25N	MEM_CS#50	MEM_DM50
MEM_A51	MEM_CK25P	MEM_CS#51	MEM_DM51
MEM_A52	MEM_CK26N	MEM_CS#52	MEM_DM52
MEM_A53	MEM_CK26P	MEM_CS#53	MEM_DM53
MEM_A54	MEM_CK27N	MEM_CS#54	MEM_DM54
MEM_A55	MEM_CK27P	MEM_CS#55	MEM_DM55
MEM_A56	MEM_CK28N	MEM_CS#56	MEM_DM56
MEM_A57	MEM_CK28P	MEM_CS#57	MEM_DM57
MEM_A58	MEM_CK29N	MEM_CS#58	MEM_DM58
MEM_A59	MEM_CK29P	MEM_CS#59	MEM_DM59
MEM_A60	MEM_CK30N	MEM_CS#60	MEM_DM60
MEM_A61	MEM_CK30P	MEM_CS#61	MEM_DM61
MEM_A62	MEM_CK31N	MEM_CS#62	MEM_DM62
MEM_A63	MEM_CK31P	MEM_CS#63	MEM_DM63
MEM_A64	MEM_CK32N	MEM_CS#64	MEM_DM64
MEM_A65	MEM_CK32P	MEM_CS#65	MEM_DM65
MEM_A66	MEM_CK33N	MEM_CS#66	MEM_DM66
MEM_A67	MEM_CK33P	MEM_CS#67	MEM_DM67
MEM_A68	MEM_CK34N	MEM_CS#68	MEM_DM68
MEM_A69	MEM_CK34P	MEM_CS#69	MEM_DM69
MEM_A70	MEM_CK35N	MEM_CS#70	MEM_DM70
MEM_A71	MEM_CK35P	MEM_CS#71	MEM_DM71
MEM_A72	MEM_CK36N	MEM_CS#72	MEM_DM72
MEM_A73	MEM_CK36P	MEM_CS#73	MEM_DM73
MEM_A74	MEM_CK37N	MEM_CS#74	MEM_DM74
MEM_A75	MEM_CK37P	MEM_CS#75	MEM_DM75
MEM_A76	MEM_CK38N	MEM_CS#76	MEM_DM76
MEM_A77	MEM_CK38P	MEM_CS#77	MEM_DM77
MEM_A78	MEM_CK39N	MEM_CS#78	MEM_DM78
MEM_A79	MEM_CK39P	MEM_CS#79	MEM_DM79
MEM_A80	MEM_CK40N	MEM_CS#80	MEM_DM80
MEM_A81	MEM_CK40P	MEM_CS#81	MEM_DM81
MEM_A82	MEM_CK41N	MEM_CS#82	MEM_DM82
MEM_A83	MEM_CK41P	MEM_CS#83	MEM_DM83
MEM_A84	MEM_CK42N	MEM_CS#84	MEM_DM84
MEM_A85	MEM_CK42P	MEM_CS#85	MEM_DM85
MEM_A86	MEM_CK43N	MEM_CS#86	MEM_DM86
MEM_A87	MEM_CK43P	MEM_CS#87	MEM_DM87
MEM_A88	MEM_CK44N	MEM_CS#88	MEM_DM88
MEM_A89	MEM_CK44P	MEM_CS#89	MEM_DM89
MEM_A90	MEM_CK45N	MEM_CS#90	MEM_DM90
MEM_A91	MEM_CK45P	MEM_CS#91	MEM_DM91
MEM_A92	MEM_CK46N	MEM_CS#92	MEM_DM92
MEM_A93	MEM_CK46P	MEM_CS#93	MEM_DM93
MEM_A94	MEM_CK47N	MEM_CS#94	MEM_DM94
MEM_A95	MEM_CK47P	MEM_CS#95	MEM_DM95
MEM_A96	MEM_CK48N	MEM_CS#96	MEM_DM96
MEM_A97	MEM_CK48P	MEM_CS#97	MEM_DM97
MEM_A98	MEM_CK49N	MEM_CS#98	MEM_DM98
MEM_A99	MEM_CK49P	MEM_CS#99	MEM_DM99
MEM_A100	MEM_CK50N	MEM_CS#100	MEM_DM100
MEM_A101	MEM_CK50P	MEM_CS#101	MEM_DM101
MEM_A102	MEM_CK51N	MEM_CS#102	MEM_DM102
MEM_A103	MEM_CK51P	MEM_CS#103	MEM_DM103
MEM_A104	MEM_CK52N	MEM_CS#104	MEM_DM104
MEM_A105	MEM_CK52P	MEM_CS#105	MEM_DM105
MEM_A106	MEM_CK53N	MEM_CS#106	MEM_DM106
MEM_A107	MEM_CK53P	MEM_CS#107	MEM_DM107
MEM_A108	MEM_CK54N	MEM_CS#108	MEM_DM108
MEM_A109	MEM_CK54P	MEM_CS#109	MEM_DM109
MEM_A110	MEM_CK55N	MEM_CS#110	MEM_DM110
MEM_A111	MEM_CK55P	MEM_CS#111	MEM_DM111
MEM_A112	MEM_CK56N	MEM_CS#112	MEM_DM112
MEM_A113	MEM_CK56P	MEM_CS#113	MEM_DM113
MEM_A114	MEM_CK57N	MEM_CS#114	MEM_DM114
MEM_A115	MEM_CK57P	MEM_CS#115	MEM_DM115
MEM_A116	MEM_CK58N	MEM_CS#116	MEM_DM116
MEM_A117	MEM_CK58P	MEM_CS#117	MEM_DM117
MEM_A118	MEM_CK59N	MEM_CS#118	MEM_DM118
MEM_A119	MEM_CK59P	MEM_CS#119	MEM_DM119
MEM_A120	MEM_CK60N	MEM_CS#120	MEM_DM120
MEM_A121	MEM_CK60P	MEM_CS#121	MEM_DM121
MEM_A122	MEM_CK61N	MEM_CS#122	MEM_DM122
MEM_A123	MEM_CK61P	MEM_CS#123	MEM_DM123
MEM_A124	MEM_CK62N	MEM_CS#124	MEM_DM124
MEM_A125	MEM_CK62P	MEM_CS#125	MEM_DM125
MEM_A126	MEM_CK63N	MEM_CS#126	MEM_DM126
MEM_A127	MEM_CK63P	MEM_CS#127	MEM_DM127
MEM_A128	MEM_CK64N	MEM_CS#128	MEM_DM128
MEM_A129	MEM_CK64P	MEM_CS#129	MEM_DM129
MEM_A130	MEM_CK65N	MEM_CS#130	MEM_DM130
MEM_A131	MEM_CK65P	MEM_CS#131	MEM_DM131
MEM_A132	MEM_CK66N	MEM_CS#132	MEM_DM132
MEM_A133	MEM_CK66P	MEM_CS#133	MEM_DM133
MEM_A134	MEM_CK67N	MEM_CS#134	MEM_DM134
MEM_A135	MEM_CK67P	MEM_CS#135	MEM_DM135
MEM_A136	MEM_CK68N	MEM_CS#136	MEM_DM136
MEM_A137	MEM_CK68P	MEM_CS#137	MEM_DM137
MEM_A138	MEM_CK69N	MEM_CS#138	MEM_DM138
MEM_A139	MEM_CK69P	MEM_CS#139	MEM_DM139
MEM_A140	MEM_CK70N	MEM_CS#140	MEM_DM140
MEM_A141	MEM_CK70P	MEM_CS#141	MEM_DM141
MEM_A142	MEM_CK71N	MEM_CS#142	MEM_DM142
MEM_A143	MEM_CK71P	MEM_CS#143	MEM_DM143
MEM_A144	MEM_CK72N	MEM_CS#144	MEM_DM144
MEM_A145	MEM_CK72P	MEM_CS#145	MEM_DM145
MEM_A146	MEM_CK73N	MEM_CS#146	MEM_DM146
MEM_A147	MEM_CK73P	MEM_CS#147	MEM_DM147
MEM_A148	MEM_CK74N	MEM_CS#148	MEM_DM148
MEM_A149	MEM_CK74P	MEM_CS#149	MEM_DM149
MEM_A150	MEM_CK75N	MEM_CS#150	MEM_DM150
MEM_A151	MEM_CK75P	MEM_CS#151	MEM_DM151
MEM_A152	MEM_CK76N	MEM_CS#152	MEM_DM152
MEM_A153	MEM_CK76P	MEM_CS#153	MEM_DM153
MEM_A154	MEM_CK77N	MEM_CS#154	MEM_DM154
MEM_A155	MEM_CK77P	MEM_CS#155	MEM_DM155
MEM_A156	MEM_CK78N	MEM_CS#156	MEM_DM156
MEM_A157	MEM_CK78P	MEM_CS#157	MEM_DM157
MEM_A158	MEM_CK79N	MEM_CS#158	MEM_DM158
MEM_A159	MEM_CK79P	MEM_CS#159	MEM_DM159
MEM_A160	MEM_CK80N	MEM_CS#160	MEM_DM160
MEM_A161	MEM_CK80P	MEM_CS#161	MEM_DM161
MEM_A162	MEM_CK81N	MEM_CS#162	MEM_DM162
MEM_A163	MEM_CK81P	MEM_CS#163	MEM_DM163
MEM_A164	MEM_CK82N	MEM_CS#164	MEM_DM164
MEM_A165	MEM_CK82P	MEM_CS#165	MEM_DM165
MEM_A166	MEM_CK83N	MEM_CS#166	MEM_DM166
MEM_A167	MEM_CK83P	MEM_CS#167	MEM_DM167
MEM_A168	MEM_CK84N	MEM_CS#168	MEM_DM168
MEM_A169	MEM_CK84P	MEM_CS#169	MEM_DM169
MEM_A170	MEM_CK85N	MEM_CS#170	MEM_DM170
MEM_A171	MEM_CK85P	MEM_CS#171	MEM_DM171
MEM_A172	MEM_CK86N	MEM_CS#172	MEM_DM172
MEM_A173	MEM_CK86P	MEM_CS#173	MEM_DM173
MEM_A174	MEM_CK87N	MEM_CS#174	MEM_DM174
MEM_A175	MEM_CK87P	MEM_CS#175	MEM_DM175
MEM_A176	MEM_CK88N	MEM_CS#176	MEM_DM176
MEM_A177	MEM_CK88P	MEM_CS#177	MEM_DM177
MEM_A178	MEM_CK89N	MEM_CS#178	MEM_DM178
MEM_A179	MEM_CK89P	MEM_CS#179	MEM_DM179
MEM_A180	MEM_CK90N	MEM_CS#180	MEM_DM180
MEM_A181	MEM_CK90P	MEM_CS#181	MEM_DM181
MEM_A182	MEM_CK91N	MEM_CS#182	MEM_DM182
MEM_A183	MEM_CK91P	MEM_CS#183	MEM_DM183
MEM_A184	MEM_CK92N	MEM_CS#184	MEM_DM184
MEM_A185	MEM_CK92P	MEM_CS#185	MEM_DM185
MEM_A186	MEM_CK93N	MEM_CS#186	MEM_DM186
MEM_A187	MEM_CK93P	MEM_CS#187	MEM_DM187
MEM_A188	MEM_CK94N	MEM_CS#188	MEM_DM188
MEM_A189	MEM_CK94P	MEM_CS#189	MEM_DM189
MEM_A190	MEM_CK95N	MEM_CS#190	MEM_DM190
MEM_A191	MEM_CK95P	MEM_CS#191	MEM_DM191
MEM_A192	MEM_CK96N	MEM_CS#192	MEM_DM192
MEM_A193	MEM_CK96P	MEM_CS#193	MEM_DM193
MEM_A194	MEM_CK97N	MEM_CS#194	MEM_DM194
MEM_A195	MEM_CK97P	MEM_CS#195	MEM_DM195
MEM_A196	MEM_CK98N	MEM_CS#196	MEM_DM196
MEM_A197	MEM_CK98P	MEM_CS#197	MEM_DM197
MEM_A198	MEM_CK99N	MEM_CS#198	MEM_DM198
MEM_A199	MEM_CK99P	MEM_CS#199	MEM_DM199
MEM_A200	MEM_CK00N	MEM_CS#200	MEM_DM200

RC410MD MEMORY I/F



2006/01/23

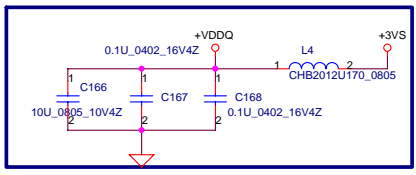
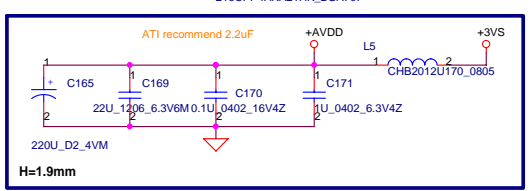
2006/01/23



PART 5 OF 6

PART 6 OF 6

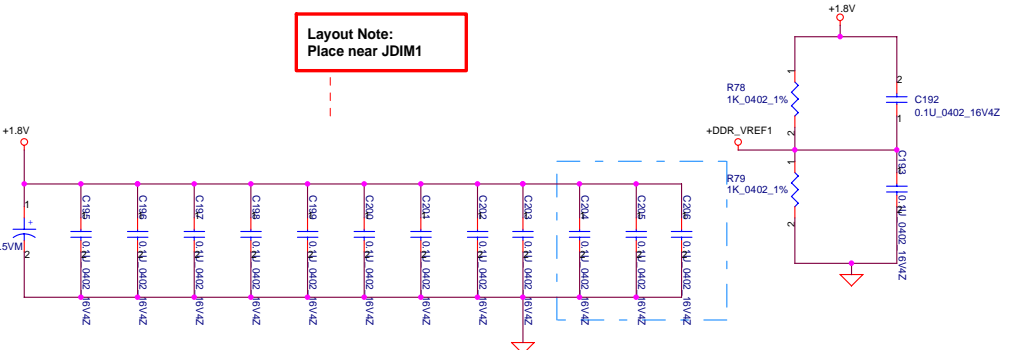
RC410MD GROUND



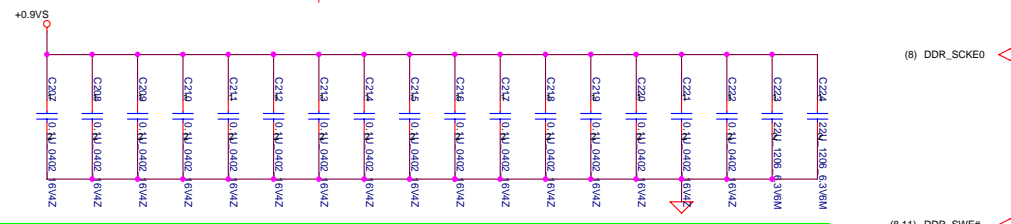
Place L close to Ball AB26  
Place C between Ball AB26,AA27

Security Classification	Compal Secret Data		Title	
Issued Date	2005/11/01	Deciphered Date	2006/11/30	RC410MB PWR/GND
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				
Document Number	HCL51-LA-3211P		Rev	0.4
Date	星期二, 四月 11, 2006	Sheet	9	of 43

Layout Note:  
Place near JDIM1

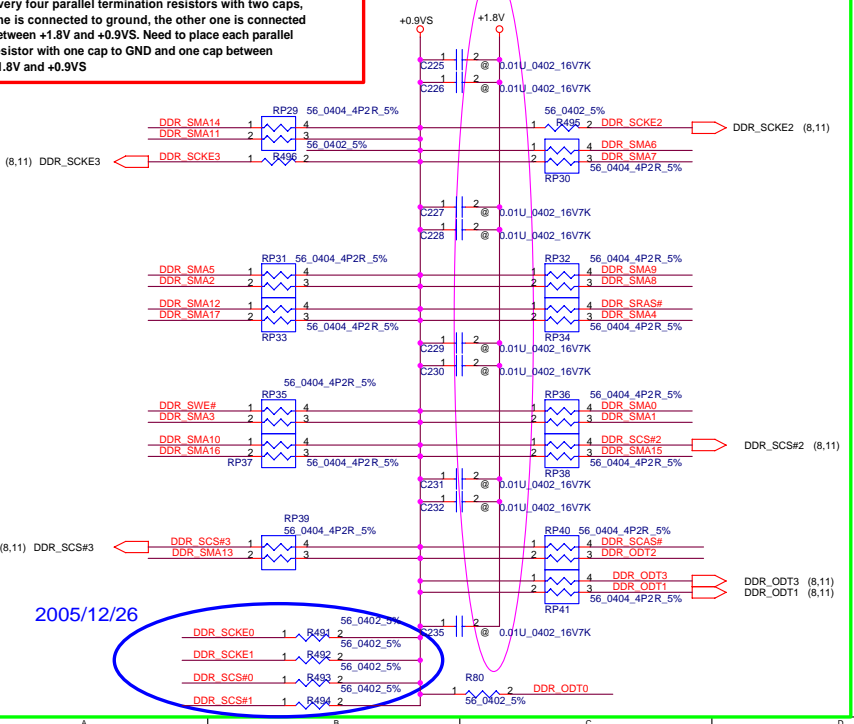


Layout Note:  
Place one cap close to every 2 pullup resistors terminated to V\_DDR\_MCH\_REF



Layout Note:  
Every four parallel termination resistors with two caps, one is connected to ground, the other one is connected between +1.8V and +0.9VS. Need to place each parallel resistor with one cap to GND and one cap between +1.8V and +0.9VS

2006/01/23



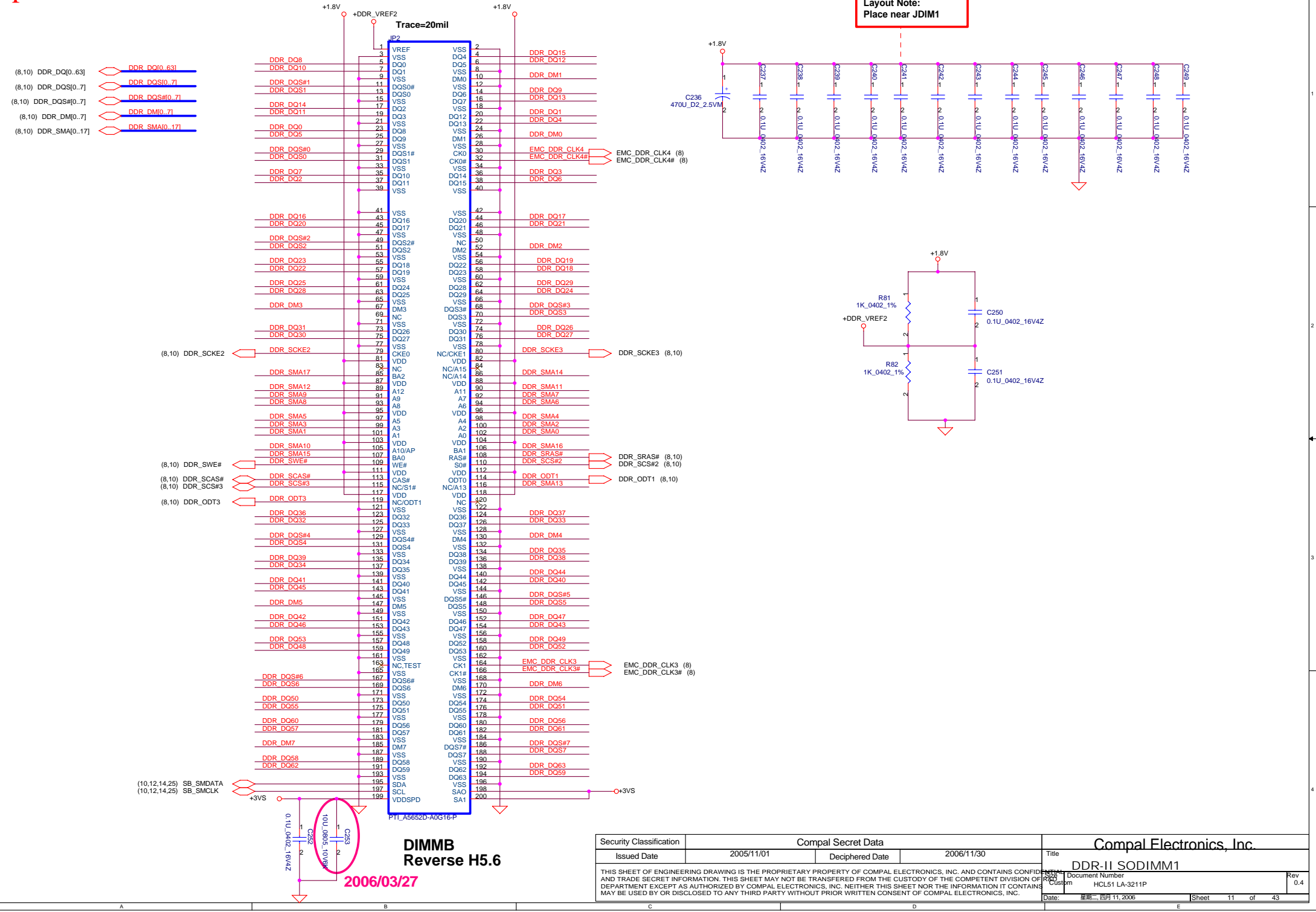
2005/12/26



DIMM Reverse H9.2

2006/03/27

Security Classification		Compal Secret Data		Title	
Issued Date	2005/11/01	Deciphered Date	2006/11/30	DDR11-SODIMM2	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Size	Document Number
				Custom	HCL51 LA-3211P
				Date:	Rev
				2006年 二月 11, 2006	0.4



Layout Note:  
Place near JDIM1

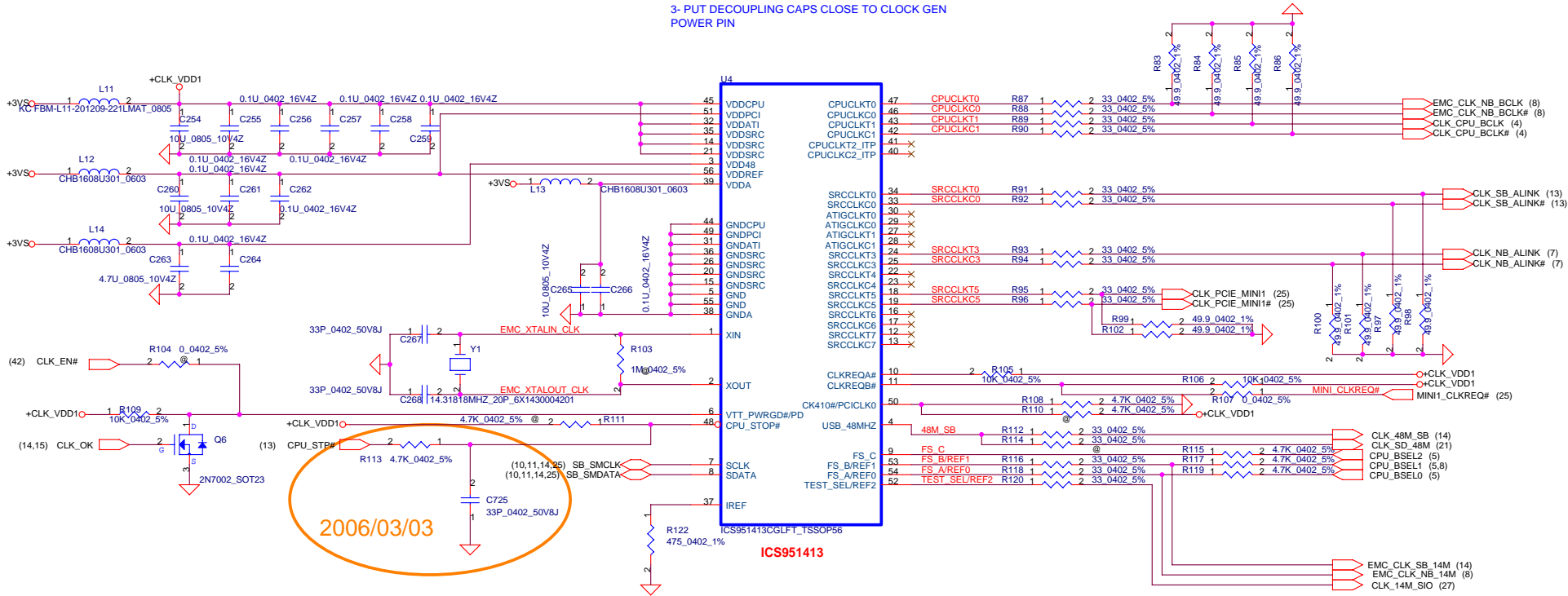
DIMMB  
Reverse H5.6

2006/03/27

Security Classification	Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2005/11/01	Deciphered Date	2006/11/30	Title
				DDR-II SODIMM1
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Rev 0.4
Date: 星期二, 四月 11, 2006				Sheet 11 of 43

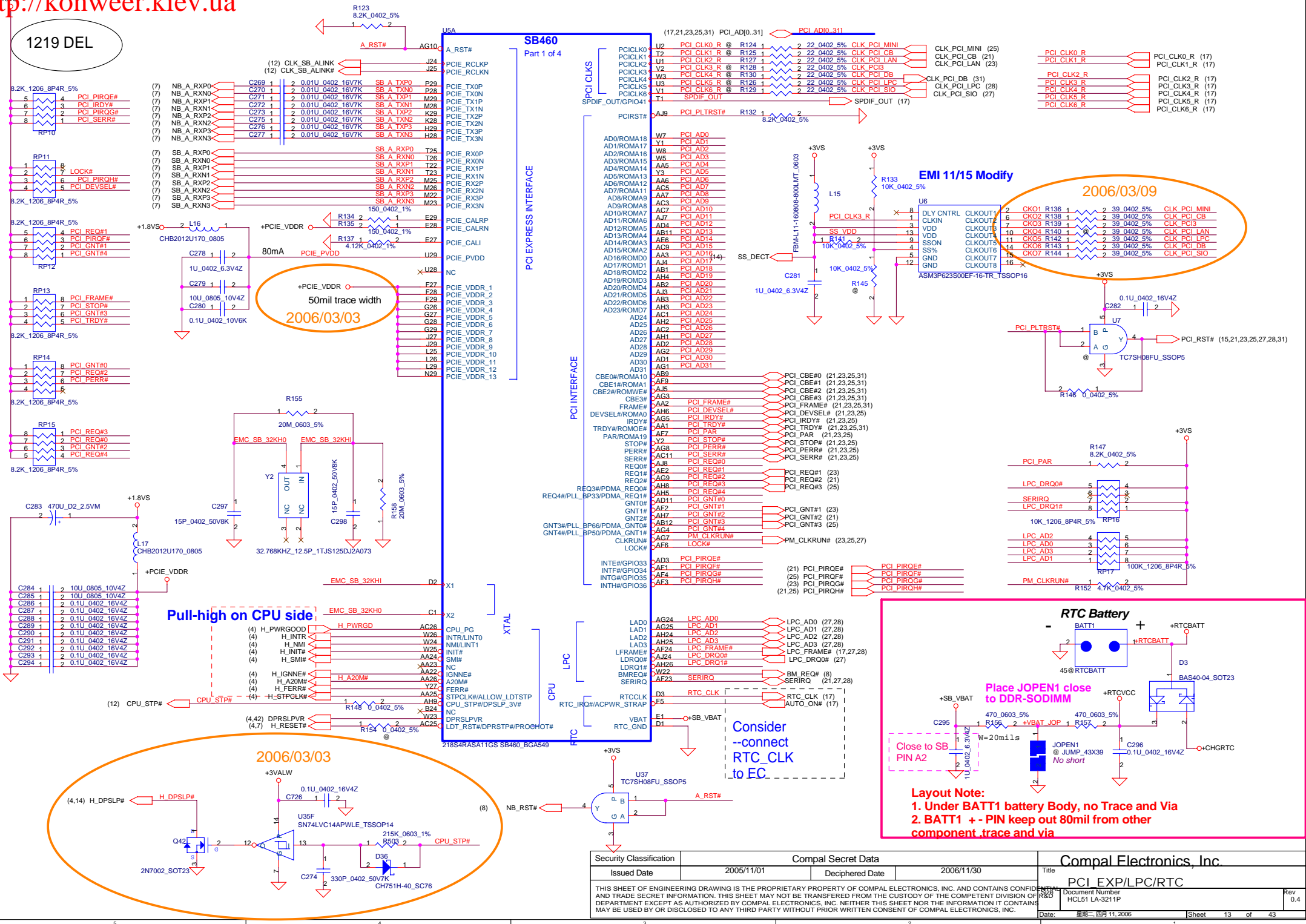
# Clock Generator

- 1- PLACE ALL THE SERIES TERMINATION RESISTORS AS CLOSE TO CLOCK GEN AS POSSIBLE
- 2- ROUTE ALL CPUCLK#, NBCLK#, ITPCLK# AND SCR/# ,AS DIFFERENT PAIR RULE
- 3- PUT DECOUPLING CAPS CLOSE TO CLOCK GEN POWER PIN

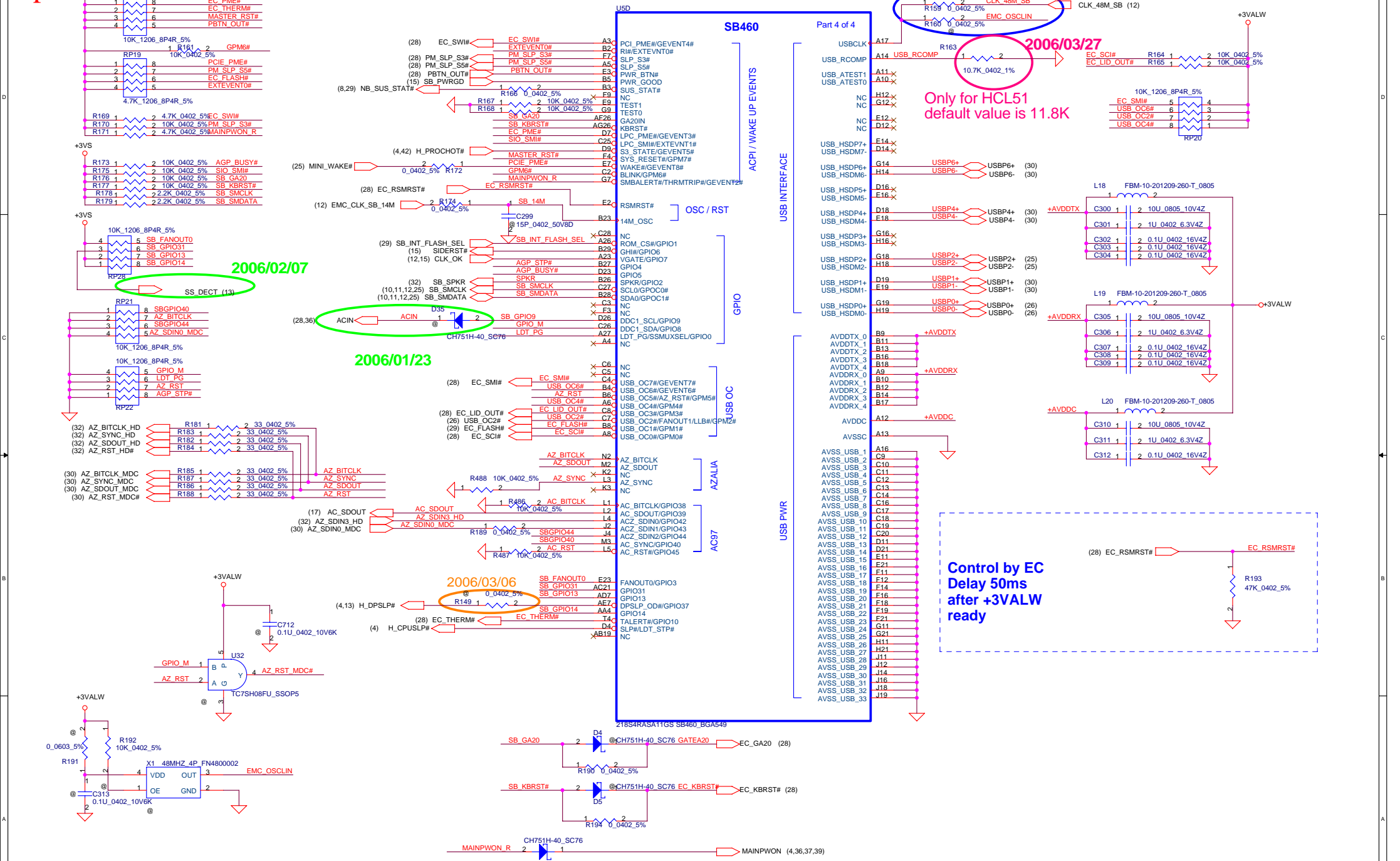


FS_C	FS_B	FS_A	CPU	SRC	PCI	REF	USB
1	0	1	100.00	100.00	33.33	14.318	48.000
0	0	1	133.33	100.00	33.33	14.318	48.000
0	1	1	166.66	100.00	33.33	14.318	48.000

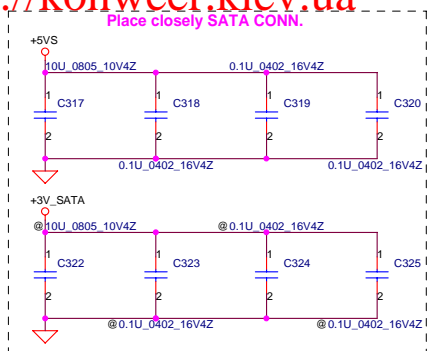
1219 DEL



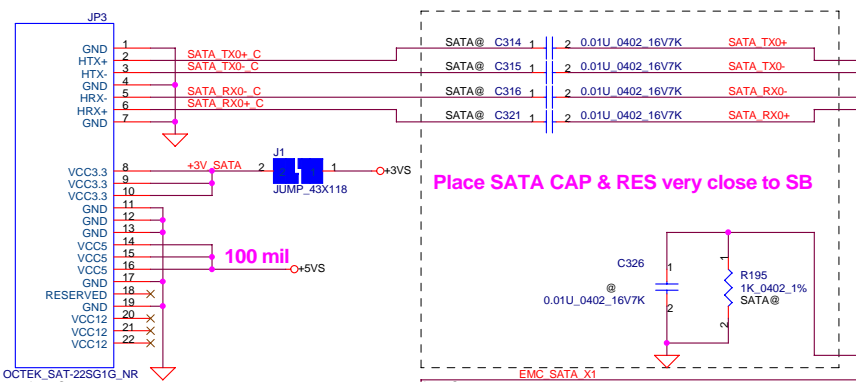
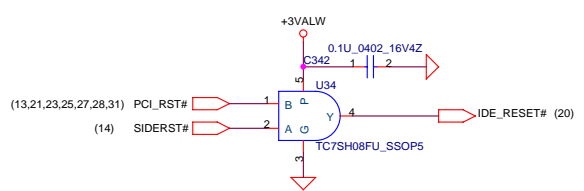
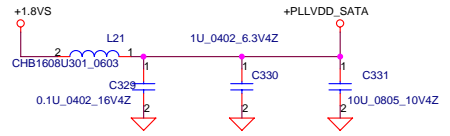
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2005/11/01	Deciphered Date	2006/11/30	Title	PCI_EXP/LPC/RTC
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.					
Document Number	HCL51 LA-3211P		Rev	0.4	
Date	星期二, 四月 11, 2006	Sheet	13	of 43	



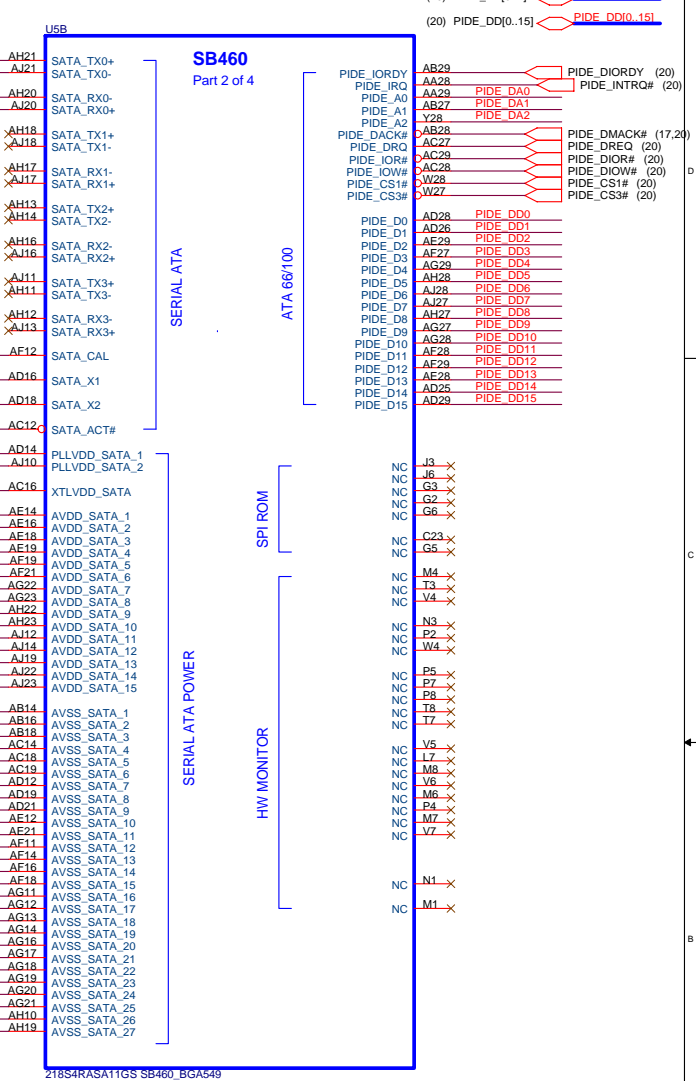
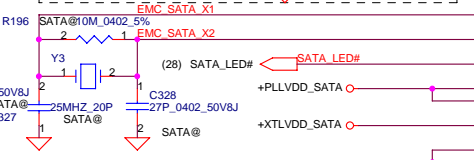
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2005/11/01	Deciphered Date	2006/11/30	Title	
				SB450 USB/ACPI/AC97/GPIO	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF COMPAL ELECTRONICS, INC. WITHOUT THE WRITTEN PERMISSION OF THE COMPETENT DIVISION OF COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number	Rev
				HCL51 LA-3211P	0.4
Date:	星期二, 四月 11, 2006	Sheet	14	of 43	



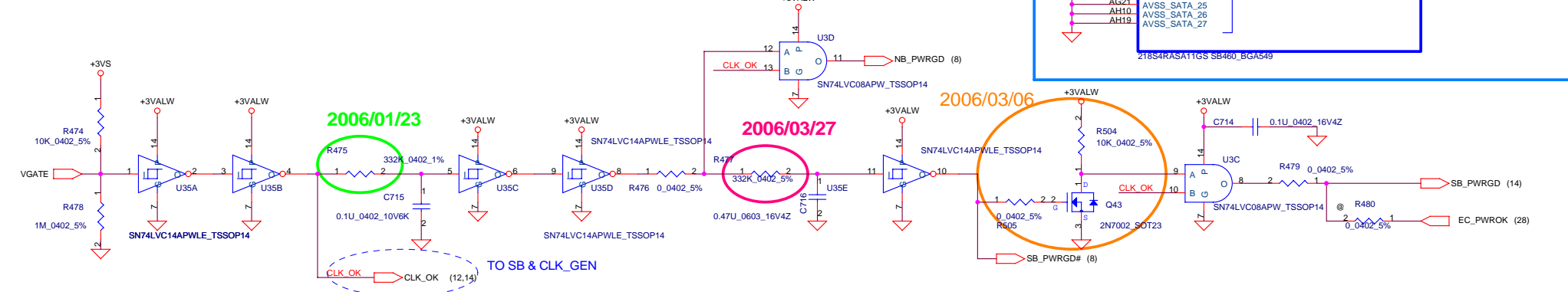
**SATA HDD CONNECTOR**



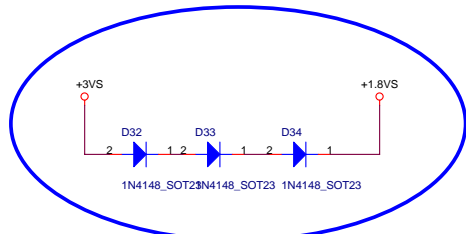
Place SATA CAP & RES very close to SB



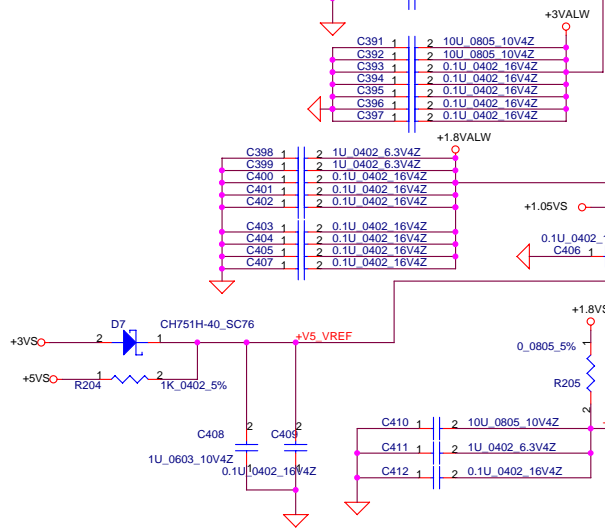
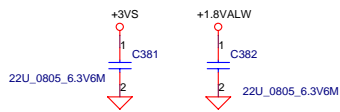
**NB & SB POWER GOOD**



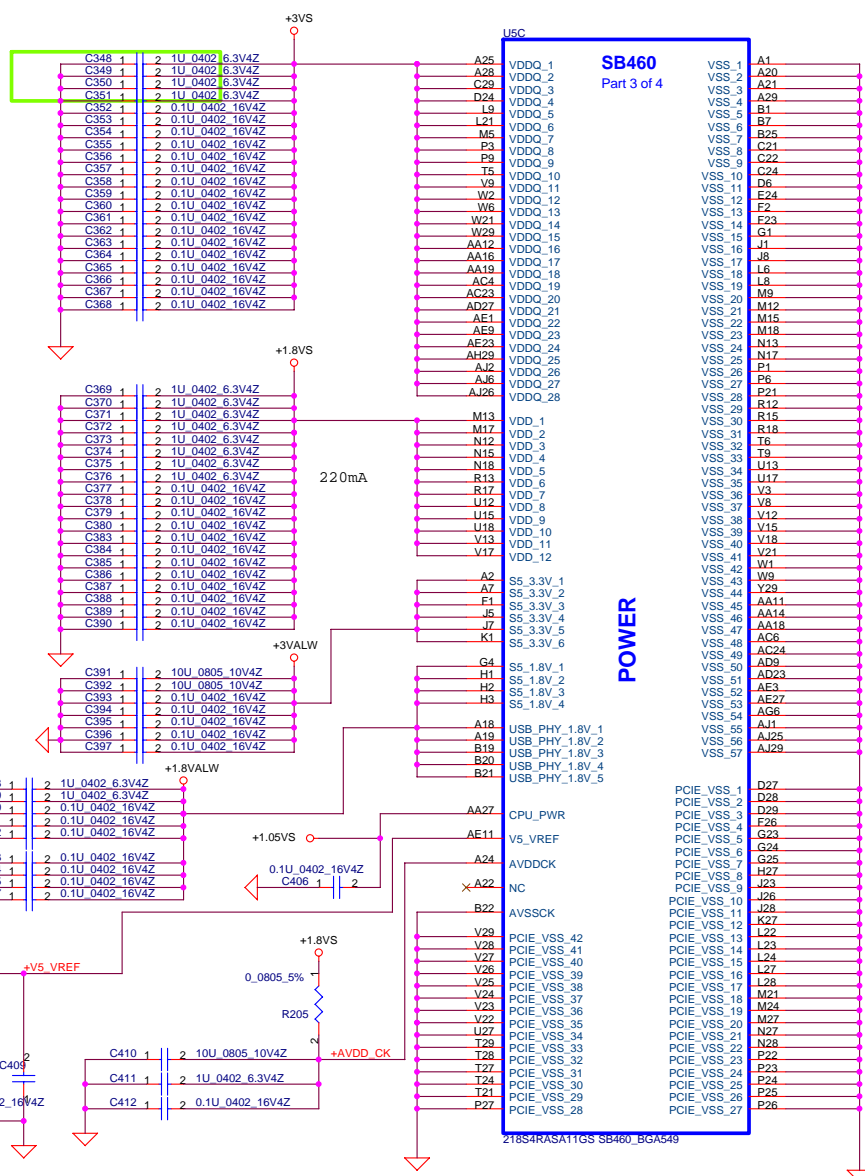
Security Classification	Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2005/11/01	Deciphered Date	2006/11/30	Title
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				SB450 IDE/SATA
Document Number			Rev	
HCL51 LA-3211P			0.4	
Date:	星期二, 四月 11, 2006	Sheet	15	of 43



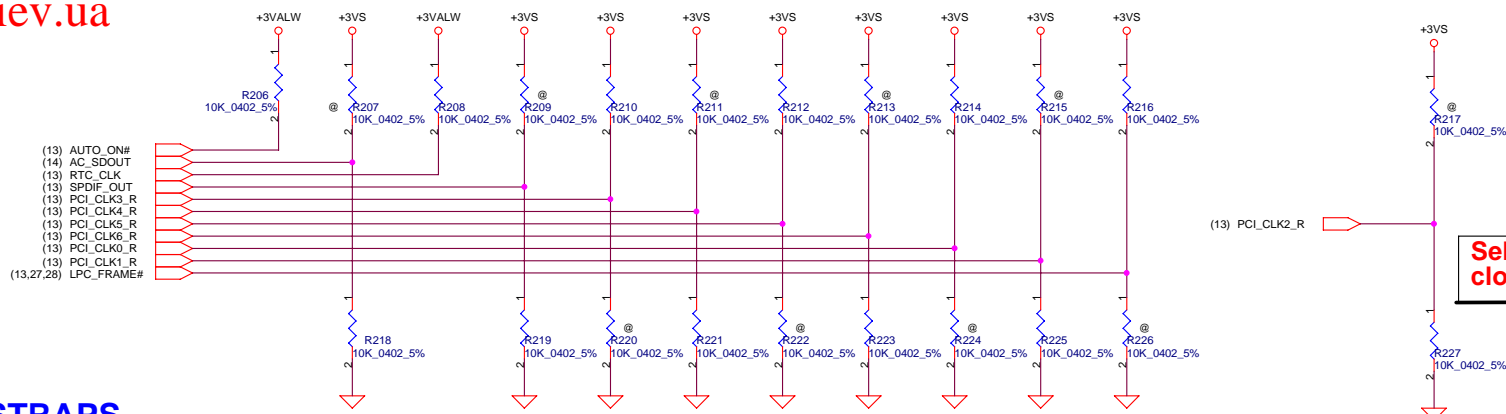
2005/12/21



**+V5 VREF (20mils)**  
**+AVDD\_CK(40mils)**



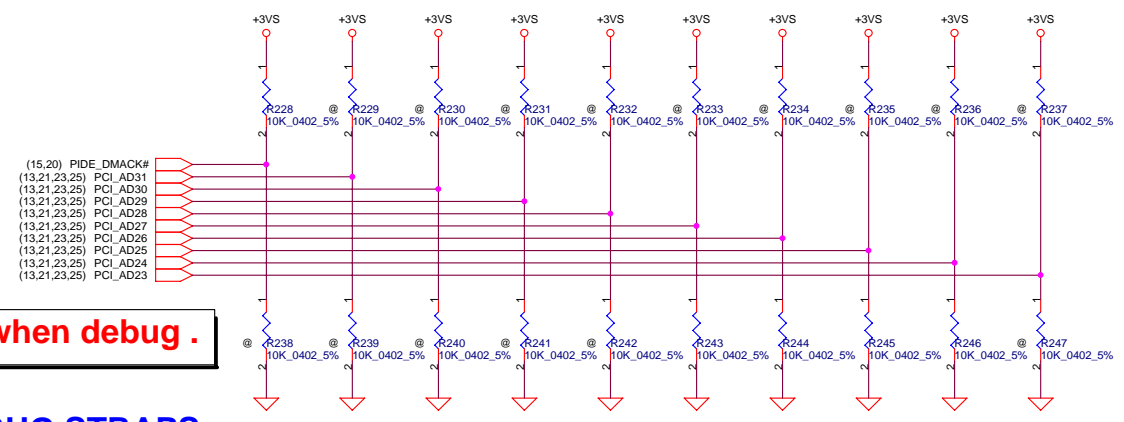
Security Classification	Compal Secret Data		Title	
Issued Date	2005/11/01	Deciphered Date	2006/11/30	SB460 POWER/GND
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Rev 0.4
Date:	星期二, 四月 11, 2006	Sheet	16	of 43



### REQUIRED STRAPS



	AUTO_ON#	AC97_SDOUT	RTC_CLK	SPDIF_OUT	CLK_PCI3	CLK_PCI4	CLK_PCI5	PCI_CLK6	PCI_CLK0	PCI_CLK1	LFRAME#	CLK_PCI2
<b>PULL HIGH</b>	MANUAL PWR ON <b>DEFAULT</b>	USE DEBUG STRAPS	INTERNAL RTC <b>DEFAULT</b>	PU for 48Mhz XTAL mode	USB PHY PWRDOWN DISABLE <b>DEFAULT</b>	Internal PLL	PCIE AUTO detect <b>DEFAULT</b>	CPU I/F = K8	ROM TYPE H,H = PCI ROM L,L = LPC ROM I <b>DEFAULT</b>		THERMTRIP# ENABLE <b>DEFAULT</b>	Crystal Pad
<b>PULL LOW</b>	AUTO PWR ON	IGNORE DEBUG STRAPS <b>DEFAULT</b>	EXTERNAL RTC (NOT SUPPORTED W/IT8712)	48m OSC mode <b>DEFAULT</b>	USB PHY PWRDOWN ENABLE	External Clock <b>DEFAULT</b>	Forcing PCIE to 2 lanes (debug only)	CPU I/F = P4 <b>DEFAULT</b>	L,L = FWH ROM		THERMTRIP# DISABLE	Clock input buffer <b>DEFAULT</b>



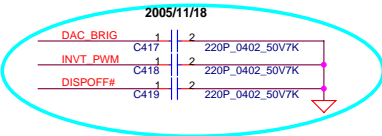
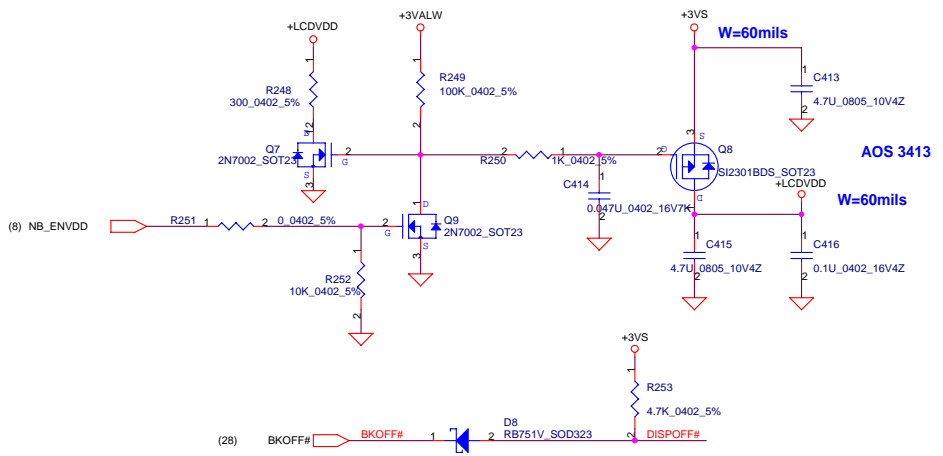
**Pop R634 when debug .**

### DEBUG STRAPS

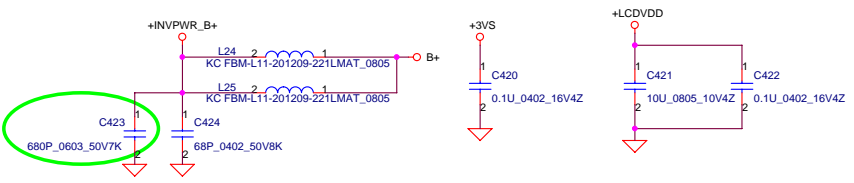
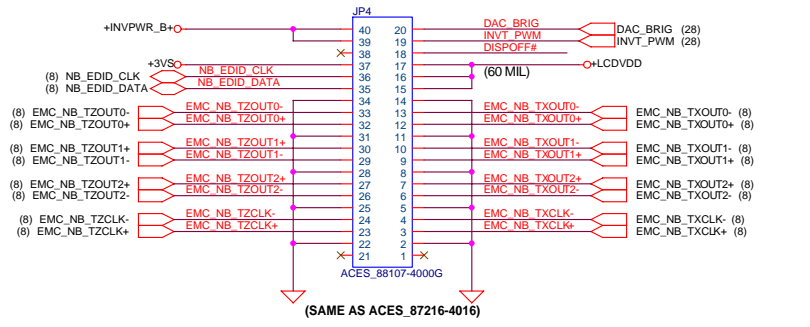
	PIDE_DMACK#	PCI_AD31	PCI_AD30	PCI_AD29	PCI_AD28	PCI_AD27	PCI_AD26	PCI_AD25	PCI_AD24	PCI_AD23
<b>PULL HIGH</b>	USE LONG RESET <b>DEFAULT</b>	Reserved	Reserved	Reserved	Reserved	BYPASS PCI PLL	BYPASS ACPI BCLK	BYPASS IDE PLL	USE EEPROM PCIE STRAPS	Reserved
<b>PULL LOW</b>	USE SHORT RESET					USE PCI PLL <b>DEFAULT</b>	USE ACPI BCLK <b>DEFAULT</b>	USE IDE PLL <b>DEFAULT</b>	USE DEFAULT PCIE STRAPS <b>DEFAULT</b>	

Security Classification	Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2005/11/01	Deciphered Date	2006/11/30	Title
				HARDWARE TRAP
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Rev 0.4
Date:	星期三, 四月 11, 2006	Sheet	17	of 43

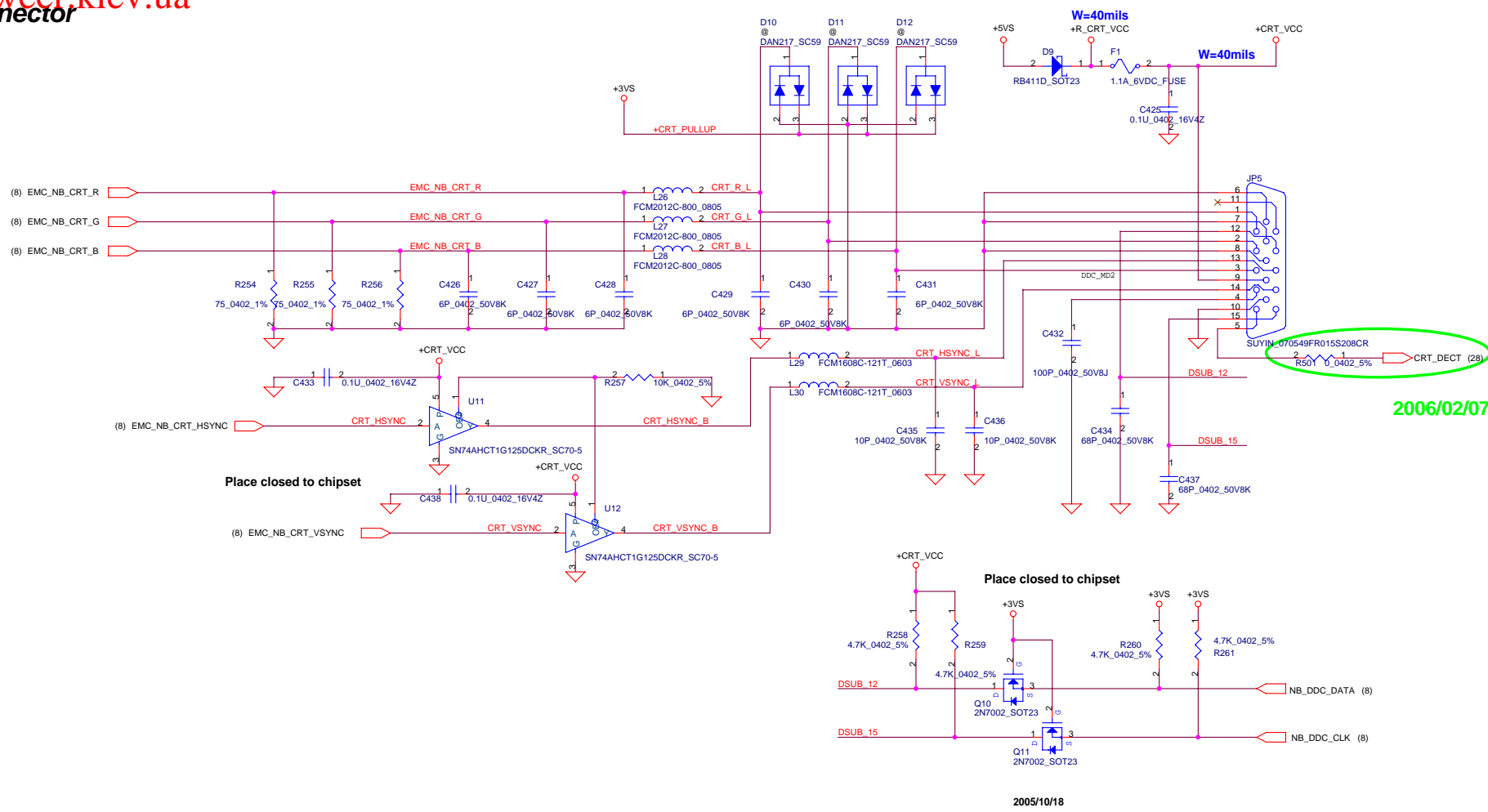
### LCD POWER CIRCUIT



### LCD/PANEL BD. Conn.



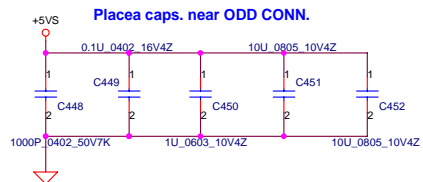
Security Classification	Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2005/06/20	Deciphered Date	2006/06/20	Title
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number
				HCL51 LA-3211P
				Rev 0.4
				Date: 星期二, 四月 11, 2006
				Sheet 18 of 43



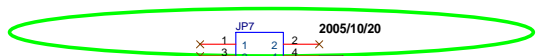
2006/02/07

2005/10/18

Security Classification	Compal Secret Data		Title	
Issued Date	2005/06/20	Deciphered Date	2006/06/20	Compal Electronics, Inc.
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number
				HCL51 LA-3211P
				Rev 0.4
				Date: 星期二, 四月 11, 2006
				Sheet 19 of 43

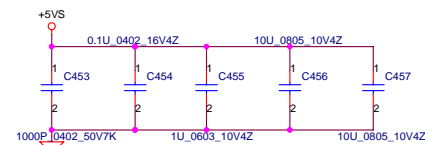
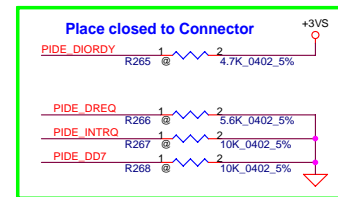


**ODD Conn.**

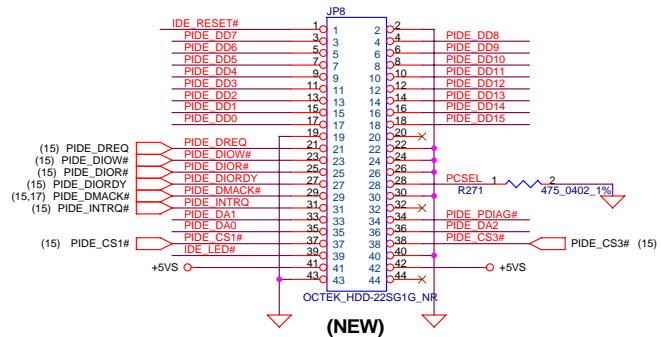


**(NEW)**

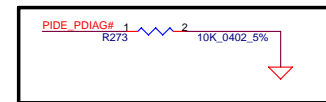
(15) PIDE\_DA[0..2]  $\hookrightarrow$  PIDE\_DA[0..2]  
 (15) PIDE\_DD[0..15]  $\hookrightarrow$  PIDE\_DD[0..15]



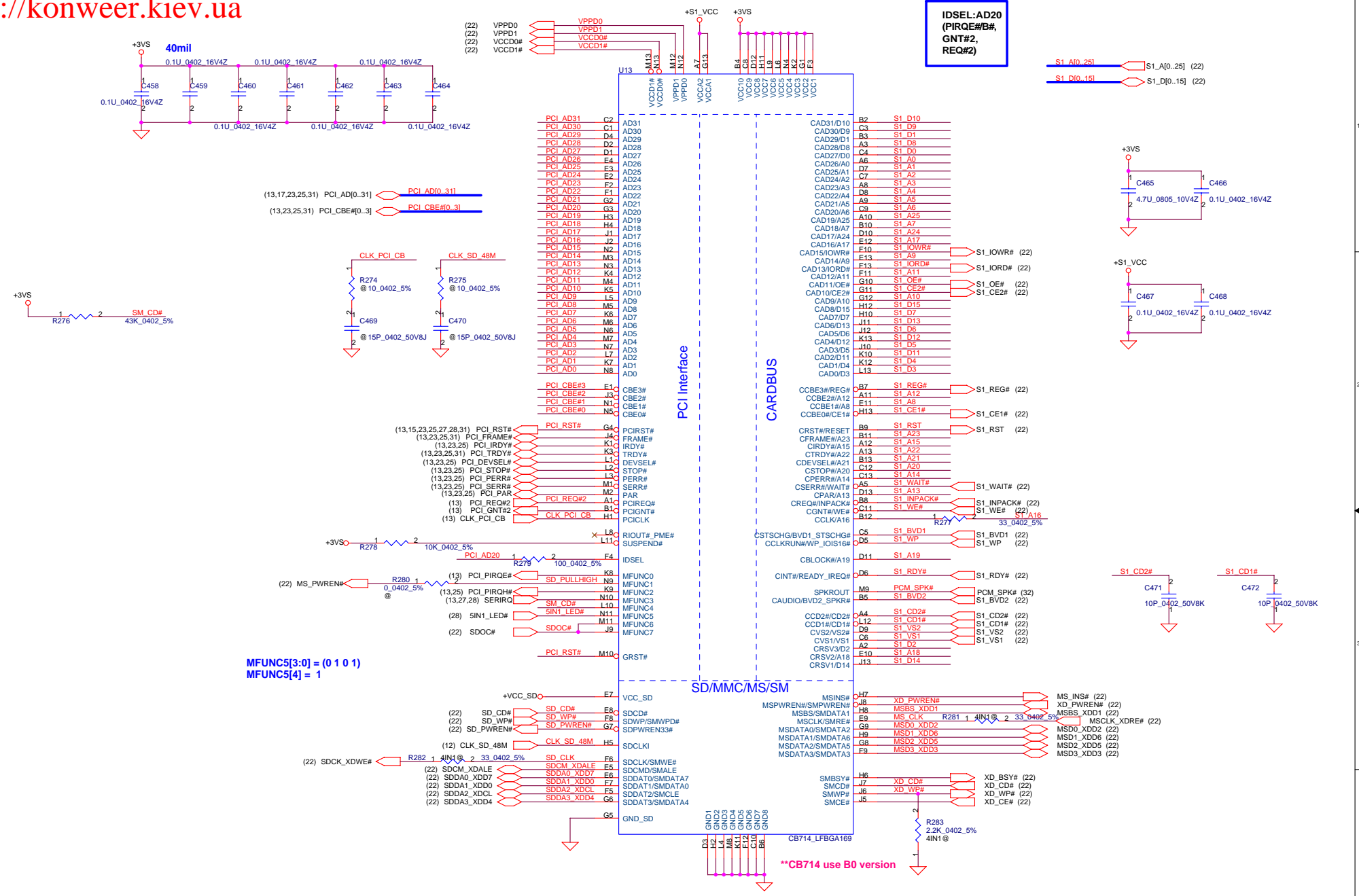
**PATA HDD Conn.**



**(NEW)**



Security Classification	Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2005/06/20	Deciphered Date	2006/06/20	Title
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.			Document Number	Rev
			HCL51 LA-3211P	0.4
			Date: 星期二, 四月 11, 2006	Sheet 20 of 43



Security Classification	Compal Secret Data		Title	
Issued Date	2005/06/20	Deciphered Date	2006/06/20	Compal Electronics, Inc.
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Cardbus Controller CB714 HCL51 LA-3211P Date: 星期二, 四月 11, 2006
				Rev 0.4 Sheet 21 of 43



(13,17,21,25,31) PCI\_AD[0..31] PCI\_AD[0..31]

**IDSEL:AD22  
(PIRQG#,  
GNT#1,  
REQ#1)**

- PCI AD0 104 AD0
- PCI AD1 103 AD1
- PCI AD2 102 AD2
- PCI AD3 97 AD3
- PCI AD4 96 AD4
- PCI AD5 95 AD5
- PCI AD6 93 AD6
- PCI AD7 92 AD7
- PCI AD8 90 AD8
- PCI AD9 89 AD9
- PCI AD10 87 AD10
- PCI AD11 86 AD11
- PCI AD12 85 AD12
- PCI AD13 83 AD13
- PCI AD14 82 AD14
- PCI AD15 79 AD15
- PCI AD16 58 AD16
- PCI AD17 57 AD17
- PCI AD18 55 AD18
- PCI AD19 53 AD19
- PCI AD20 53 AD20
- PCI AD21 50 AD21
- PCI AD22 49 AD22
- PCI AD23 47 AD23
- PCI AD24 43 AD24
- PCI AD25 42 AD25
- PCI AD26 40 AD26
- PCI AD27 37 AD27
- PCI AD28 37 AD28
- PCI AD29 36 AD29
- PCI AD30 34 AD30
- PCI AD31 33 AD31

- (13,21,25,31) PCI\_CBE#0 C/BE#0
- (13,21,25,31) PCI\_CBE#1 C/BE#1
- (13,21,25,31) PCI\_CBE#2 C/BE#2
- (13,21,25,31) PCI\_CBE#3 C/BE#3

PCI AD22 LAN IDSEL 46

- (13,21,25) PCI\_PAR 61 PAR
- (13,21,25,31) PCI\_FRAME# 63 FRAME#
- (13,21,25) PCI\_IRDY# 67 IRDY#
- (13,21,25,31) PCI\_TRDY# 68 TRDY#
- (13,21,25) PCI\_DEVSEL# 69 DEVSEL#
- (13,21,25) PCI\_STOP# 70 STOP#
- (13,21,25) PCI\_PERR# 75 PERR#
- (13,21,25) PCI\_SERR# 75 SERR#

- (13) PCI\_REQ#1 30 REQ#
- (13) PCI\_GNT#1 29 GNT#
- (13) PCI\_PIRQG# 25 INTA#
- (25,28) LAN\_PME# 31 PME#
- (13,15,21,25,27,28,31) PCI\_RST# 27 RST#

- (13) CLK\_PCI\_LAN 28 CLK
- (13,25,27) PM\_CLKRUN# 65 PM\_CLKRUN#

- 4 GND/VSS
- 17 GND/VSS
- 128 GND/VSS

- 21 GND/VSSPST
- 38 GND/VSSPST
- 51 GND/VSSPST
- 66 GND/VSSPST
- 81 GND/VSSPST
- 91 GND/VSSPST
- 101 GND/VSSPST
- 119 GND/VSSPST

- 35 GND
- 52 GND
- 80 GND
- 100 GND

- 126 VDD12
- 32 VDD12
- 54 VDD12
- 78 VDD12
- 99 VDD12

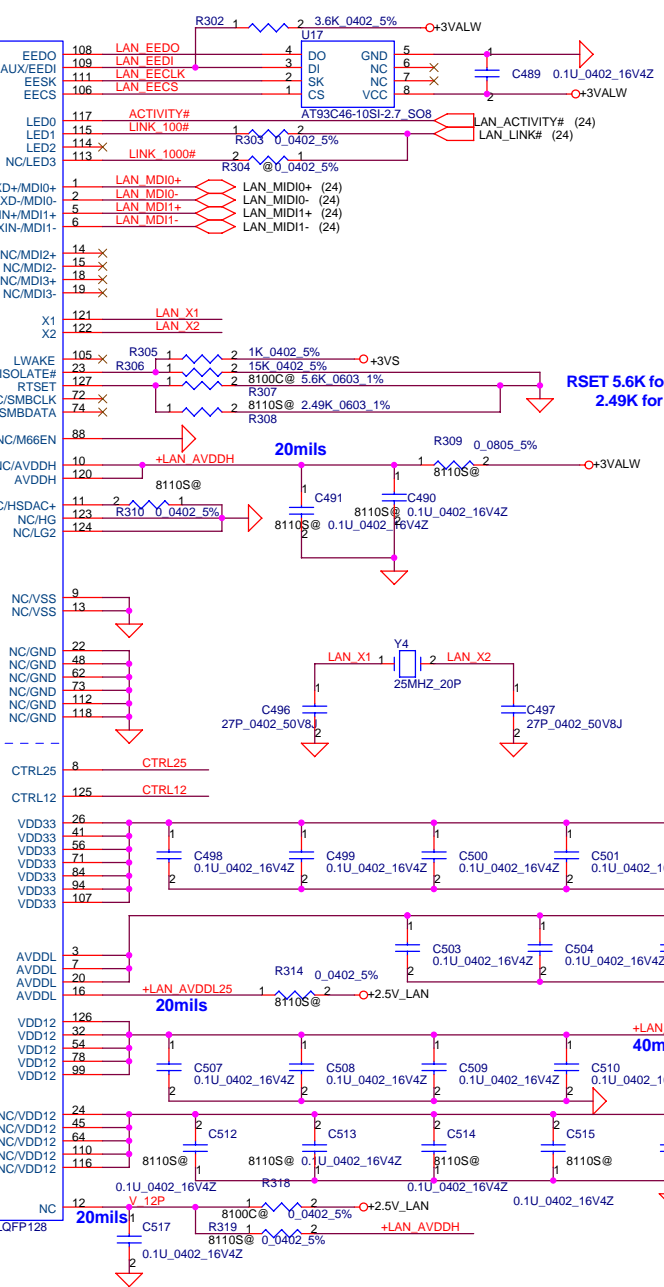
- 24 NC/VDD12
- 45 NC/VDD12
- 64 NC/VDD12
- 110 NC/VDD12
- 116 NC/VDD12

- 12 V12P

PCI I / F

LAN I / F

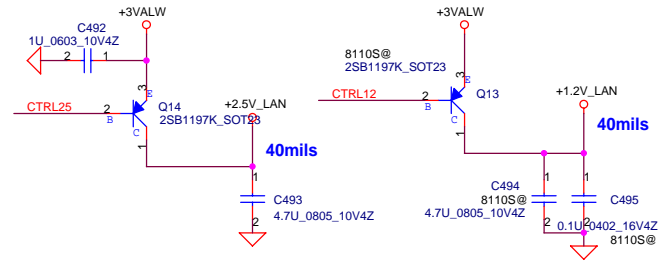
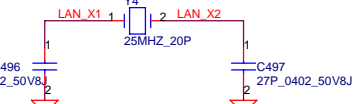
Power



PIN	8100CL(10/100 LAN)	8110SBL(10/100/1000 LAN)
RSET	5.6K	2.49K

BOM structure	8100CL(10/100 LAN)	8110SBL(10/100/1000 LAN)
8100C@	Stuff	No_Stuff
8110S@	No_Stuff	Stuff
@	No_Stuff	No_Stuff

RSET 5.6K for 8100CL  
2.49K for 8110S(B)



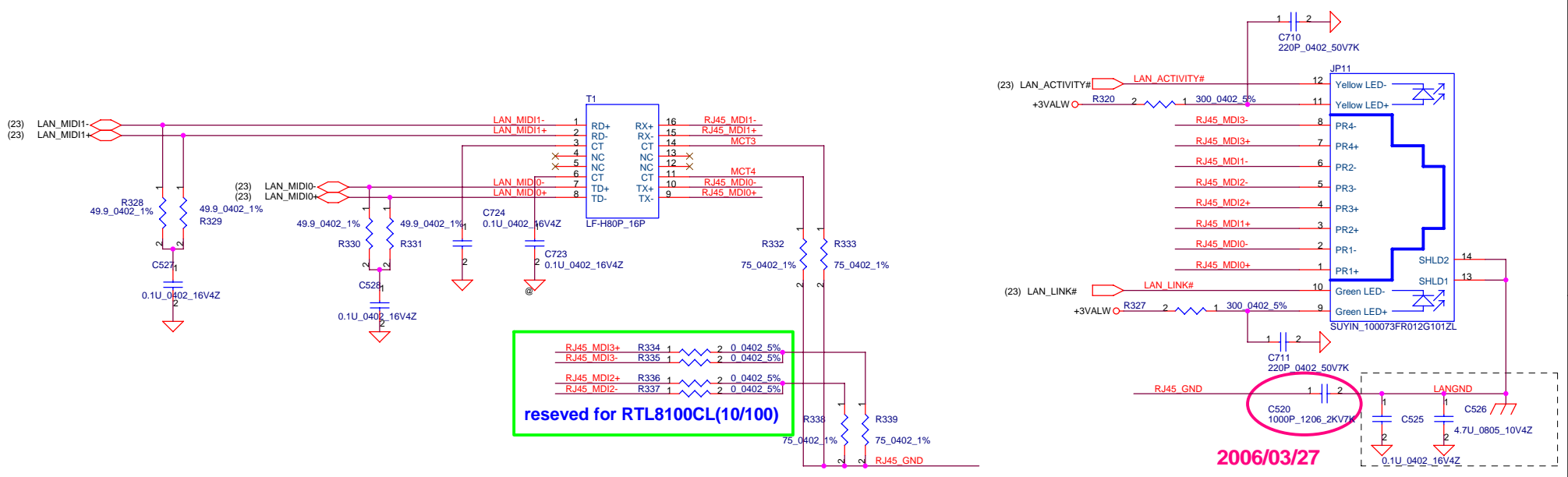
RTL8110SBL change to Ver.D

8100CL(10/100 LAN) P/N:SA081000310 ver.A.2

Security Classification	Compal Secret Data	
Issued Date	2005/07/29	Deciphered Date
		2006/07/29
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.		

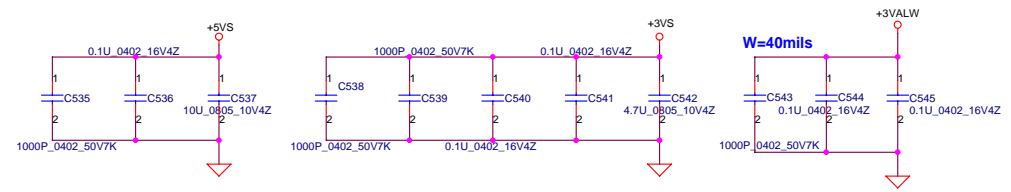
<b>Compal Electronics, Inc.</b>		
Title <b>LAN RTL8110SBL</b>		
Document Number	Rev	
<b>HCL51 LA-3211P</b>	0.4	
Date: 星期二, 四月 11, 2006	Sheet	23 of 43

# LAN RTL8100CL

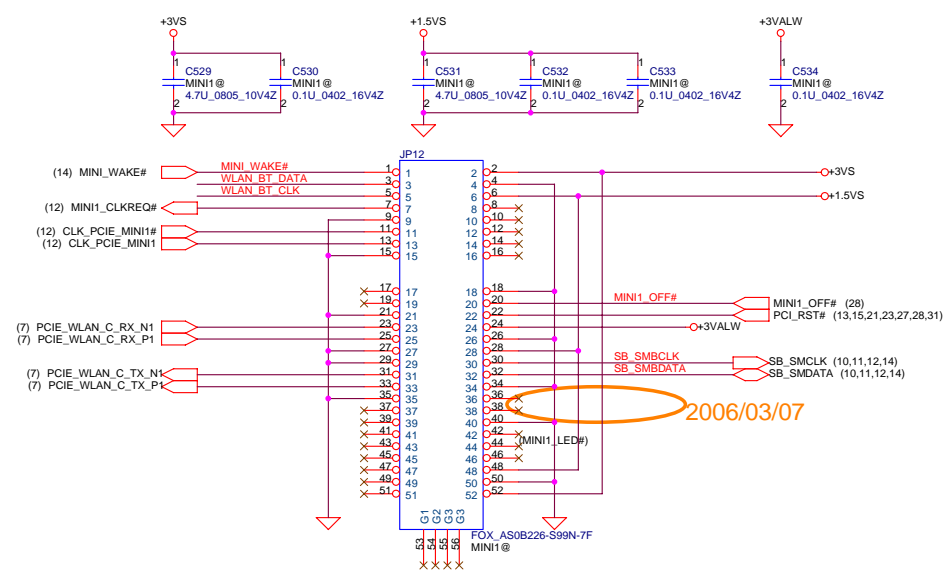
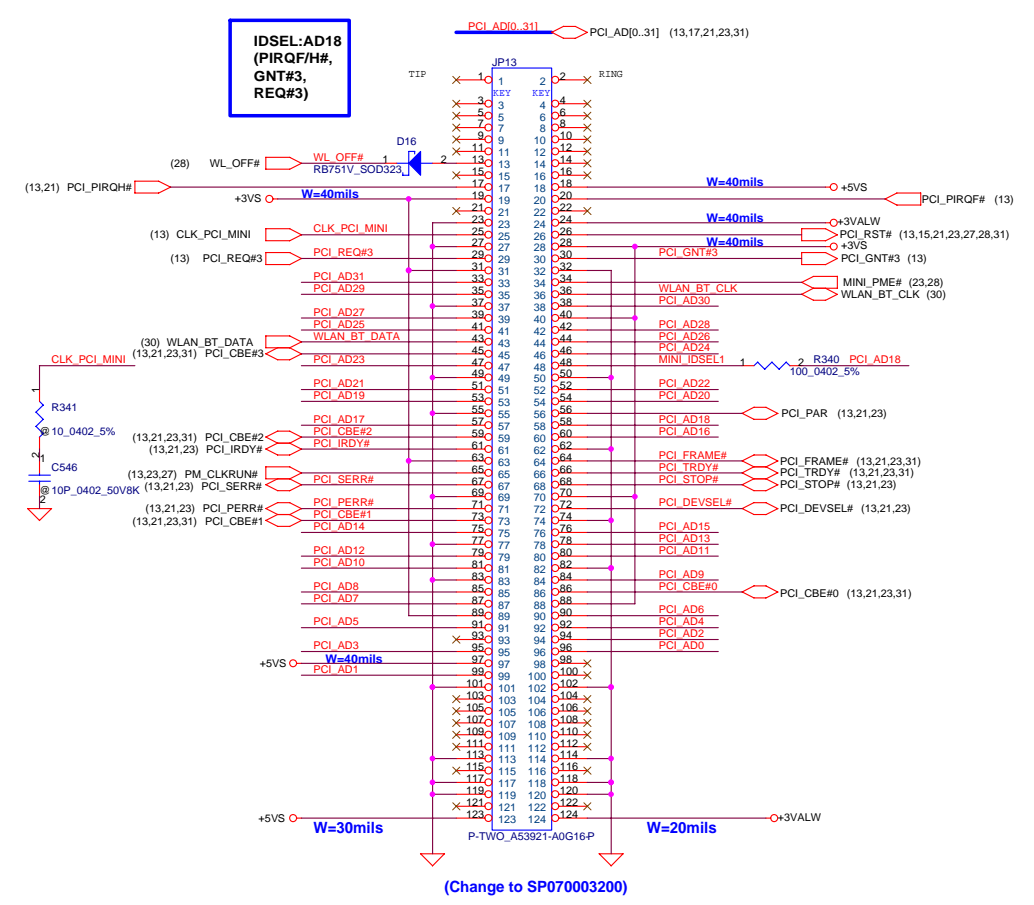


2006/03/27

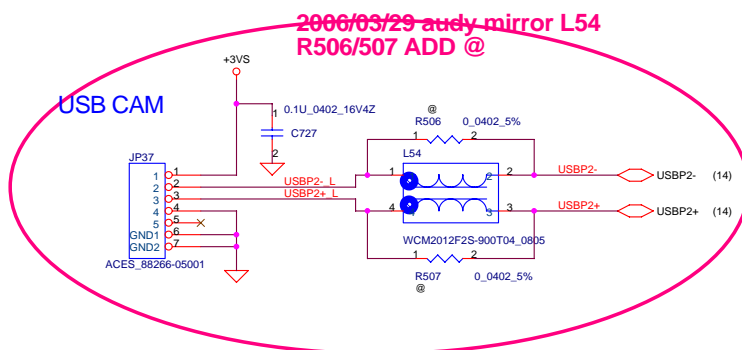
Security Classification	Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2005/07/29	Deciphered Date	2006/07/29	Title
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF THE DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number
				HCL51 LA-3211P
				Rev 0.4
Date: 星期二, 四月 11, 2006				Sheet 24 of 43



**IDSEL:AD18  
(PIRQF/H#,  
GNT#3,  
REQ#3)**



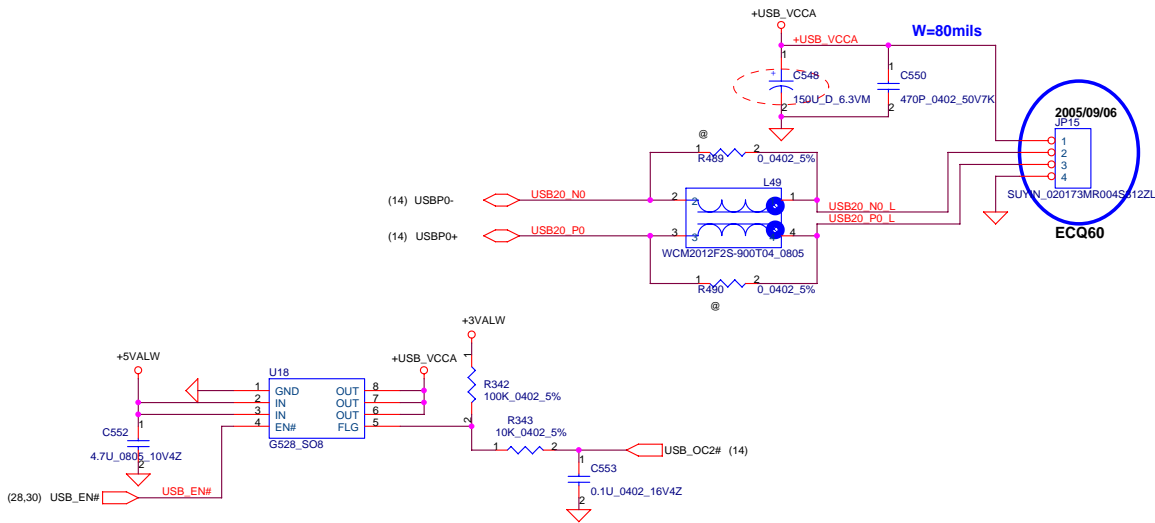
2006/03/07



Mini Card Power Rating			
Power	Primary Power (mA)		Auxiliary Power (mA)
	Peak	Normal	Normal
+3VS	1000	750	
+3VALW	330	250	250 (wake enable)
+1.5VS	500	375	5 (Not wake enable)

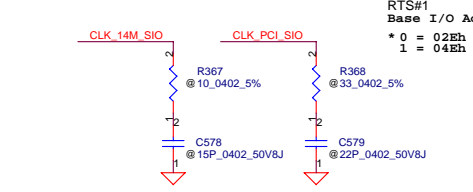
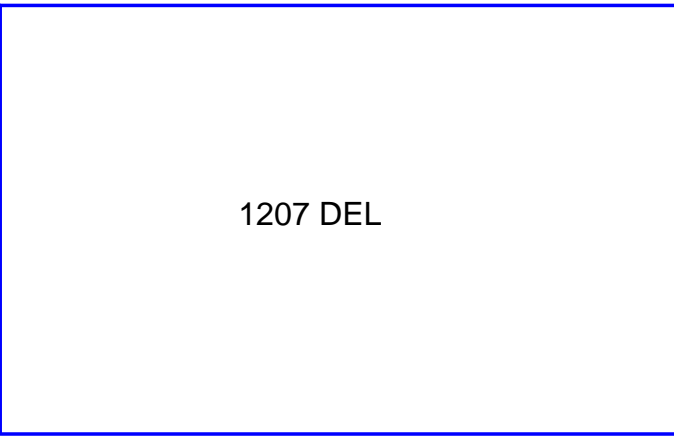
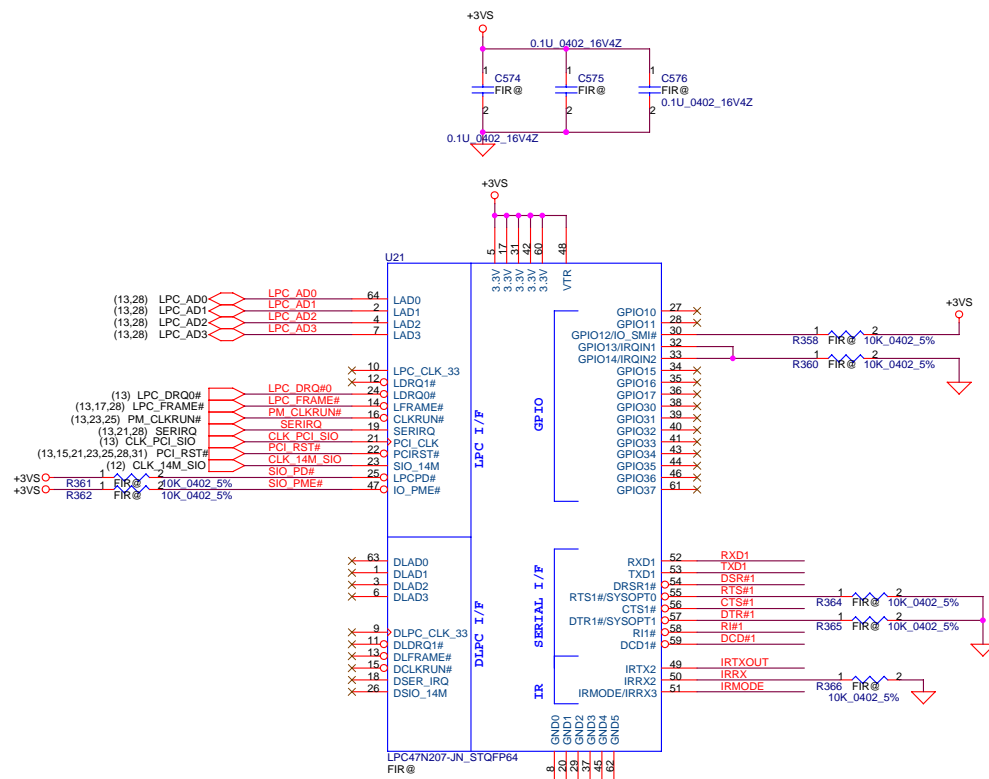
Security Classification	Compal Secret Data		Title	Compal Electronics, Inc.
Issued Date	2005/06/20	Deciphered Date		
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.			Document Number	Rev
			HCL51 LA-3211P	0.4
			Date: 星期二, 四月 11, 2006	Sheet 25 of 43

USB CONN. 1 & 2

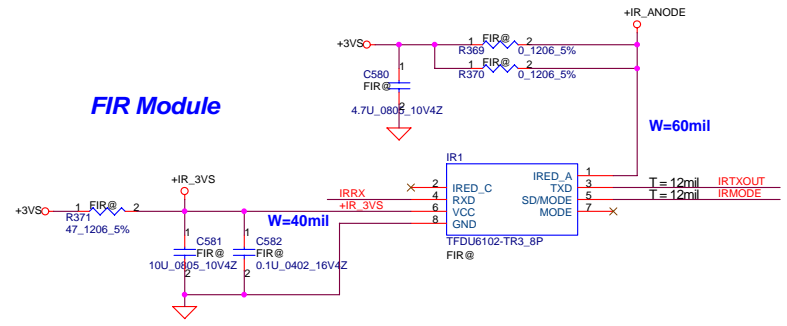
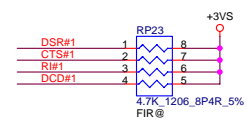
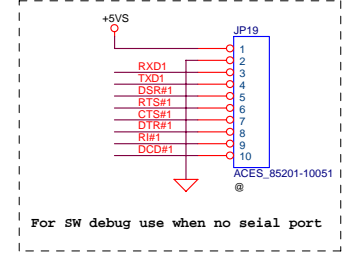


1207 DEL

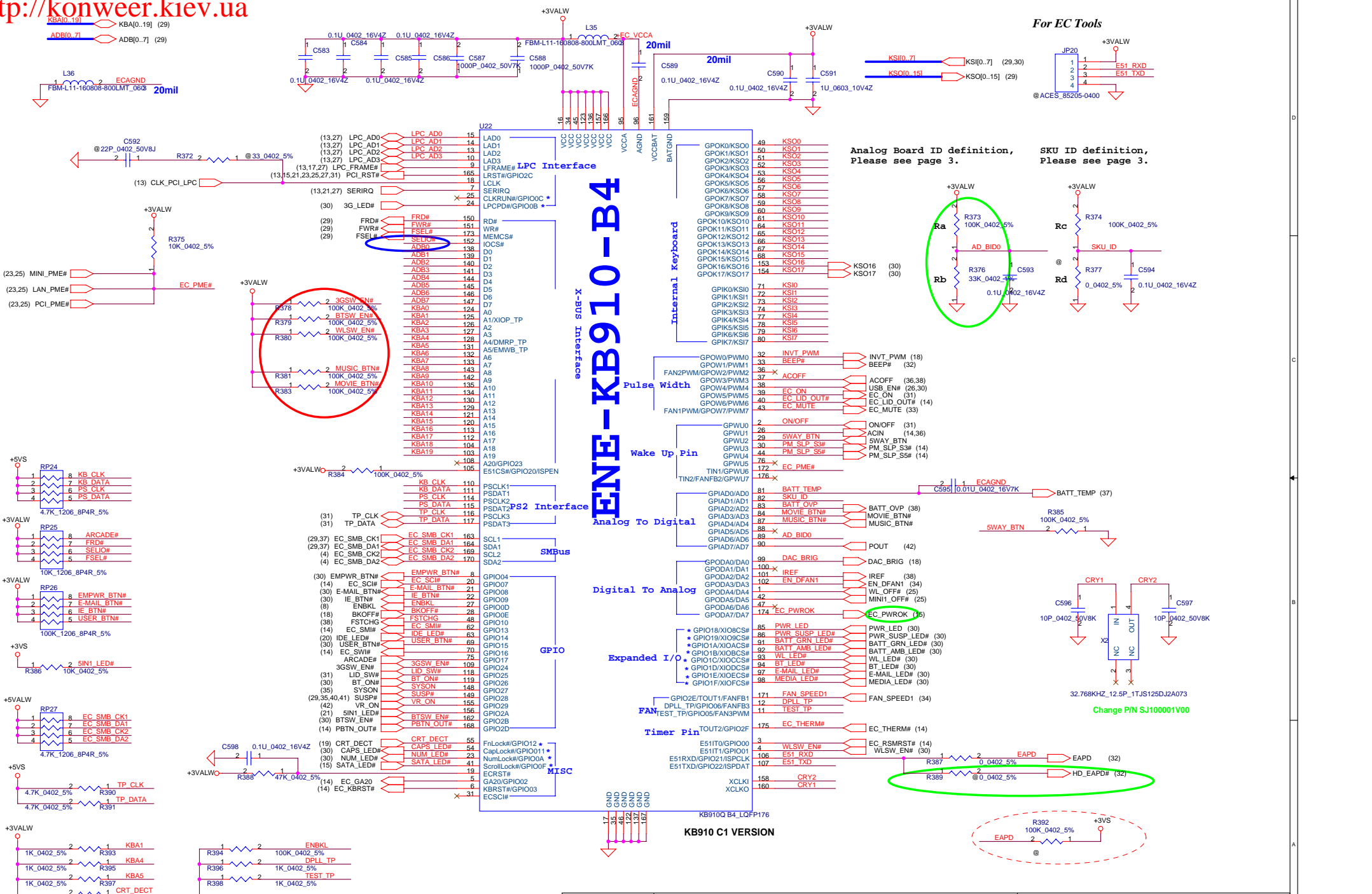
Security Classification	Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2005/06/20	Deciphered Date	2006/06/20	Title
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				NEW CARD SOCKET
Date: 星期二, 四月 11, 2006			Document Number	Rev
			HCL51 LA-3211P	0.4
			Sheet 26 of 43	



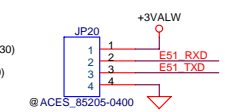
Place on the BOT side (near MINIPCI conn.)



Security Classification	Compal Secret Data		Title	
Issued Date	2005/06/20	Deciphered Date	2006/06/20	SIO1036 & FIR
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number HCL51 LA-3211P
Date: 星期四, 四月 11, 2006				Rev 0.4
Sheet 27 of 43				

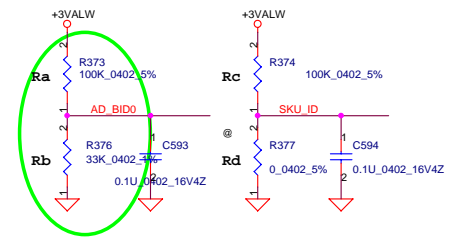


For EC Tools



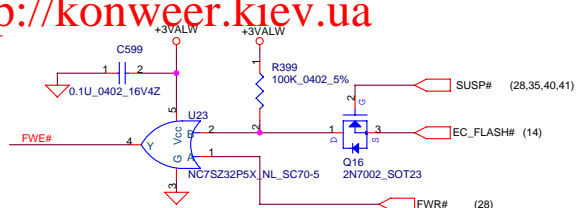
Analog Board ID definition, Please see page 3.

SKU ID definition, Please see page 3.

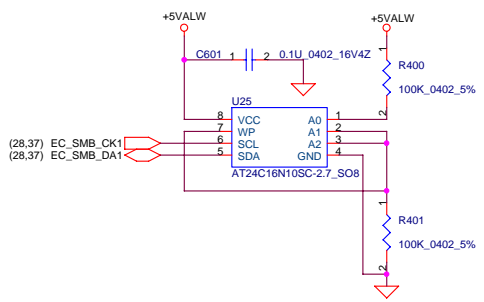


- Internal Keyboard**
- GPOK0/KSO0
  - GPOK1/KSO1
  - GPOK2/KSO2
  - GPOK3/KSO3
  - GPOK4/KSO4
  - GPOK5/KSO5
  - GPOK6/KSO6
  - GPOK7/KSO7
  - GPOK8/KSO8
  - GPOK9/KSO9
  - GPOK10/KSO10
  - GPOK11/KSO11
  - GPOK12/KSO12
  - GPOK13/KSO13
  - GPOK14/KSO14
  - GPOK15/KSO15
  - GPOK16/KSO16
  - GPOK17/KSO17
  - KS00
  - KS01
  - KS02
  - KS03
  - KS04
  - KS05
  - KS06
  - KS07
  - KS08
  - KS09
  - KS10
  - KS11
  - KS12
  - KS13
  - KS14
  - KS15
  - KS16
  - KS17
  - KS18
  - KS19
  - KS20
  - KS21
  - KS22
  - KS23
  - KS24
  - KS25
  - KS26
  - KS27
  - KS28
  - KS29
  - KS30
  - KS31
  - KS32
  - KS33
  - KS34
  - KS35
  - KS36
  - KS37
  - KS38
  - KS39
  - KS40
  - KS41
  - KS42
  - KS43
  - KS44
  - KS45
  - KS46
  - KS47
  - KS48
  - KS49
  - KS50
  - KS51
  - KS52
  - KS53
  - KS54
  - KS55
  - KS56
  - KS57
  - KS58
  - KS59
  - KS60
  - KS61
  - KS62
  - KS63
  - KS64
  - KS65
  - KS66
  - KS67
  - KS68
  - KS69
  - KS70
  - KS71
  - KS72
  - KS73
  - KS74
  - KS75
  - KS76
  - KS77
  - KS78
  - KS79
  - KS80
  - KS81
  - KS82
  - KS83
  - KS84
  - KS85
  - KS86
  - KS87
  - KS88
  - KS89
  - KS90
  - KS91
  - KS92
  - KS93
  - KS94
  - KS95
  - KS96
  - KS97
  - KS98
  - KS99
  - KS100
- Wake Up Pin**
- GPWU0
  - GPWU1
  - GPWU2
  - GPWU3
  - GPWU4
  - GPWU5
  - TIN1/GPWU6
  - TIN2/FANFB2/GPWU7
- Timer Pin**
- GPIOD0/AD0
  - GPIAD1/AD1
  - GPIAD2/AD2
  - GPIAD3/AD3
  - GPIAD4/AD4
  - GPIAD5/AD5
  - GPIAD6/AD6
  - GPIAD7/AD7
  - GPODA0/DA0
  - GPODA1/DA1
  - GPODA2/DA2
  - GPODA3/DA3
  - GPODA4/DA4
  - GPODA5/DA5
  - GPODA6/DA6
  - GPODA7/DA7
  - GPIOD8/XIO0CS#
  - GPIOD9/XIO0CS#
  - GPIOD10/XIO0CS#
  - GPIOD11/XIO0CS#
  - GPIOD12/XIO0CS#
  - GPIOD13/XIO0CS#
  - GPIOD14/XIO0CS#
  - GPIOD15/XIO0CS#
  - GPIOD16/XIO0CS#
  - GPIOD17/XIO0CS#
  - GPIOD18/XIO0CS#
  - GPIOD19/XIO0CS#
  - GPIOD20/XIO0CS#
  - GPIOD21/XIO0CS#
  - GPIOD22/XIO0CS#
  - GPIOD23/XIO0CS#
  - GPIOD24/XIO0CS#
  - GPIOD25/XIO0CS#
  - GPIOD26/XIO0CS#
  - GPIOD27/XIO0CS#
  - GPIOD28/XIO0CS#
  - GPIOD29/XIO0CS#
  - GPIOD30/XIO0CS#
  - GPIOD31/XIO0CS#
  - GPIOD32/XIO0CS#
  - GPIOD33/XIO0CS#
  - GPIOD34/XIO0CS#
  - GPIOD35/XIO0CS#
  - GPIOD36/XIO0CS#
  - GPIOD37/XIO0CS#
  - GPIOD38/XIO0CS#
  - GPIOD39/XIO0CS#
  - GPIOD40/XIO0CS#
  - GPIOD41/XIO0CS#
  - GPIOD42/XIO0CS#
  - GPIOD43/XIO0CS#
  - GPIOD44/XIO0CS#
  - GPIOD45/XIO0CS#
  - GPIOD46/XIO0CS#
  - GPIOD47/XIO0CS#
  - GPIOD48/XIO0CS#
  - GPIOD49/XIO0CS#
  - GPIOD50/XIO0CS#
  - GPIOD51/XIO0CS#
  - GPIOD52/XIO0CS#
  - GPIOD53/XIO0CS#
  - GPIOD54/XIO0CS#
  - GPIOD55/XIO0CS#
  - GPIOD56/XIO0CS#
  - GPIOD57/XIO0CS#
  - GPIOD58/XIO0CS#
  - GPIOD59/XIO0CS#
  - GPIOD60/XIO0CS#
  - GPIOD61/XIO0CS#
  - GPIOD62/XIO0CS#
  - GPIOD63/XIO0CS#
  - GPIOD64/XIO0CS#
  - GPIOD65/XIO0CS#
  - GPIOD66/XIO0CS#
  - GPIOD67/XIO0CS#
  - GPIOD68/XIO0CS#
  - GPIOD69/XIO0CS#
  - GPIOD70/XIO0CS#
  - GPIOD71/XIO0CS#
  - GPIOD72/XIO0CS#
  - GPIOD73/XIO0CS#
  - GPIOD74/XIO0CS#
  - GPIOD75/XIO0CS#
  - GPIOD76/XIO0CS#
  - GPIOD77/XIO0CS#
  - GPIOD78/XIO0CS#
  - GPIOD79/XIO0CS#
  - GPIOD80/XIO0CS#
  - GPIOD81/XIO0CS#
  - GPIOD82/XIO0CS#
  - GPIOD83/XIO0CS#
  - GPIOD84/XIO0CS#
  - GPIOD85/XIO0CS#
  - GPIOD86/XIO0CS#
  - GPIOD87/XIO0CS#
  - GPIOD88/XIO0CS#
  - GPIOD89/XIO0CS#
  - GPIOD90/XIO0CS#
  - GPIOD91/XIO0CS#
  - GPIOD92/XIO0CS#
  - GPIOD93/XIO0CS#
  - GPIOD94/XIO0CS#
  - GPIOD95/XIO0CS#
  - GPIOD96/XIO0CS#
  - GPIOD97/XIO0CS#
  - GPIOD98/XIO0CS#
  - GPIOD99/XIO0CS#
  - GPIOD100/XIO0CS#

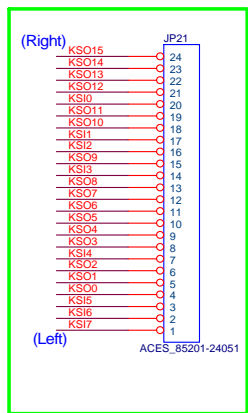
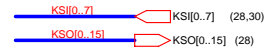
Security Classification	2005/06/20	Deciphered Date	2006/06/20	Title	EC ENE KB910
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number	HCL51 LA-3211P
				Date	星期二, 四月 11, 2006
				Sheet	28 of 43



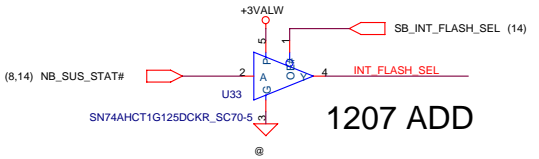
Check PCB Footprint



INT\_KBD Conn.

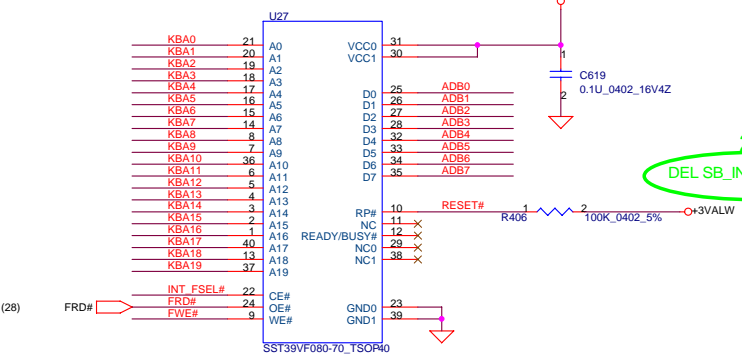


FOR DEBUG ONLY

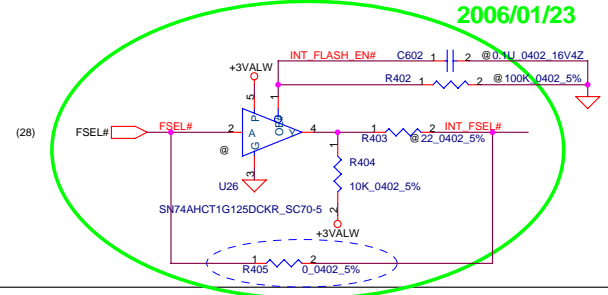


1MB Flash ROM

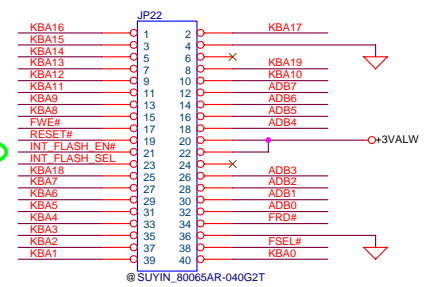
(28) KBA[0..19] KBA[0..19]  
 (28) ADB[0..7] ADB[0..7]



2006/01/23

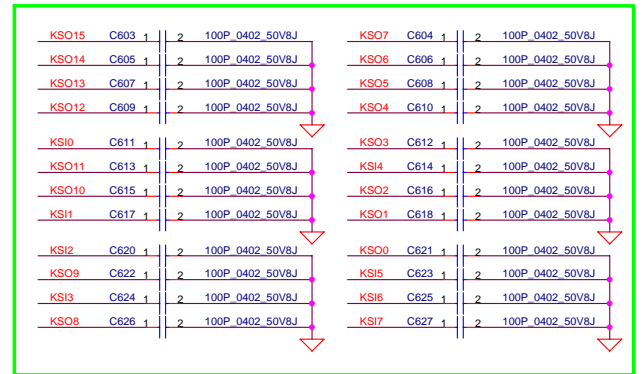


1MB ROM Socket

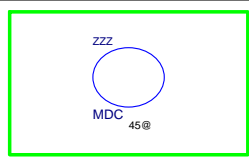


2006/01/23

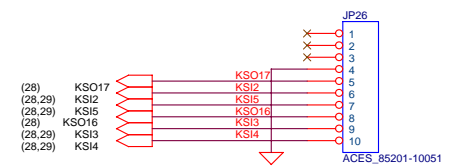
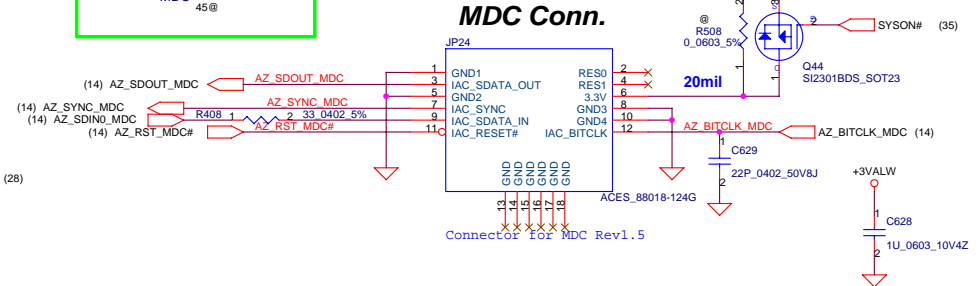
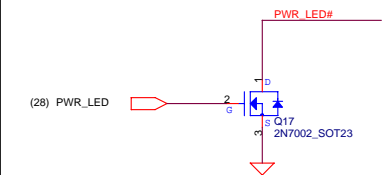
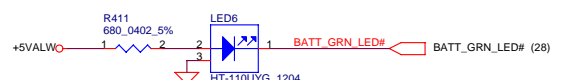
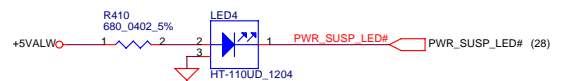
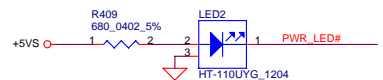
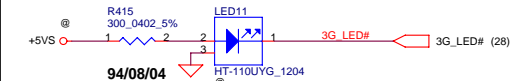
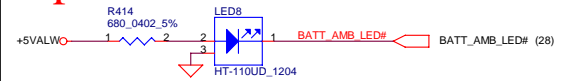
DEL SB\_INT\_FLASH\_SEL



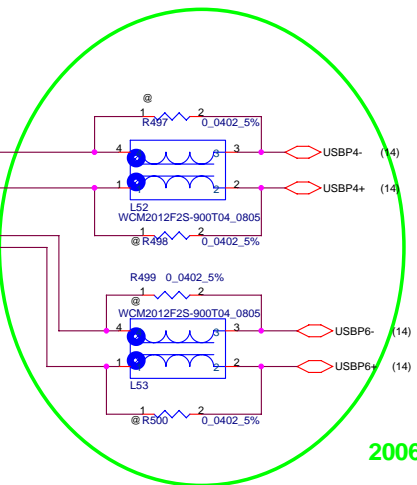
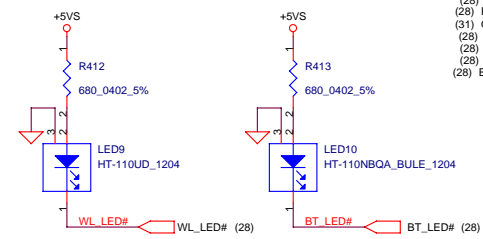
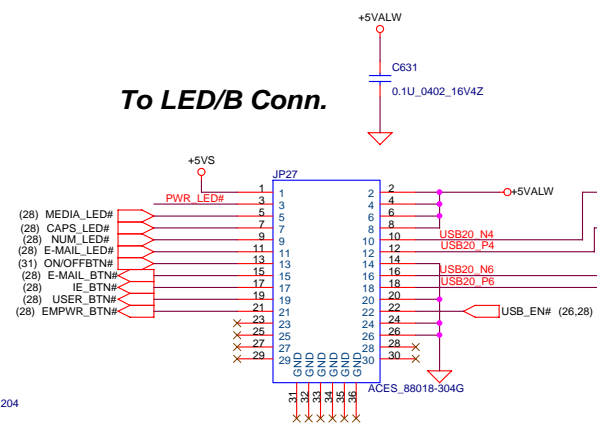
Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2005/06/20	Deciphered Date	2006/06/20	Title	BIOS, I/O Port & K/B Connector
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.					
Date:	星期二, 四月 11, 2006	Sheet	29	of	43



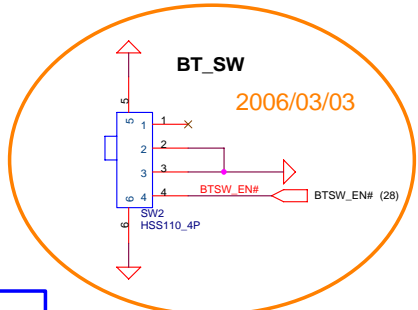
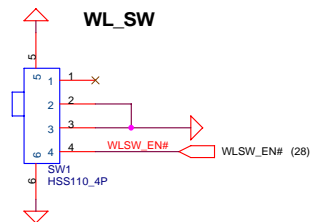
2006/01/23



To LED/B Conn.

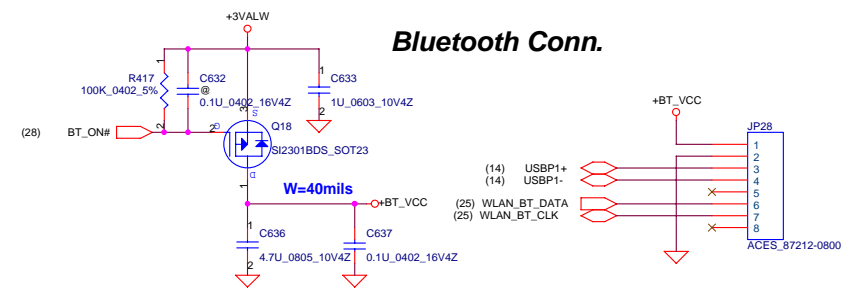


2006/01/24



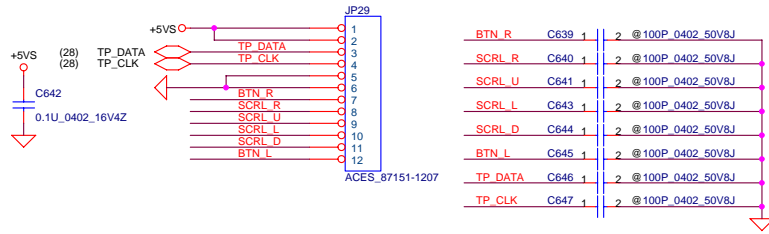
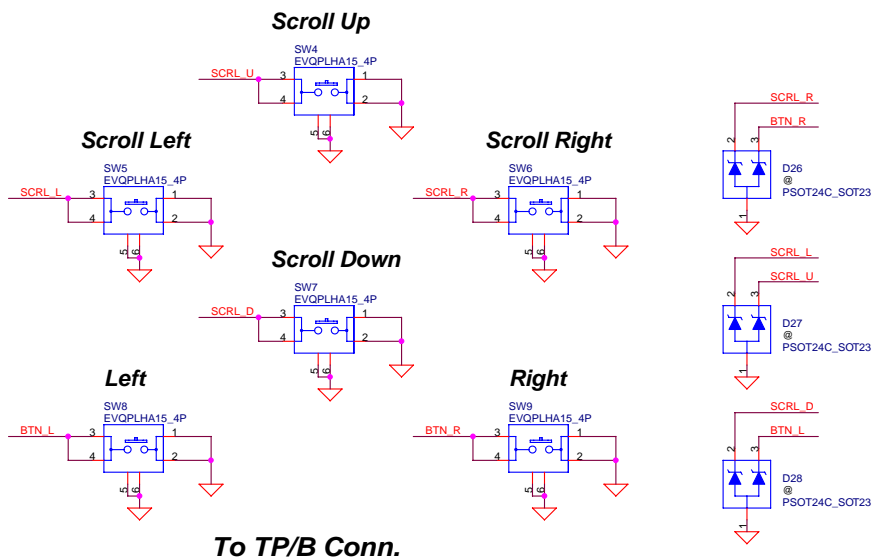
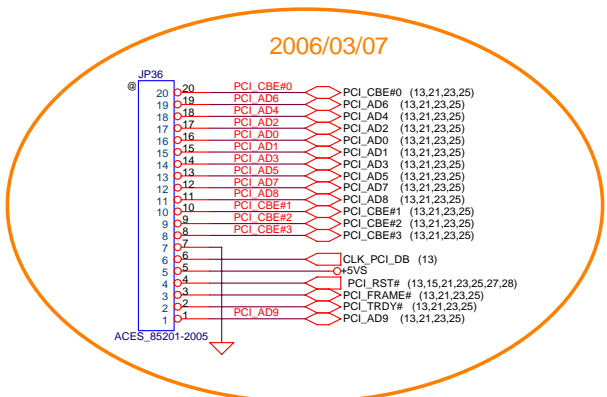
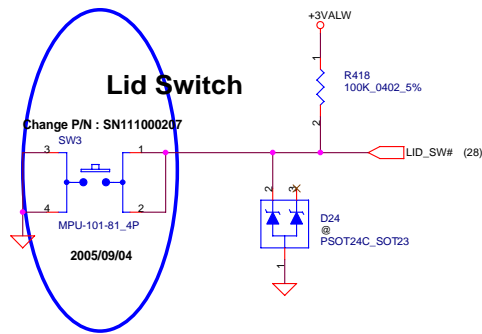
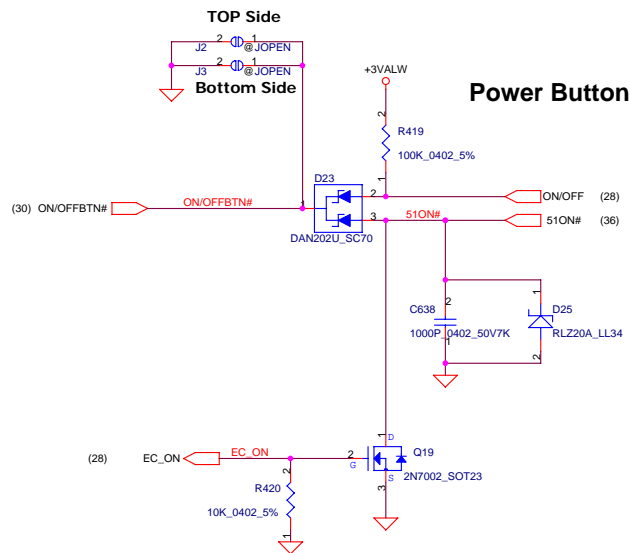
2006/03/03

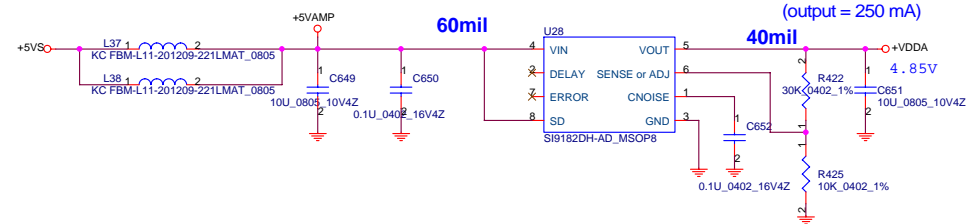
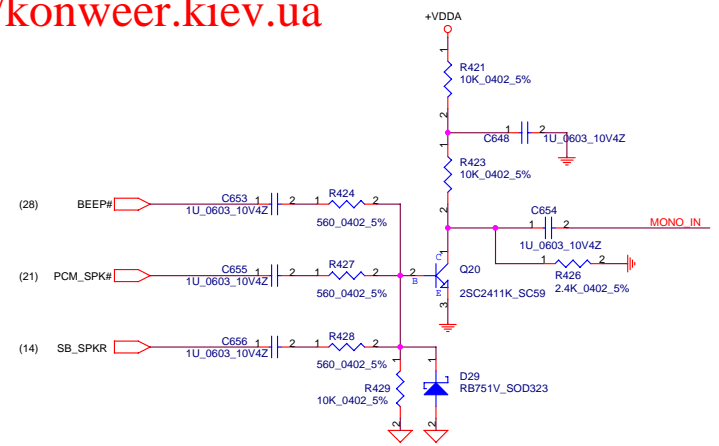
Geneva			2005/09/04		Grapevine	
	KSO16	KSO17	KSO16	KSO17		
KSI0	VOL_UP	LEFT				
KSI1	RIGHT	VOL_DOWN				
KSI2	PLAY	ENTER	KSI2	PLAY		
KSI3	STOP		KSI3	STOP	VOL_UP	
KSI4	NEXT		KSI4	NEXT	VOL_DOWN	
KSI5	REV		KSI5	REV	ARCADE_TV	
KSI6		RECORD				



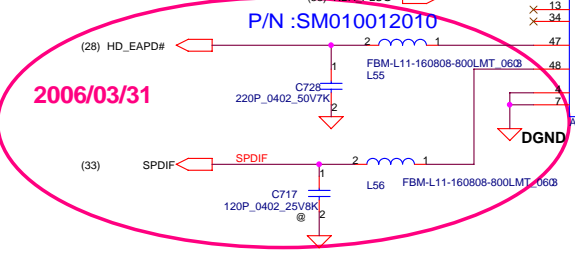
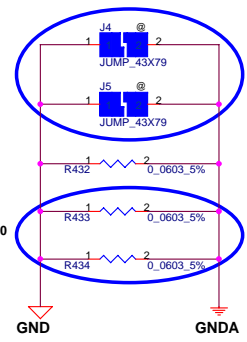
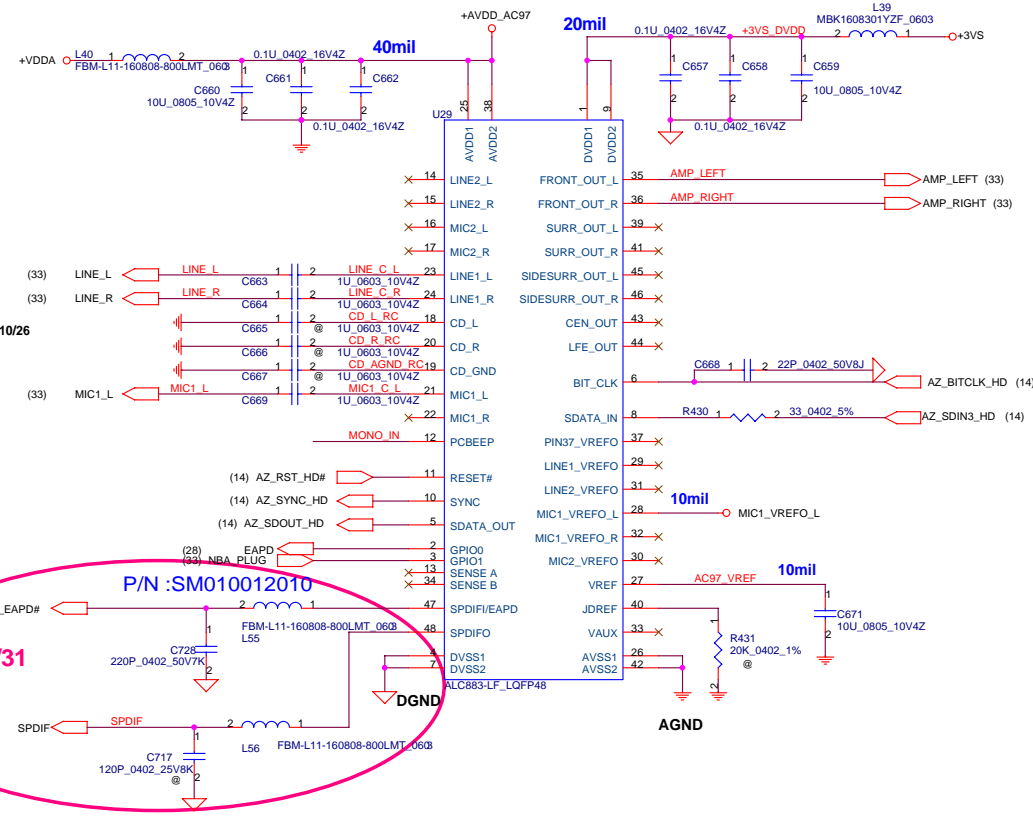
Security Classification		Compal Secret Data		Title		Compal Electronics, Inc.	
Issued Date	2005/06/20	Deciphered Date	2006/06/20	CD-PLAY / MDC / BT / CIR / LED			
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF THE DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number	HCL51 LA-3211P		Rev 0.4
Date:	星期二, 四月 11, 2006	Sheet	30	of	43		

ON/OFF switch





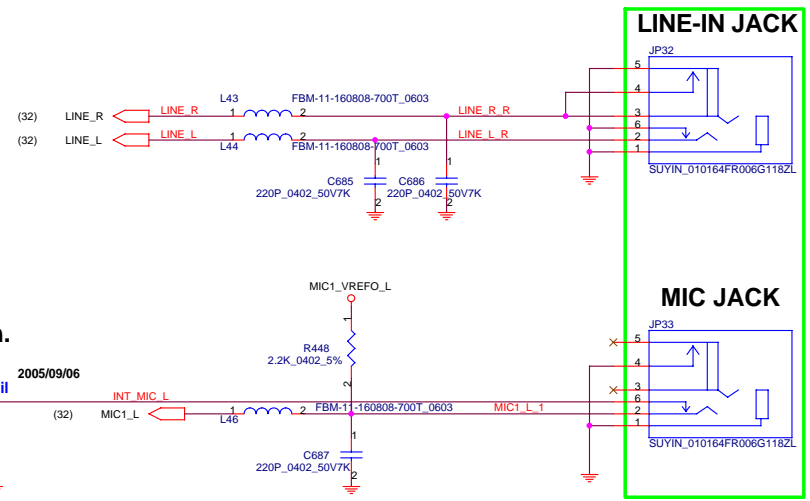
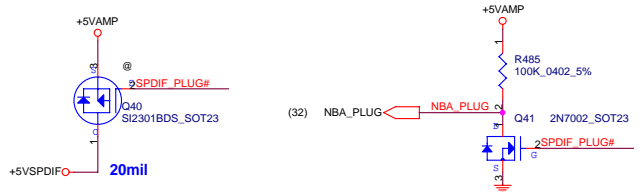
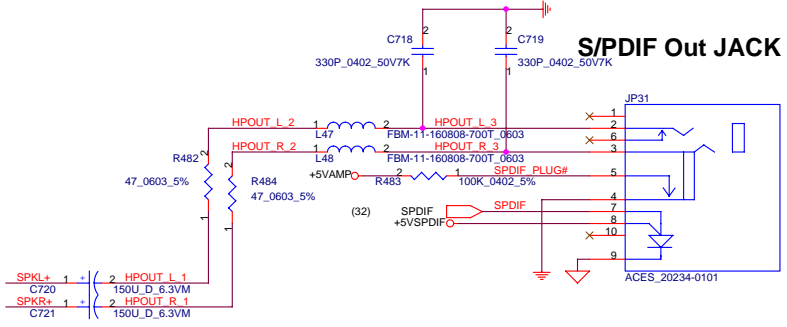
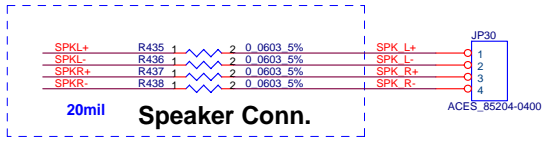
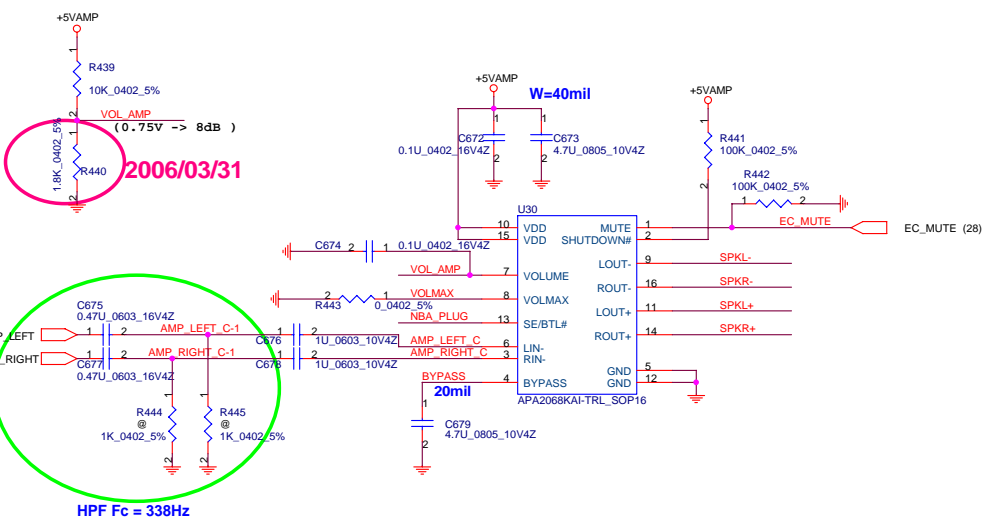
### HD Audio Codec



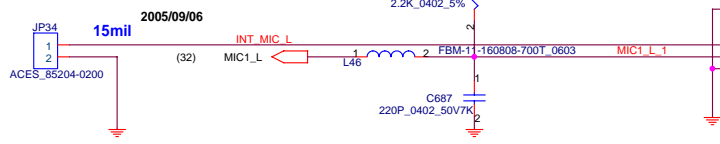
2006/03/31

P/N : SM010012010

Security Classification	Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2005/06/20	Deciphered Date	2006/06/20	Title
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number
				HCL51 LA-3211P
				Rev 0.4
				Date: 星期二, 四月 11, 2006   Sheet 32 of 43

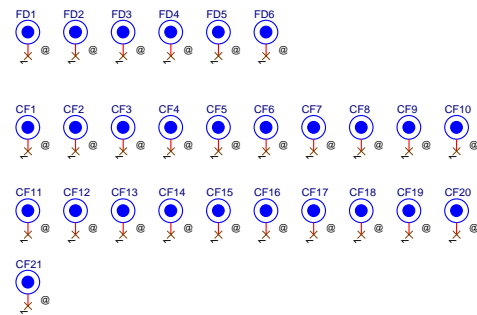
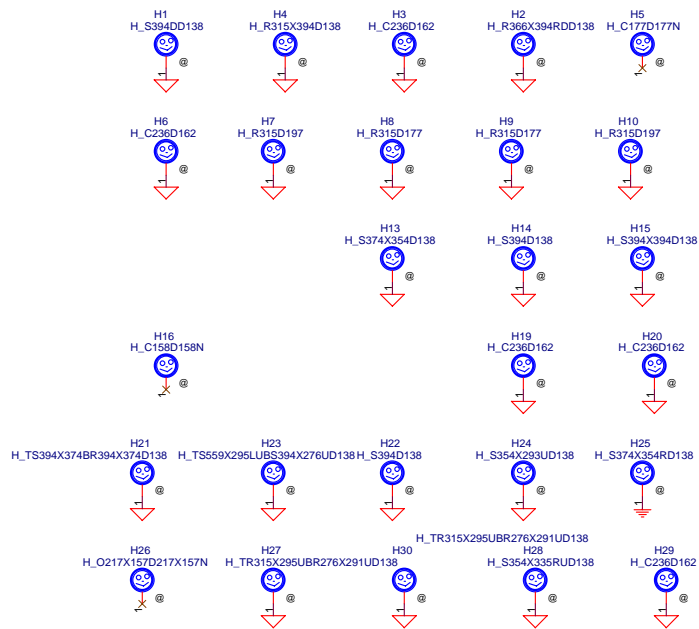
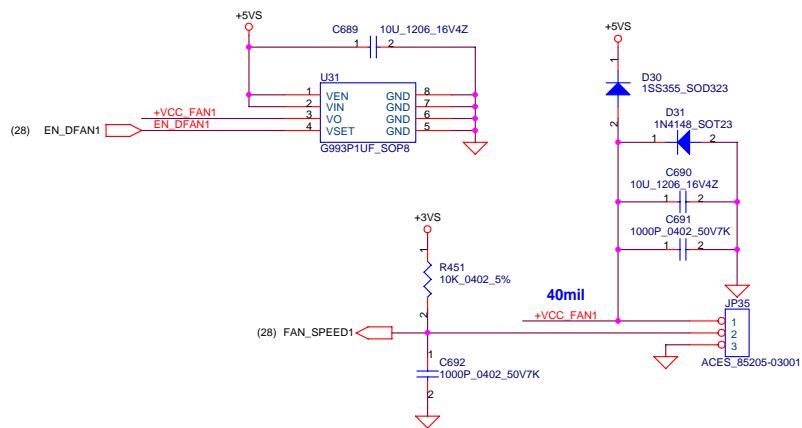


Int MIC Conn.



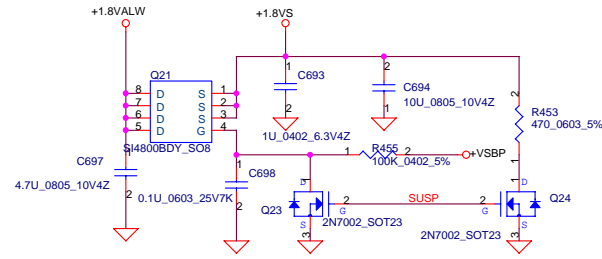
Security Classification	Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2005/06/20	Deciphered Date	2006/06/20	Title
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number
				HCL51 LA-3211P
				Rev 0.4
				Date: 星期二, 四月 11, 2006
				Sheet 33 of 43

FAN1 Conn

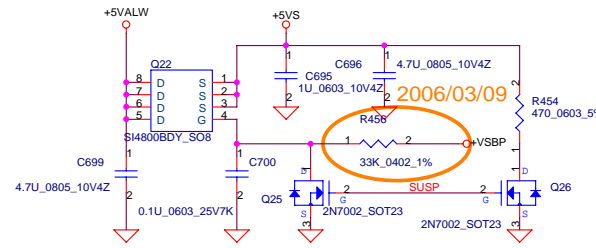


Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2005/06/20	Deciphered Date	2006/06/20	Title	
				FAN & Screw Hole	
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF COMPAL ELECTRONICS, INC. DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number	Rev
				HCL51 LA-3211P	0.4
				Date: 星期二, 四月 11, 2006	Sheet 34 of 43

**+1.8VALW TO +1.8VS**

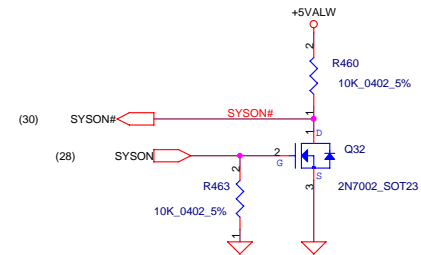
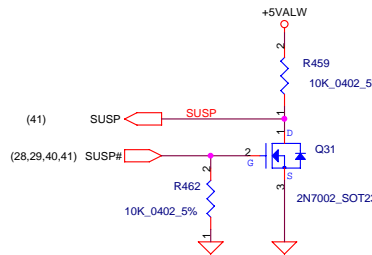
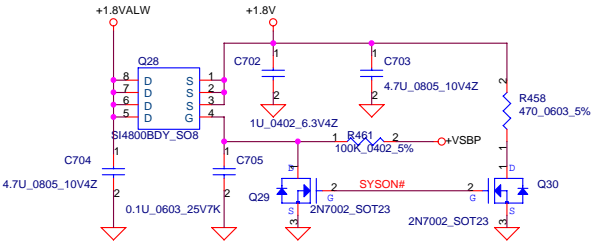


**+5VALW TO +5VS**

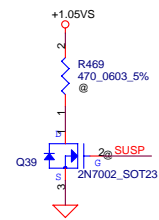
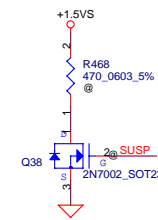
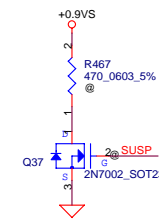
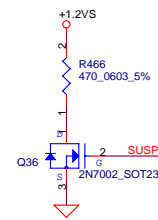
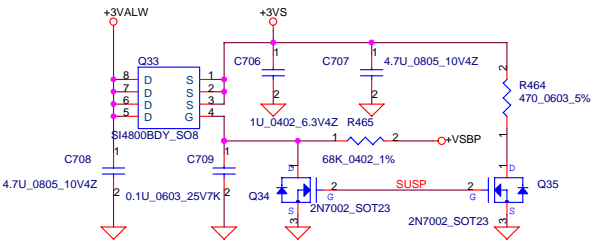


1207 DEL

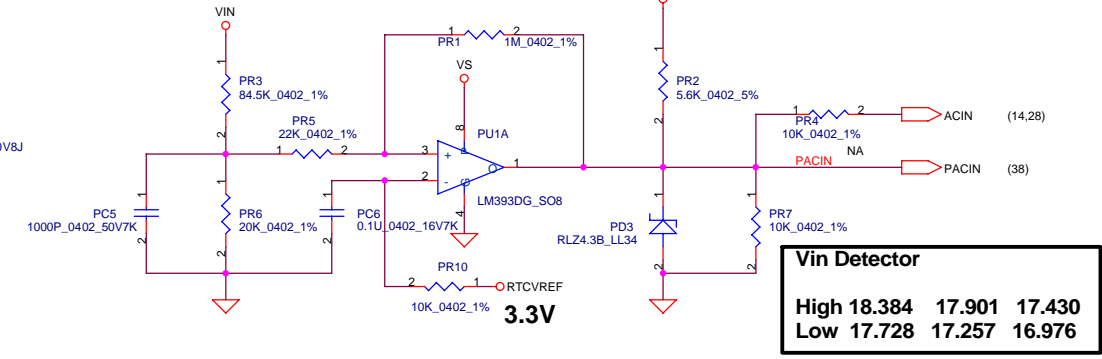
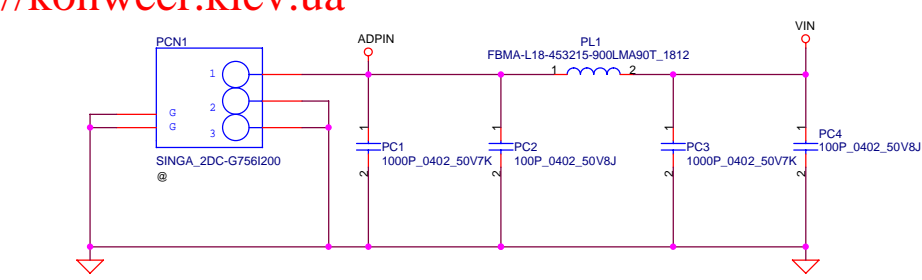
**+1.8VALW TO +1.8V**



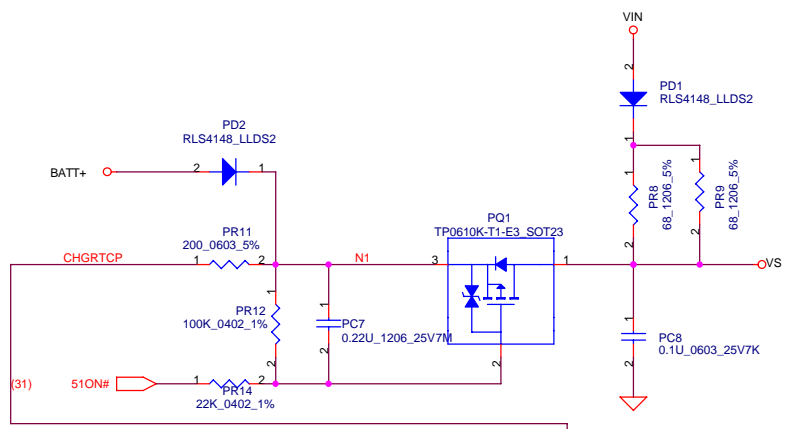
**+3VALW TO +3VS**



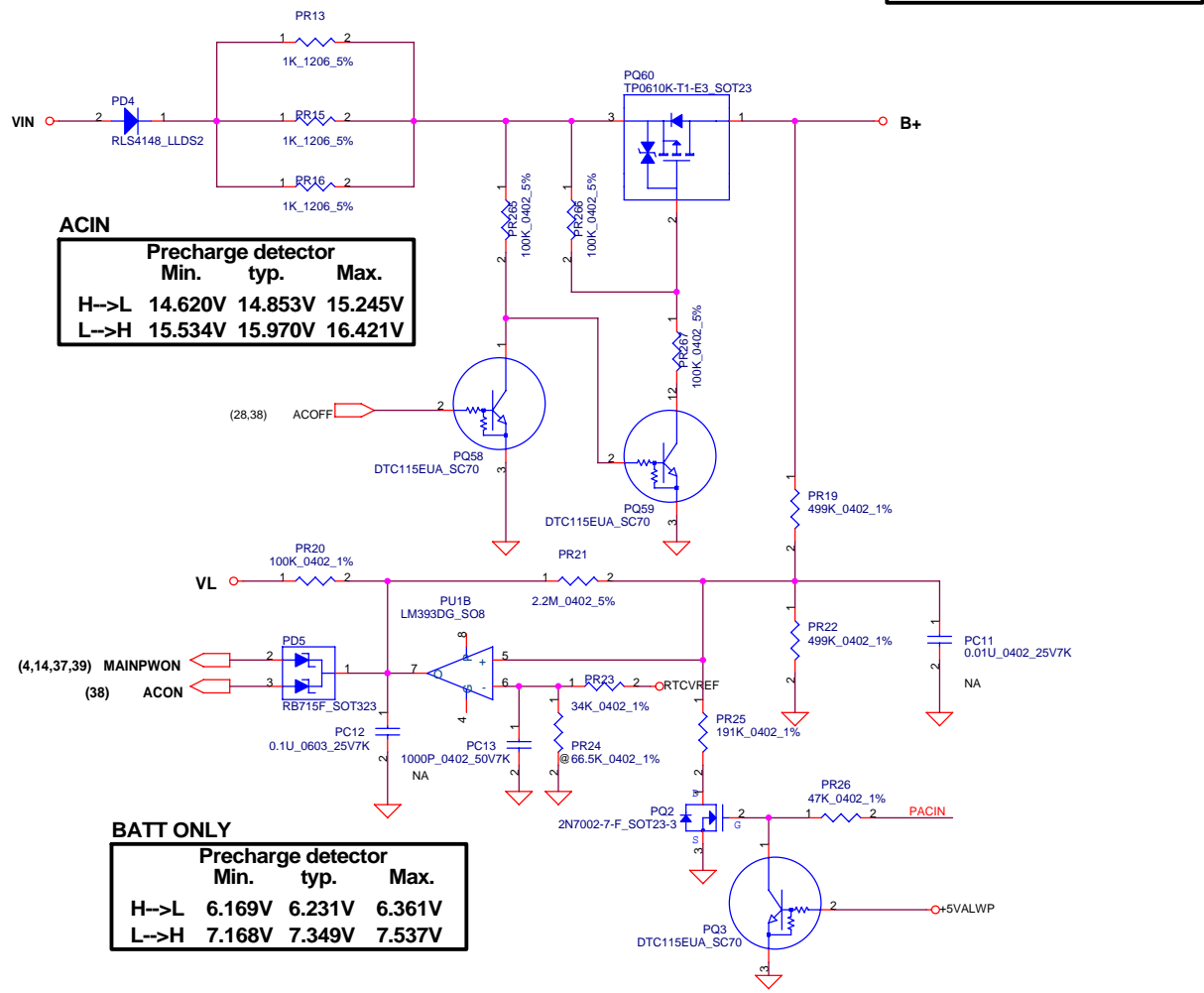
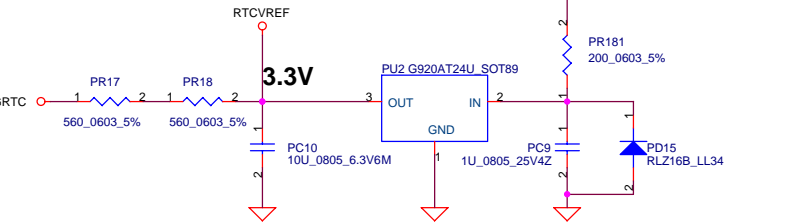
Security Classification	Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2005/11/01	Deciphered Date	2006/11/30	Title
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF THE CUSTOMER DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number
				HCL51 LA-3211P
				Rev 0.4
				Date: 星期三, 四月 11, 2006
				Sheet 35 of 43



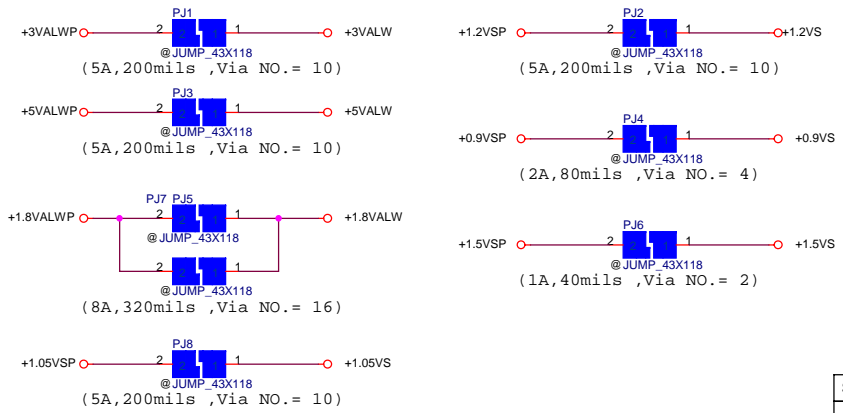
**Vin Detector**  
 High 18.384 17.901 17.430  
 Low 17.728 17.257 16.976



**ACIN**  
 Precharge detector  
 Min. typ. Max.  
 H->L 14.620V 14.853V 15.245V  
 L->H 15.534V 15.970V 16.421V

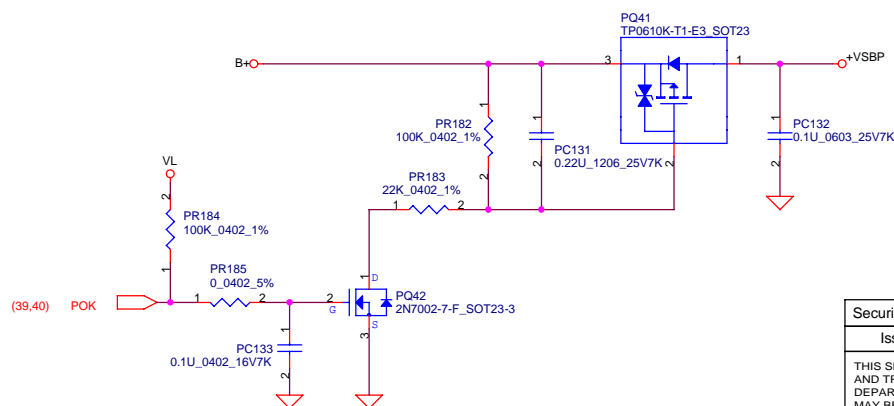
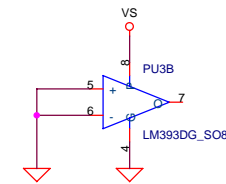
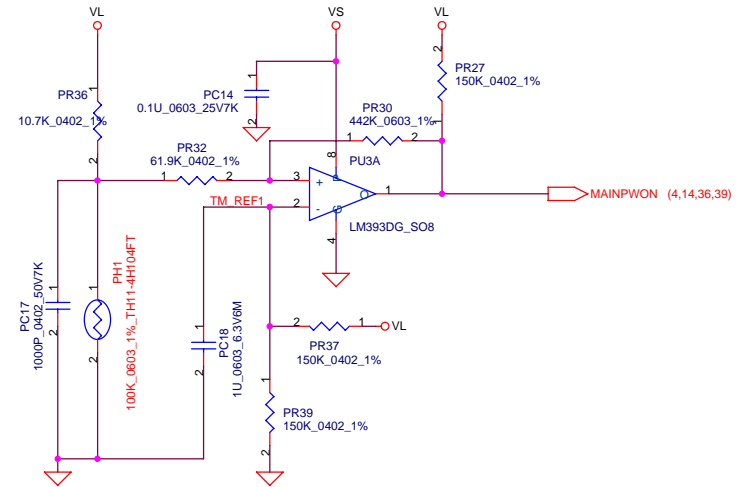
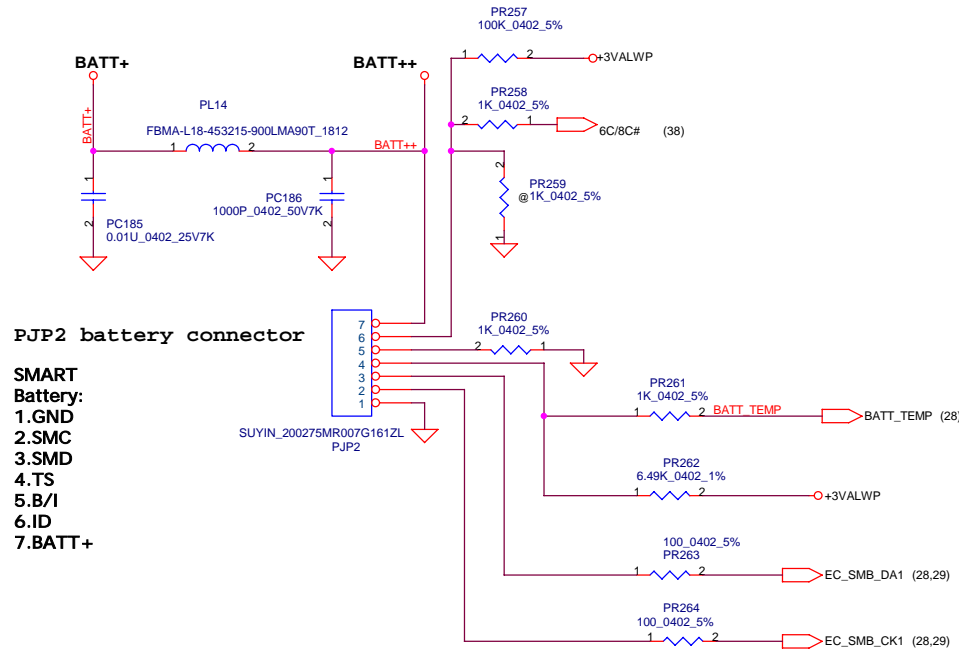


**BATT ONLY**  
 Precharge detector  
 Min. typ. Max.  
 H->L 6.169V 6.231V 6.361V  
 L->H 7.168V 7.349V 7.537V



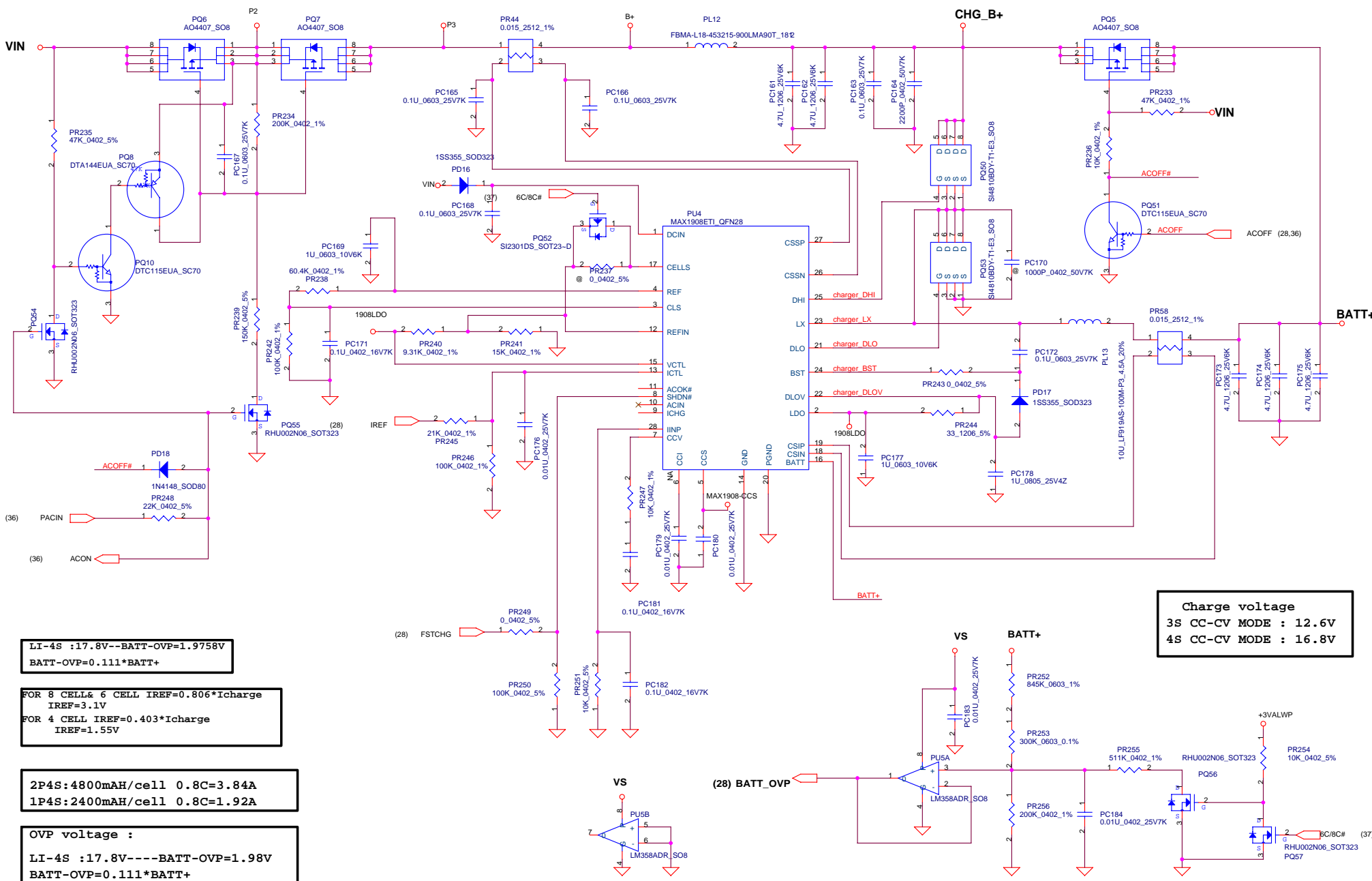
Security Classification	Compal Secret Data		Title	
Issued Date	2005/11/01	Deciphered Date	2006/11/30	DCIN & DETECTOR
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF RESEARCH AND DEVELOPMENT DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number
				HCL51 LA-3211P
				0.4 Rev
Date: 星期二, 四月 11, 2006				Sheet 36 of 43

PH1 under CPU botten side :  
 CPU thermal protection at 85 degree C  
 Recovery at 70 degree C



Security Classification	Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2005/06/23	Deciphered Date	2006/10/22	Title
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number
				HCL51 LA-3211P
				0.4 Rev
Date: 星期二, 四月 11, 2006				Sheet 37 of 43

I<sub>adp</sub>=0~3.117A (65W)



**Charge voltage**  
 3S CC-CV MODE : 12.6V  
 4S CC-CV MODE : 16.8V

LI-4S : 17.8V--BATT-OVP=1.9758V  
 BATT-OVP=0.111\*BATT+

FOR 8 CELL& 6 CELL IREF=0.806\*Icharge  
 IREF=3.1V  
 FOR 4 CELL IREF=0.403\*Icharge  
 IREF=1.55V

2P4S:4800mAH/cell 0.8C=3.84A  
 1P4S:2400mAH/cell 0.8C=1.92A

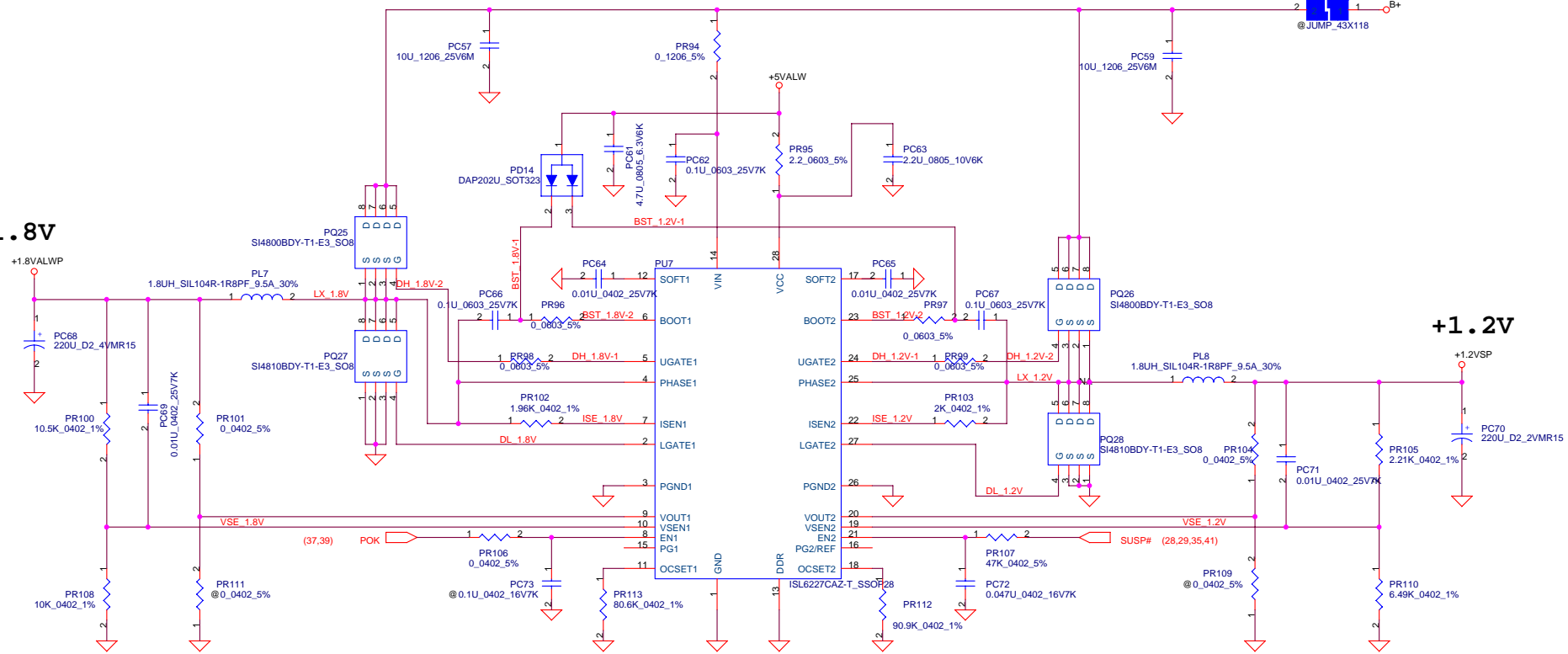
OVP voltage :  
 LI-4S : 17.8V---BATT-OVP=1.98V  
 BATT-OVP=0.111\*BATT+  
 LI-3S : 13.35V---BATT-OVP=1.98V  
 BATT-OVP=0.111\*BATT+

Security Classification	Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2005/06/20	Deciphered Date	2006/06/20	Title
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number
				Rev 0.4
Date: 星期二, 四月 11, 2006				Sheet 38 of 43

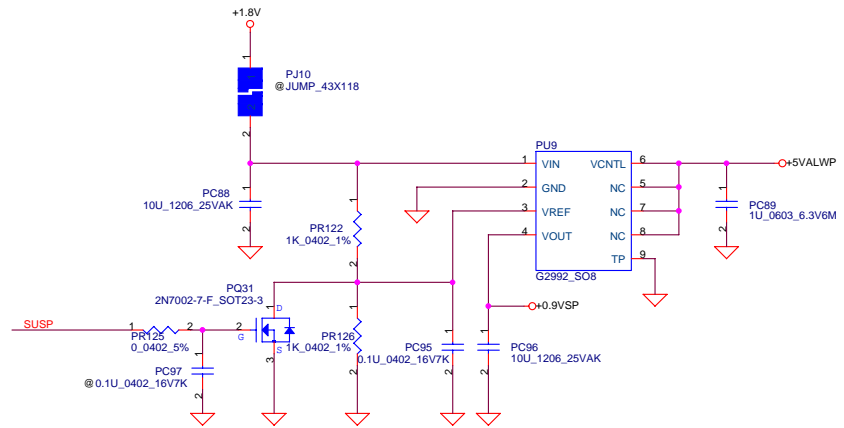
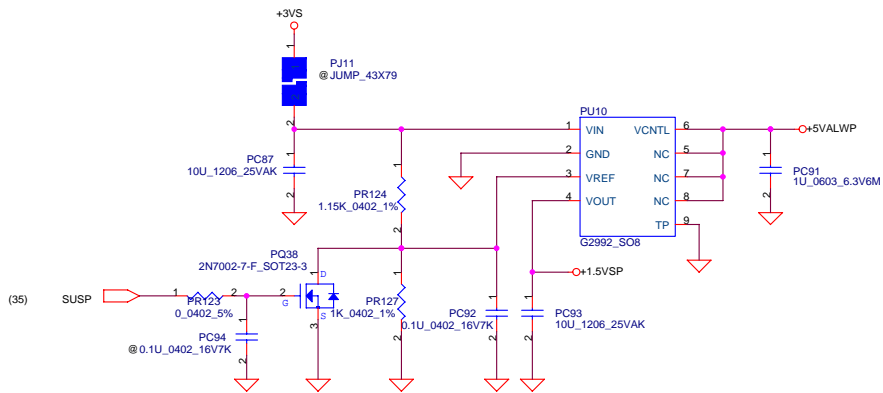
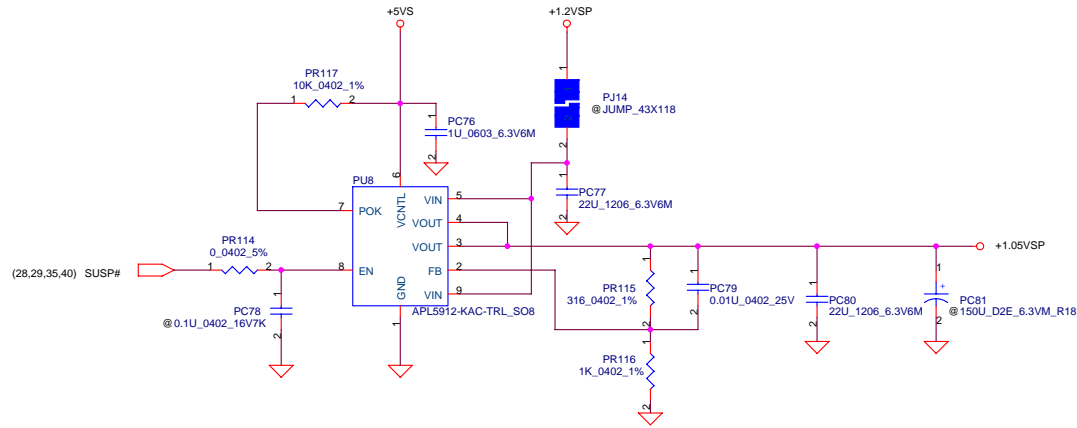


+1.8V

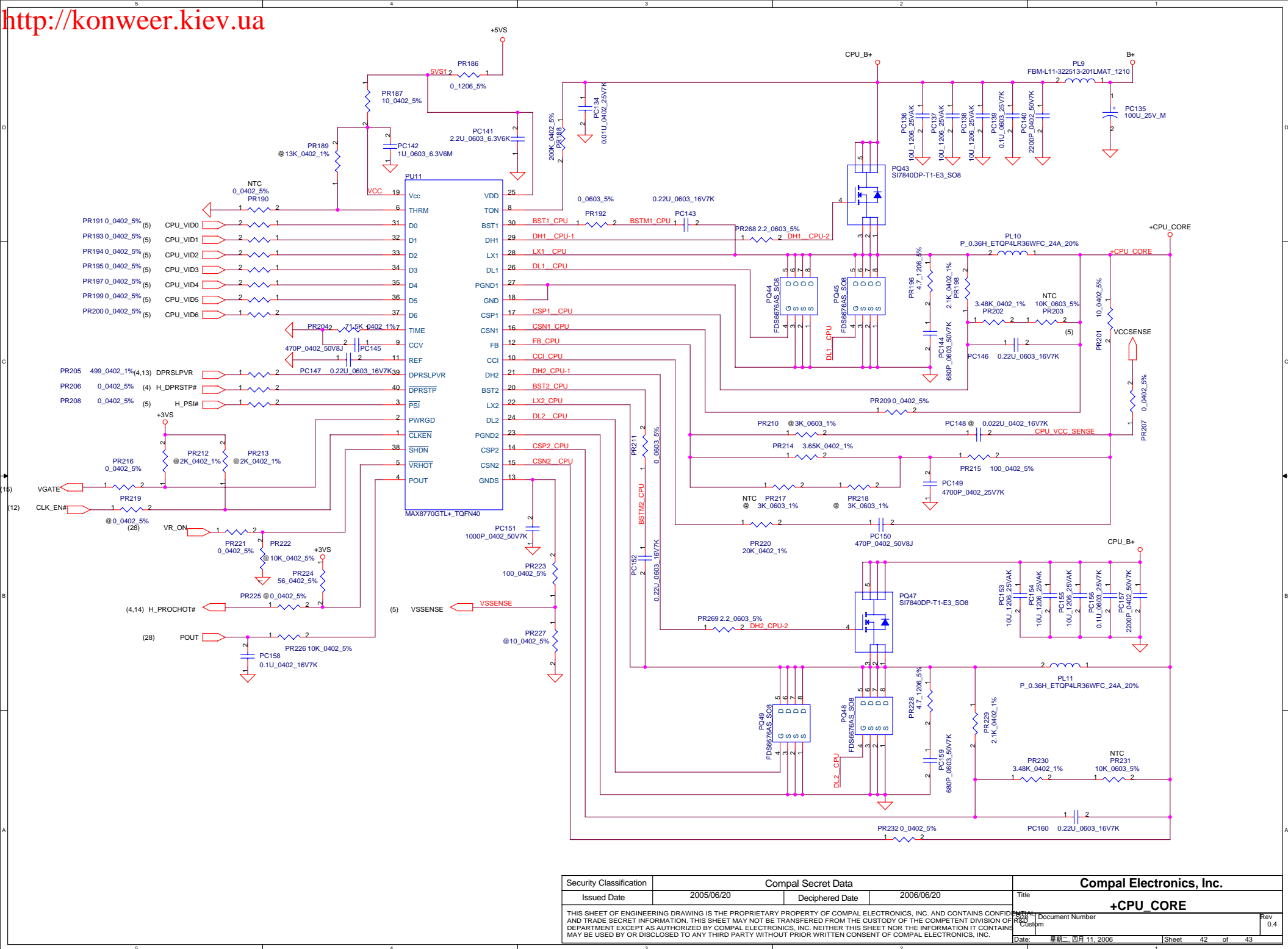
+1.2V



Security Classification	Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2005/11/01	Deciphered Date	2006/11/30	Title
				+1.8V/+1.2V
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF COMPAL ELECTRONICS, INC. DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number
				HCL51 LA-3211P
				Rev
				0.4
Date: 星期三, 四月 11, 2006				Sheet 40 of 43



Security Classification	Compal Secret Data		Title	
Issued Date	2005/06/23	Deciphered Date	2006/10/22	Compal Electronics, Inc.
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number
				HCL51 LA-3211P
				Rev 0.4
				Date: 2006年11月11日
				Sheet 41 of 43



Security Classification	Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2005/06/20	Deciphered Date	2006/06/20	Title
				<b>+CPU_CORE</b>
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				
Rev	0.4	Document Number	R2201-Custpm	
Date:	星期二, 四月 11, 2006	Sheet	42	of 43

Version change list (P.I.R. List)

Item	Fixed Issue	Reason for change	Rev.	PG#	Modify List	VER	Phase
1		For EMI require CPU core change from 0 to 2.2		42	MODIFY PR268/PR269 FROM 0 TO 2.2		DVT
2		modify 1.2V sequence(HW require)		40	change PR107 from 0 to 1k ;add PC72: :0.01uF		DVT
3		modify 1.2V sequence(HW require)		40	change PR107 from 1K to 47K ;change PC72: : from 0.01uF to 0.047uF		PVT
4							
5							
6							
7							
8							
9							
10							
11							
8							
9							

<b>Compal Electronics, Inc.</b>			
Title			
PIR (PWR)			
Size	Document Number		Rev
			0.4
Date:	星期二, 四月 11, 2006	Sheet	43 of 43