

Dunlin LA-2601 Schematics Document

Intel Dothan / Alviso GM(PM) / DDR-2 / ICH6-M
(NV43/44M)

2005 / 03 / 14

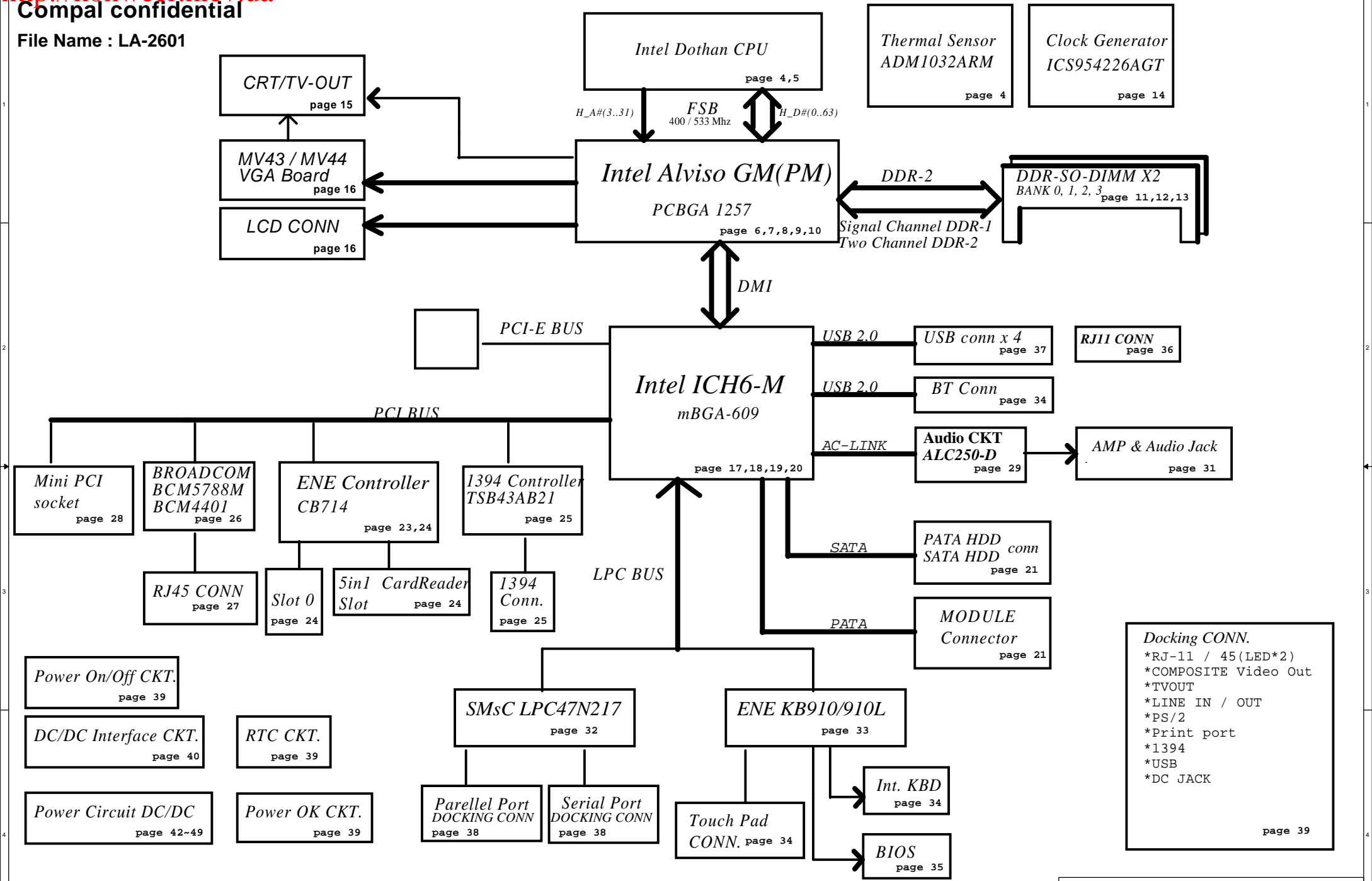
Rev:1.0

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- P44-PWR-Battery Select
- P45-PWR-3V/5V/12V
- P46-PWR-GMCH_CORE/1.8V/0.9V
- P47-PWR-1.5V/2.5V
- P48-PWR-CPU_CORE
- P49-PWR-OTP
- P50-PWR-PIR

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Docking CONN.
 *RJ-11 / 45(LED*2)
 *COMPOSITE Video Out
 *TVOUT
 *LINE IN / OUT
 *PS/2
 *Print port
 *1394
 *USB
 *DC JACK

page 39

Voltage Rails

Power Plane	Description	S1	S3	S5
VIN	Adapter power supply (19V)	N/A	N/A	N/A
B+	AC or battery power rail for power circuit.	N/A	N/A	N/A
+CPU_CORE	Core voltage for CPU	ON	OFF	OFF
+1.05VS	1.05V switched power rail	ON	OFF	OFF
+DDRVTT	1.25V switched power rail for DDR terminator	ON	OFF	OFF
+1.5VALW	1.5V always on power rail	ON	ON	ON*
+1.5VS	1.5V switched power rail	ON	OFF	OFF
+1.8VS	1.8V switched power rail	ON	OFF	OFF
+DDRVC	2.5V power rail for DDR	ON	ON	OFF
+2.5VS	2.5V switched power rail	ON	OFF	OFF
+3VALW	3.3V always on power rail	ON	ON	ON*
+3V	3.3V power rail	ON	ON	OFF
+3VS	3.3V switched power rail	ON	OFF	OFF
+5VALW	5V always on power rail	ON	ON	ON*
+5VS	5V switched power rail	ON	OFF	OFF
+5VMOD	5V switched power rail for Module Bay	ON	OFF	OFF
+12VALW	12V always on power rail	ON	ON	ON*
+RTCVCC	RTC power	ON	ON	ON

Note : ON* means that this power plane is ON only with AC power available, otherwise it is OFF.

External PCI Devices

Device	IDSEL#	REQ#/GNT#	Interrupts
CardBus	AD20	2	PIRQA/PIRQB
1394	AD16	0	PIRQE
SD	AD20	2	PIRQA/PIRQB
Mini-PCI	AD18	1	PIRQG/PIRQH
LAN	AD17	3	PIRQF

EC SM Bus1 address

Device	Address
Smart Battery	0001 011X b
EEPROM(24C16/02)	1010 000X b
(24C04)	1011 000Xb

EC SM Bus2 address

Device	Address
ADM1032	1001 110X b
2nd Battery	1001 011X b

ICH6M SM Bus address

Device	Address
Clock Generator (ICS 952623)	1101 001Xb
DDR DIMM0	1010 000Xb
DDR DIMM2	1010 010Xb

STATE	SIGNAL	SLP_S1#	SLP_S3#	SLP_S4#	SLP_S5#	+VALW	+V	+VS	Clock
Full ON		HIGH	HIGH	HIGH	HIGH	ON	ON	ON	ON
S1(Power On Suspend)		LOW	HIGH	HIGH	HIGH	ON	ON	ON	LOW
S3 (Suspend to RAM)		LOW	LOW	HIGH	HIGH	ON	ON	OFF	OFF
S4 (Suspend to Disk)		LOW	LOW	LOW	HIGH	ON	OFF	OFF	OFF
S5 (Soft OFF)		LOW	LOW	LOW	LOW	ON	OFF	OFF	OFF

Board ID / SKU ID Table for AD channel

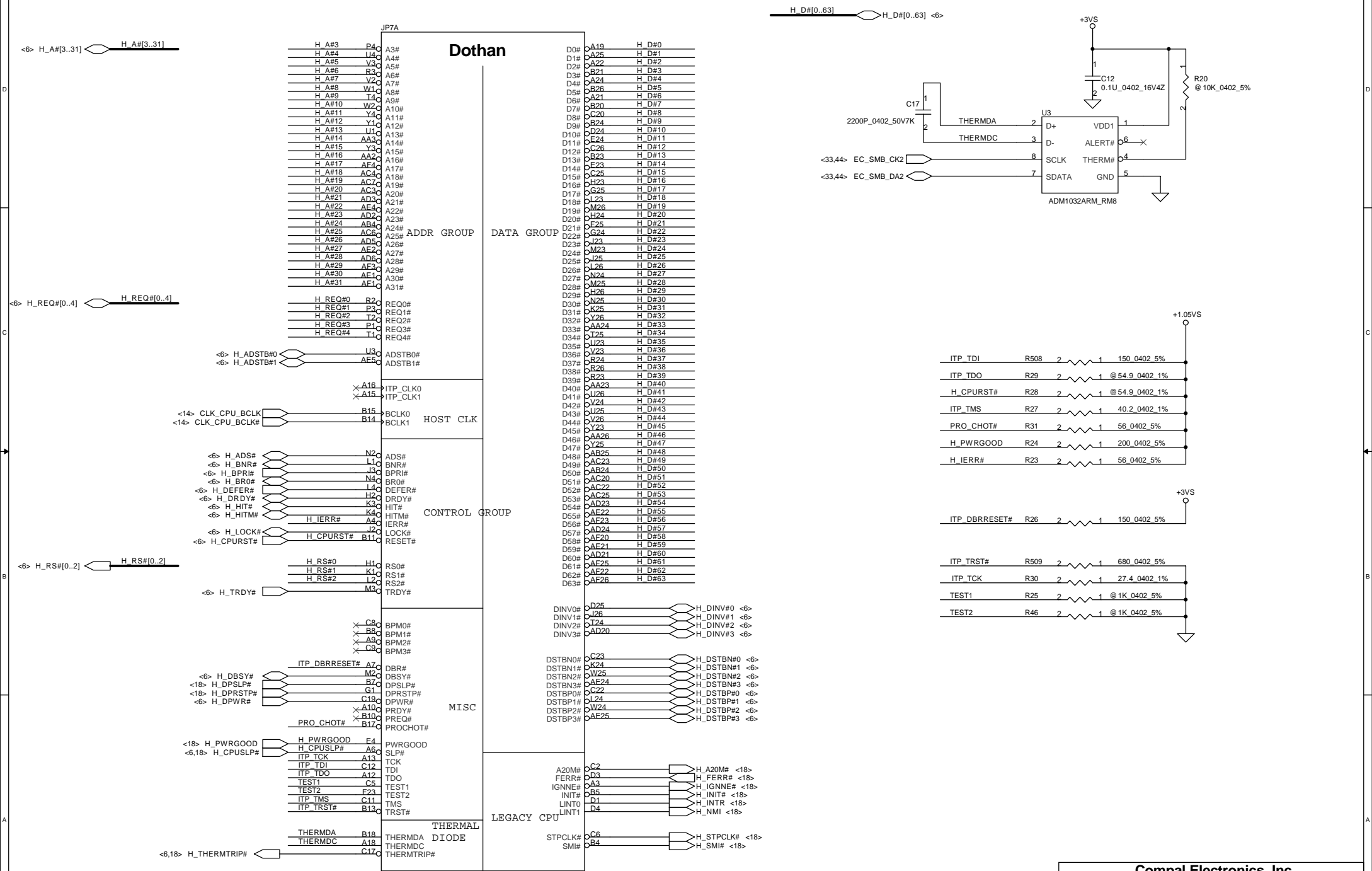
Vcc	3.3V +/- 5%			
Ra / Rc	100K +/- 5%			
Board ID	Rb / Rd	V _{AD_BID} min	V _{AD_BID} typ	V _{AD_BID} max
0	0	0 V	0 V	0 V
1	8.2K +/- 5%	0.216 V	0.250 V	0.289 V
2	18K +/- 5%	0.436 V	0.503 V	0.538 V
3	33K +/- 5%	0.712 V	0.819 V	0.875 V
4	56K +/- 5%	1.036 V	1.185 V	1.264 V
5	100K +/- 5%	1.453 V	1.650 V	1.759 V
6	200K +/- 5%	1.935 V	2.200 V	2.341 V
7	NC	2.500 V	3.300 V	3.300 V

BOARD ID Table

Board ID	PCB Revision
0	0.1
1	
2	
3	
4	
5	
6	
7	

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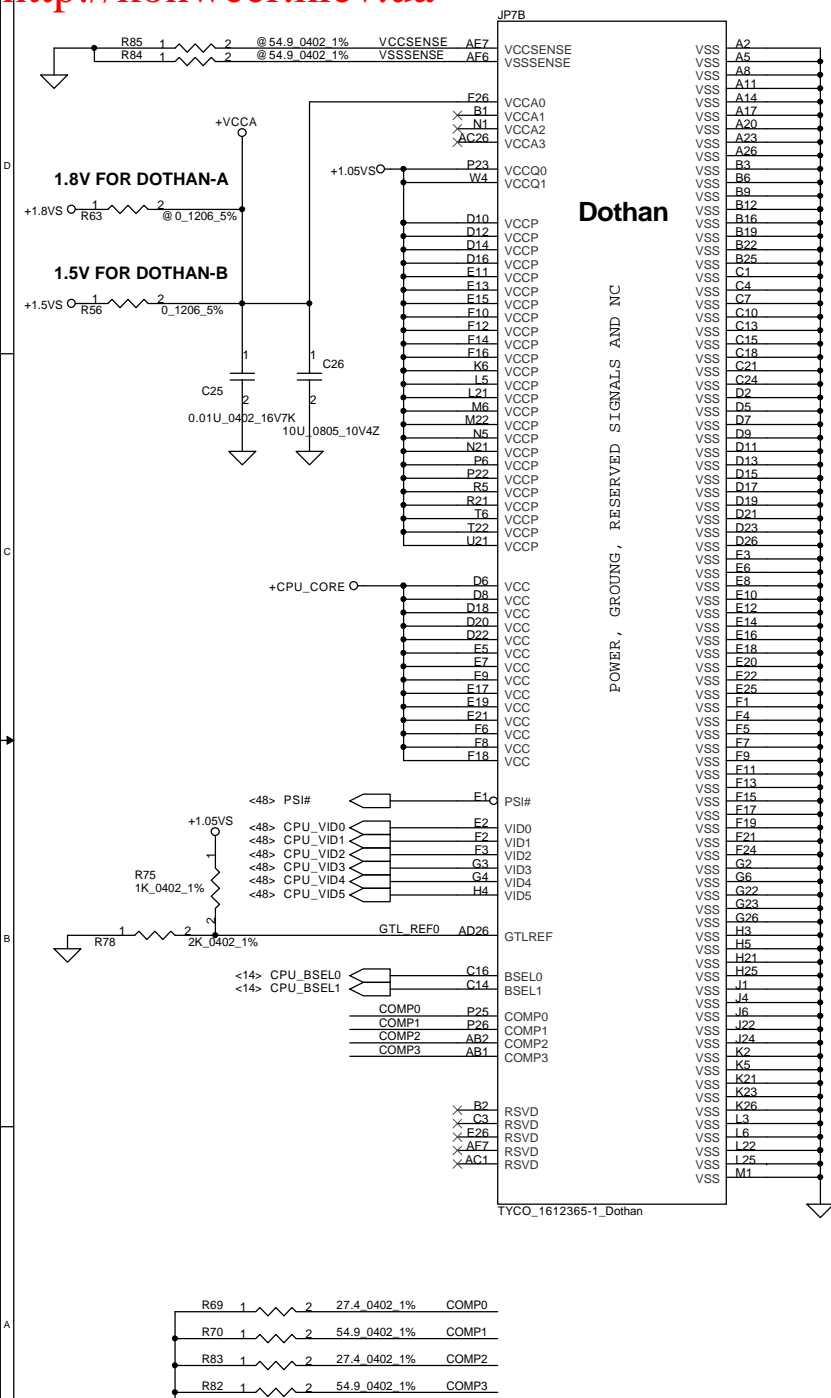
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THERMDA & THERMDC Trace / Space = 10 / 10 mil

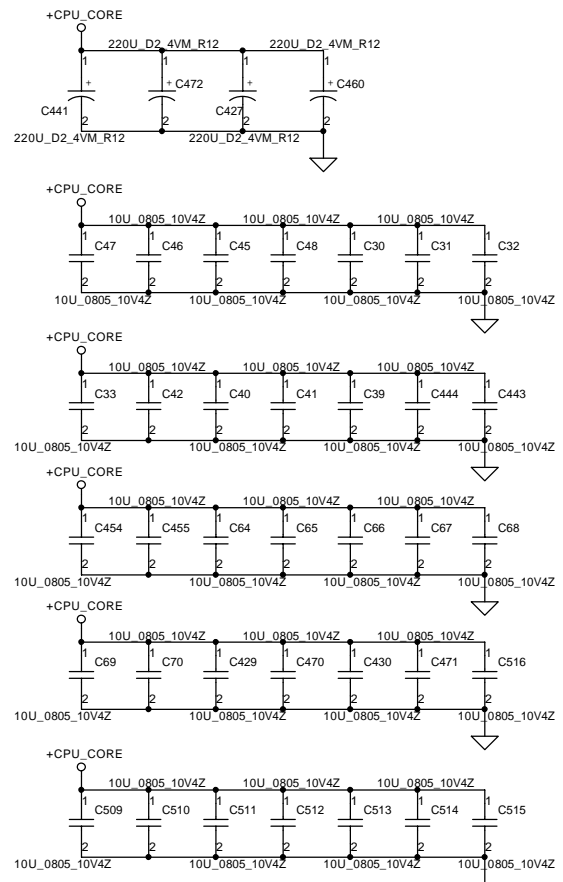
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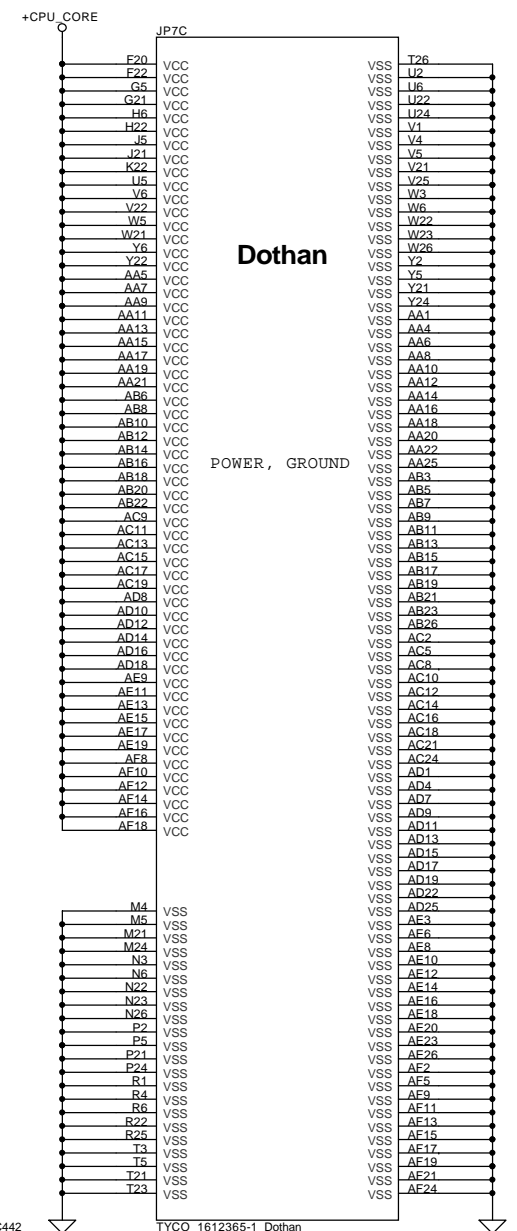
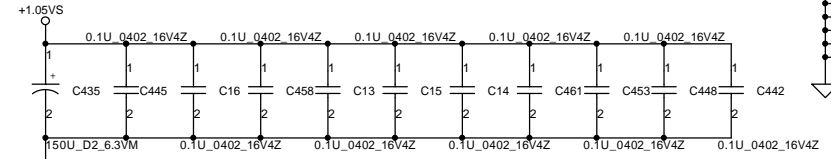


Dothan

POWER, GROUND, RESERVED SIGNALS AND NC



Vcc-core Decoupling	C, uF	ESR, mohm	ESL, nH
SPCAP, Polymer	3X330uF	9m ohm/3	3.5nH/4
MLCC 0805 X5R	35X10uF	5m ohm/35	0.6nH/35



Dothan

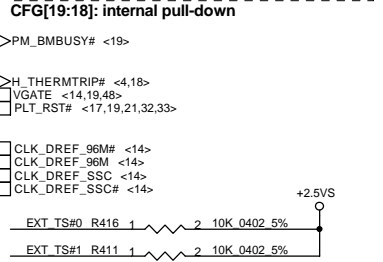
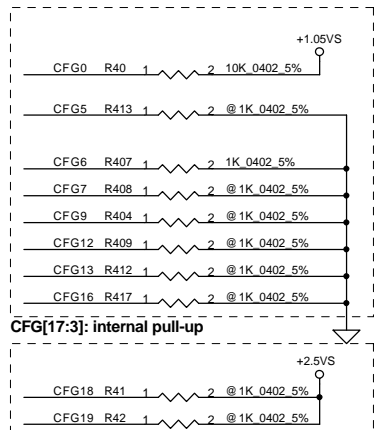
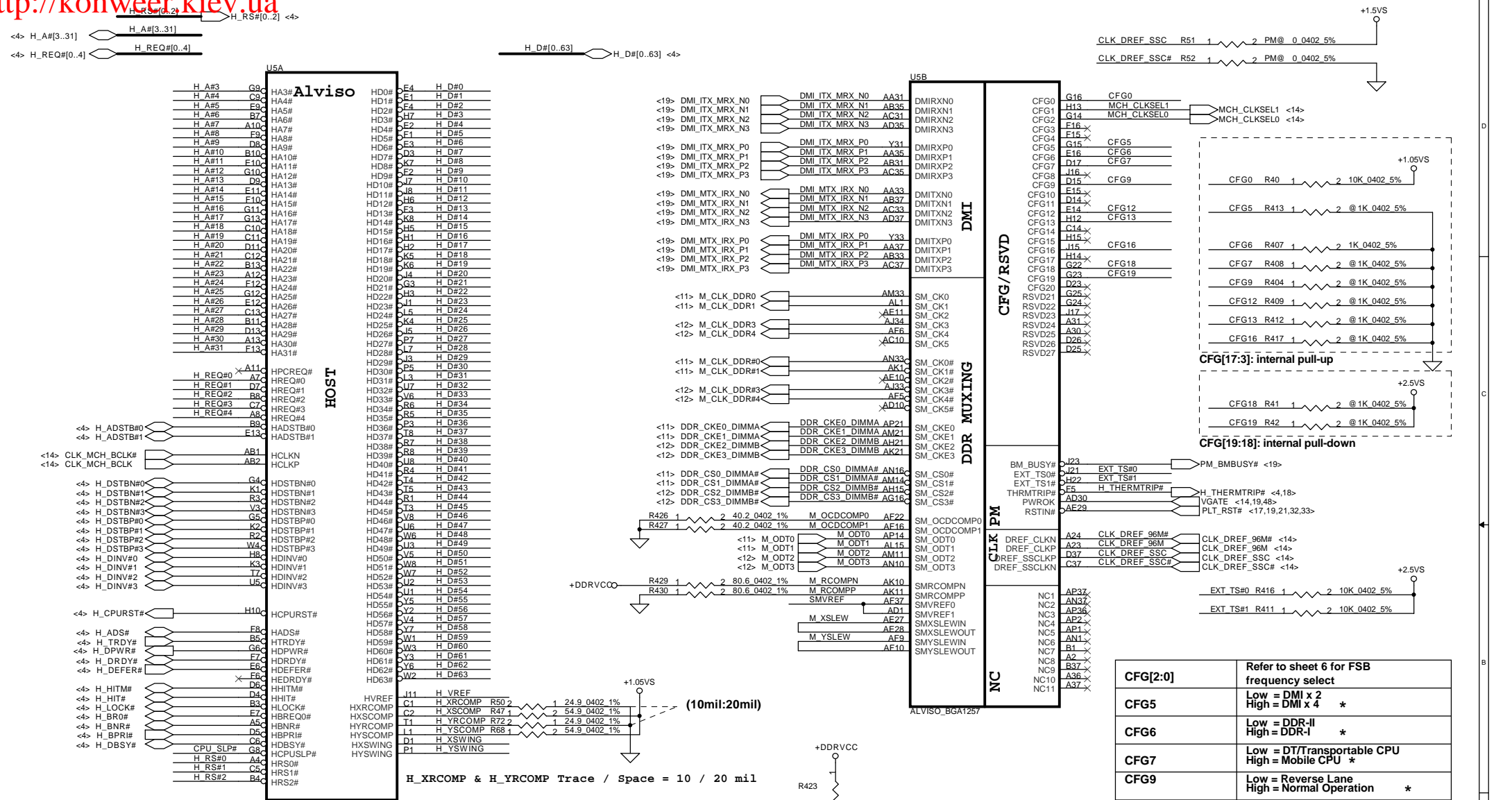
POWER, GROUND

TRACE CLOSELY CPU < 0.5"

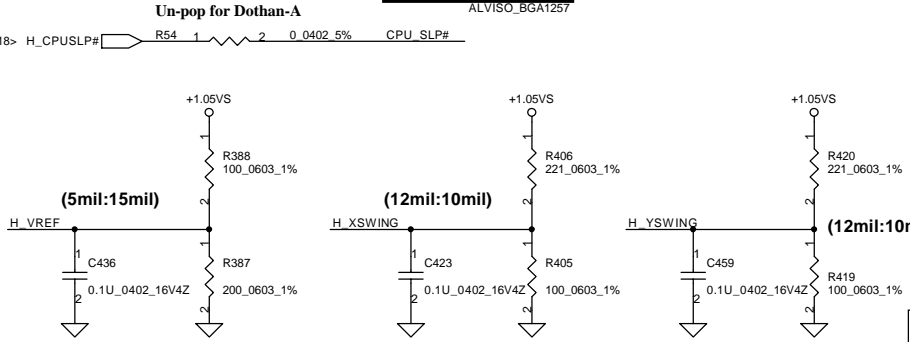
COMP0, COMP2 layout : Width 18mils and Space 25mils
 COMP1, COMP3 layout : Space 25mils

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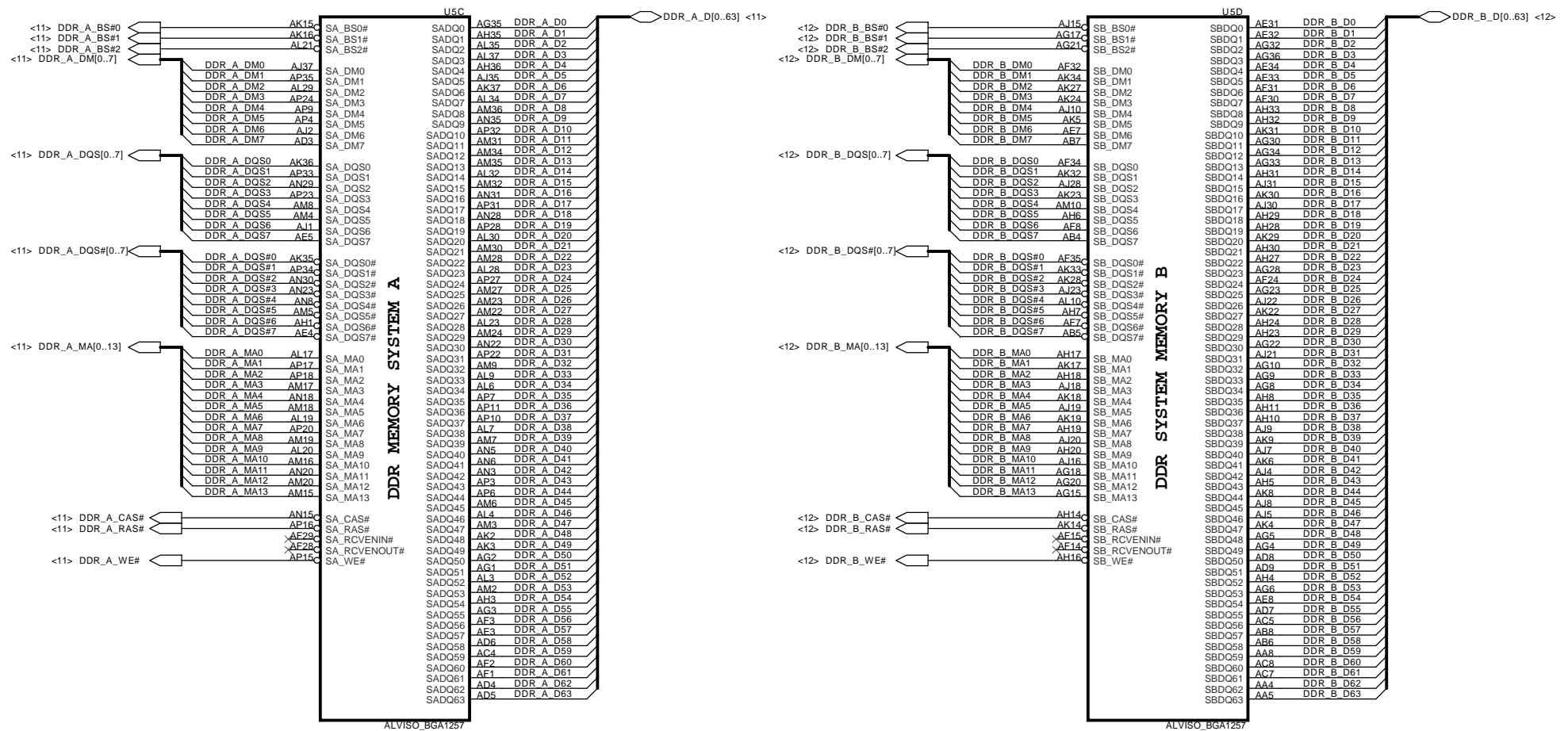
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CFG	Refer to sheet 6 for FSF frequency select
CFG5	Low = DMI x 2 High = DMI x 4 *
CFG6	Low = DDR-II High = DDR-I *
CFG7	Low = DT/transportable CPU High = Mobile CPU *
CFG9	Low = Reverse Lane High = Normal Operation *
CFG[13:12]	00 = Reserved 01 = XOR Mode Enabled 10 = All Z Mode Enabled 11 = Normal Operation (Default) *
CFG16 (FSB Dynamic ODT)	Low = Disabled High = Enabled *
CFG18 (VCC Select)	Low = 1.05V (Default) * High = 1.5V
CFG19 (VTT Select)	Low = 1.05V (Default) * High = 1.2V



H_XRCOMP & H_YRCOMP Trace / Space = 10 / 20 mil

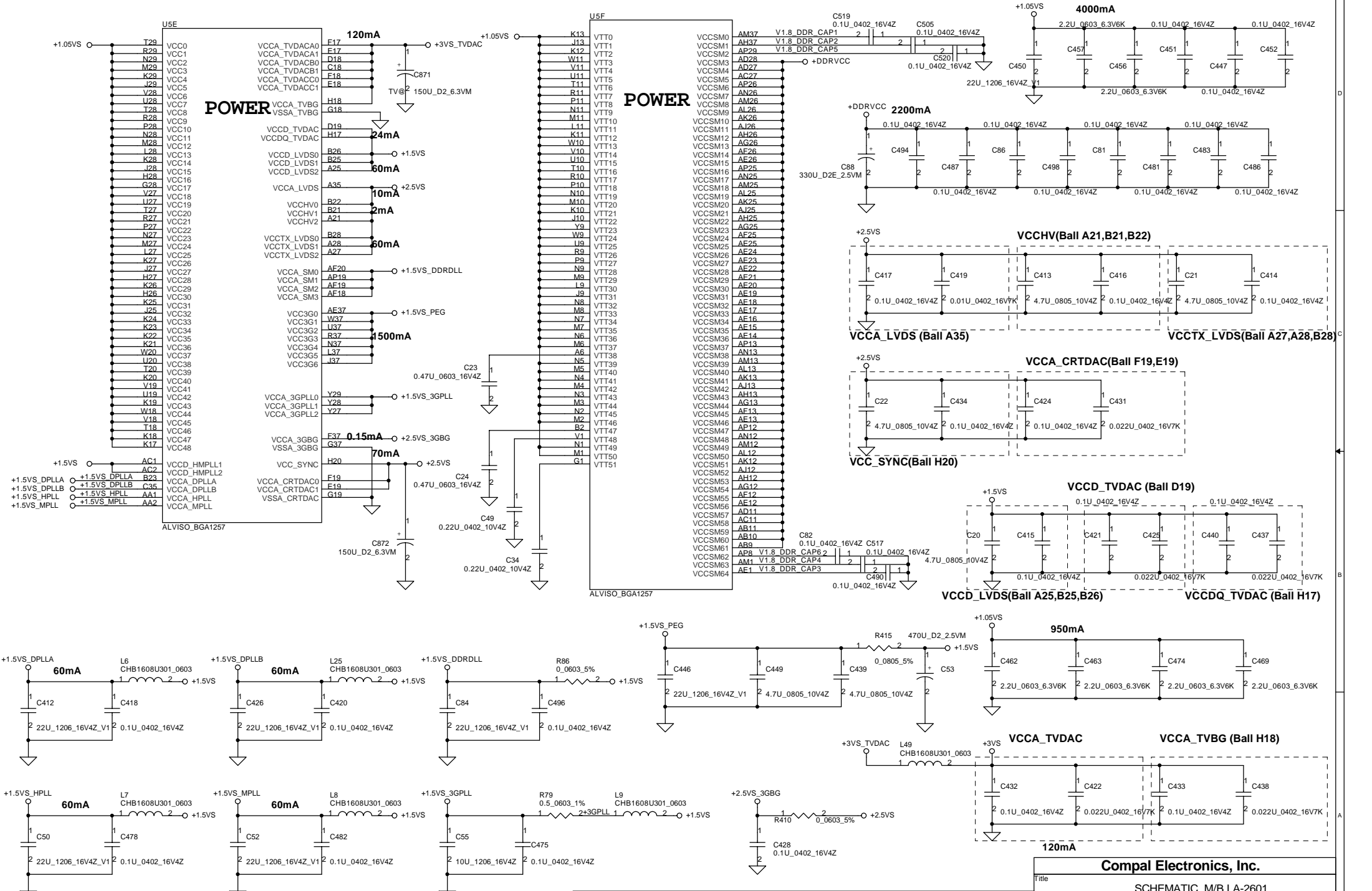


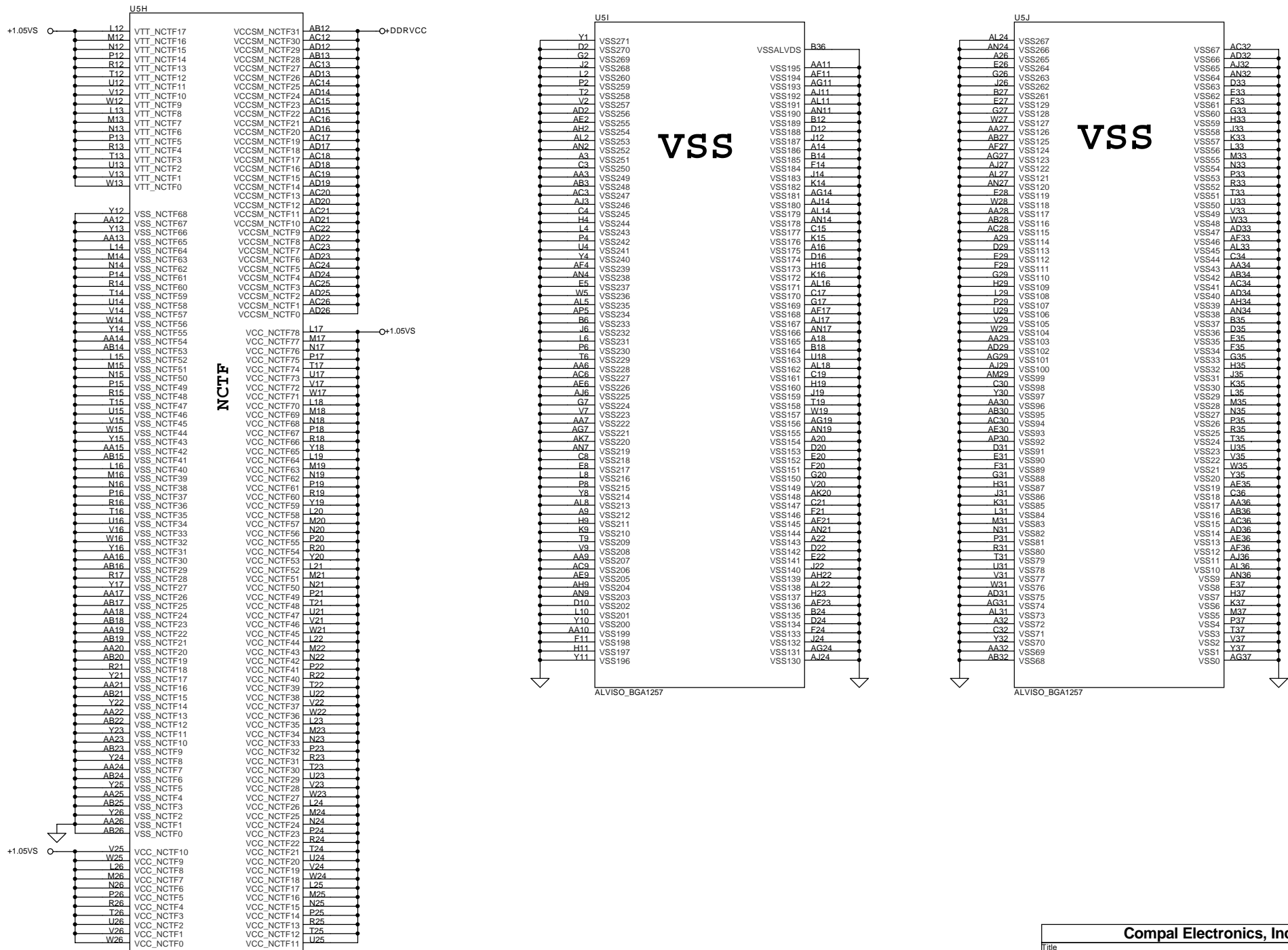
ALVISO_BGAT257

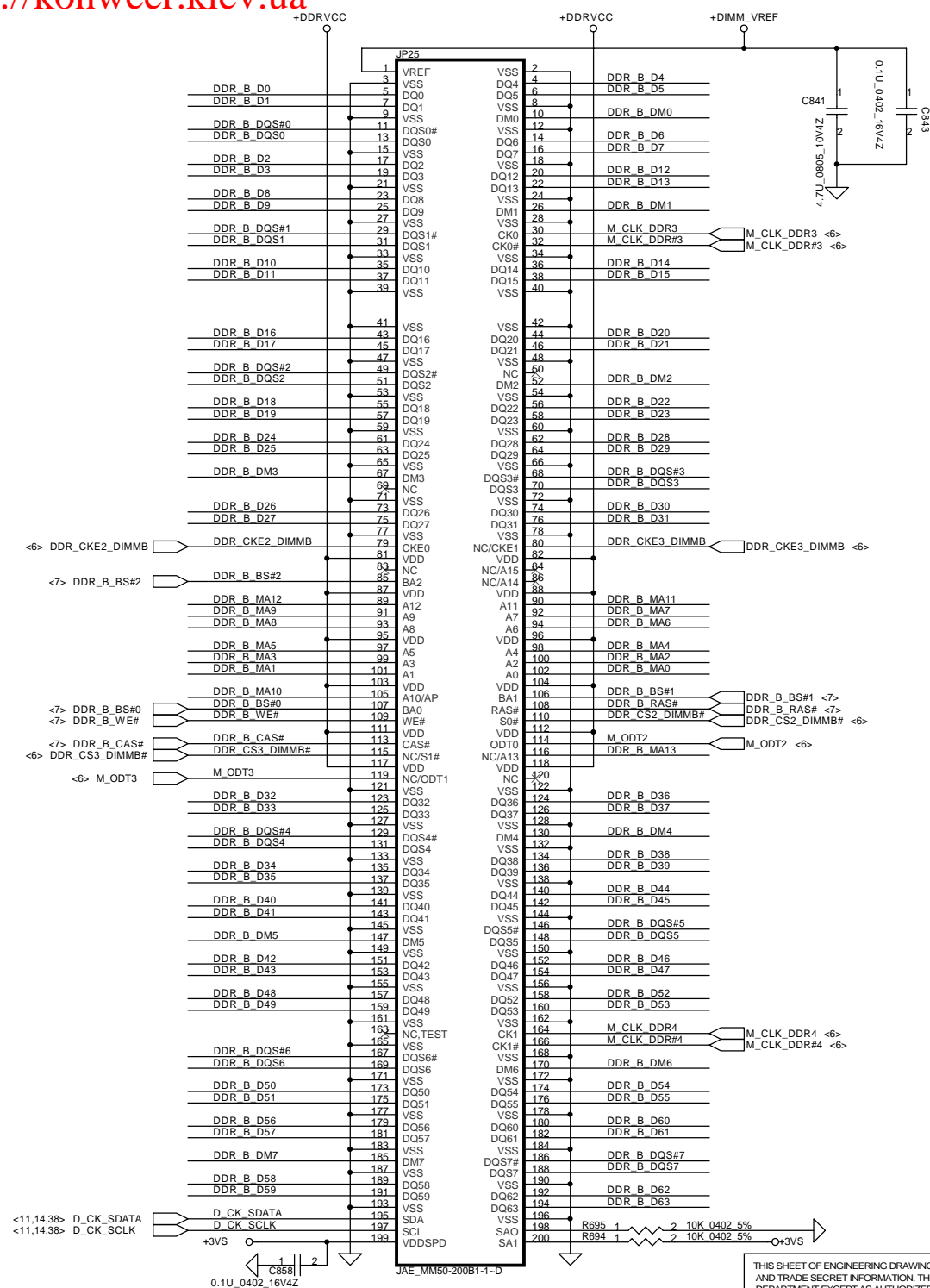
ALVISO_BGAT257

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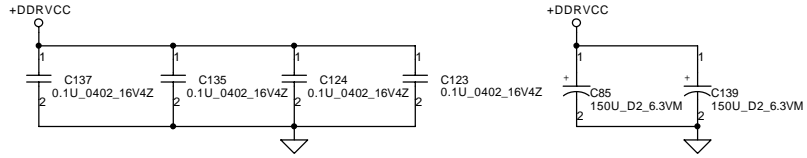
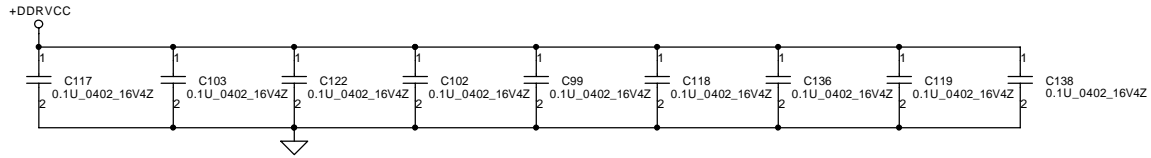
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Layout note :
Distribute as close as possible
to DDR-SODIMM.



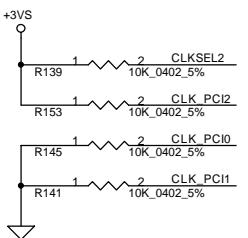
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FSC	FSB	FSA	CPU MHz	SRC MHz	PCI MHz
1	0	1	100	100	33.3
0	0	1	133	100	33.3
0	1	1	166	100	33.3
0	1	0	200	100	33.3

Table : ICS 954206B

CLK_PCI2 = 1, Pin 32,33 are PEREQ# pin



CLK_PCIO = 0, Pin 35,36 are PCIe CLK pair

CLK_PC1 = 0, Pin 17,18 are 96MHz



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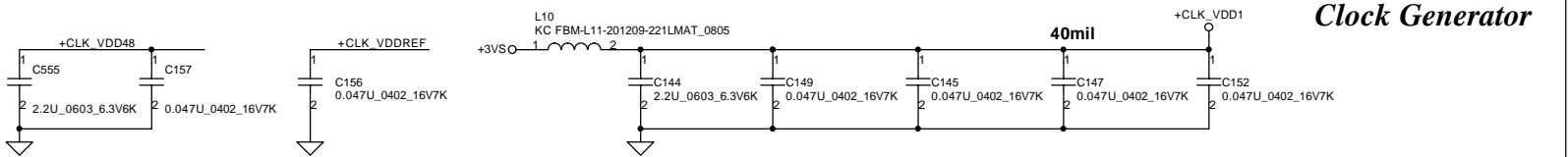
CLK_PCIO = 0, Pin 35,36 are PCIe CLK pair

CLK_PC1 = 0, Pin 17,18 are 96MHz

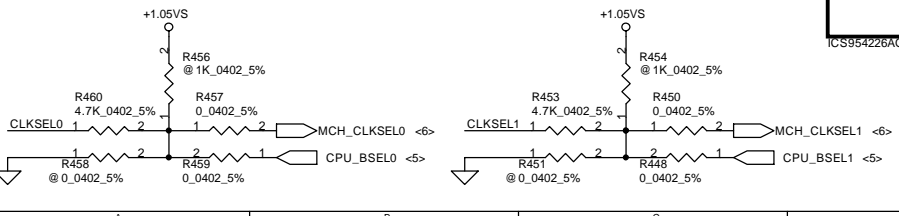
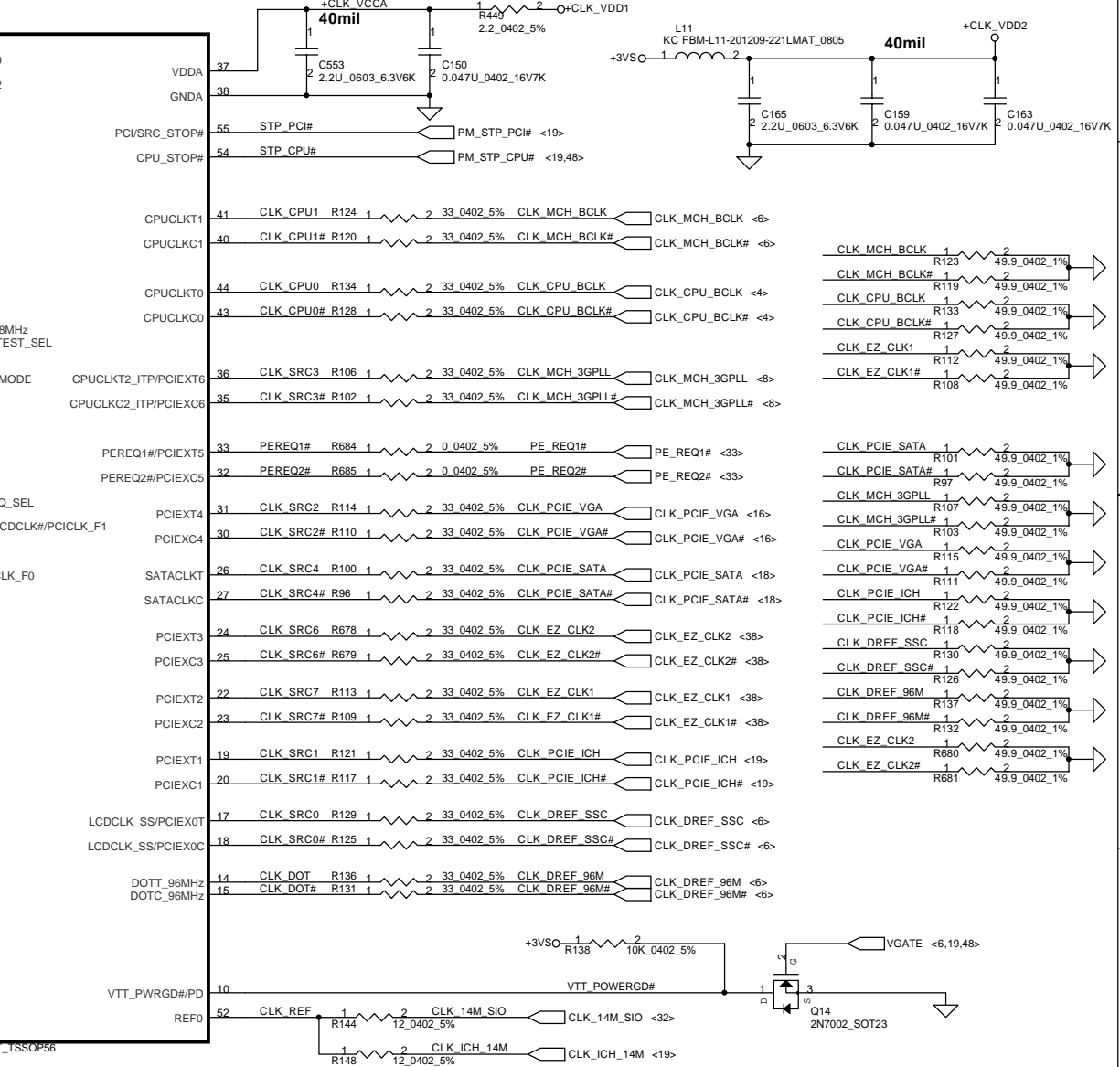
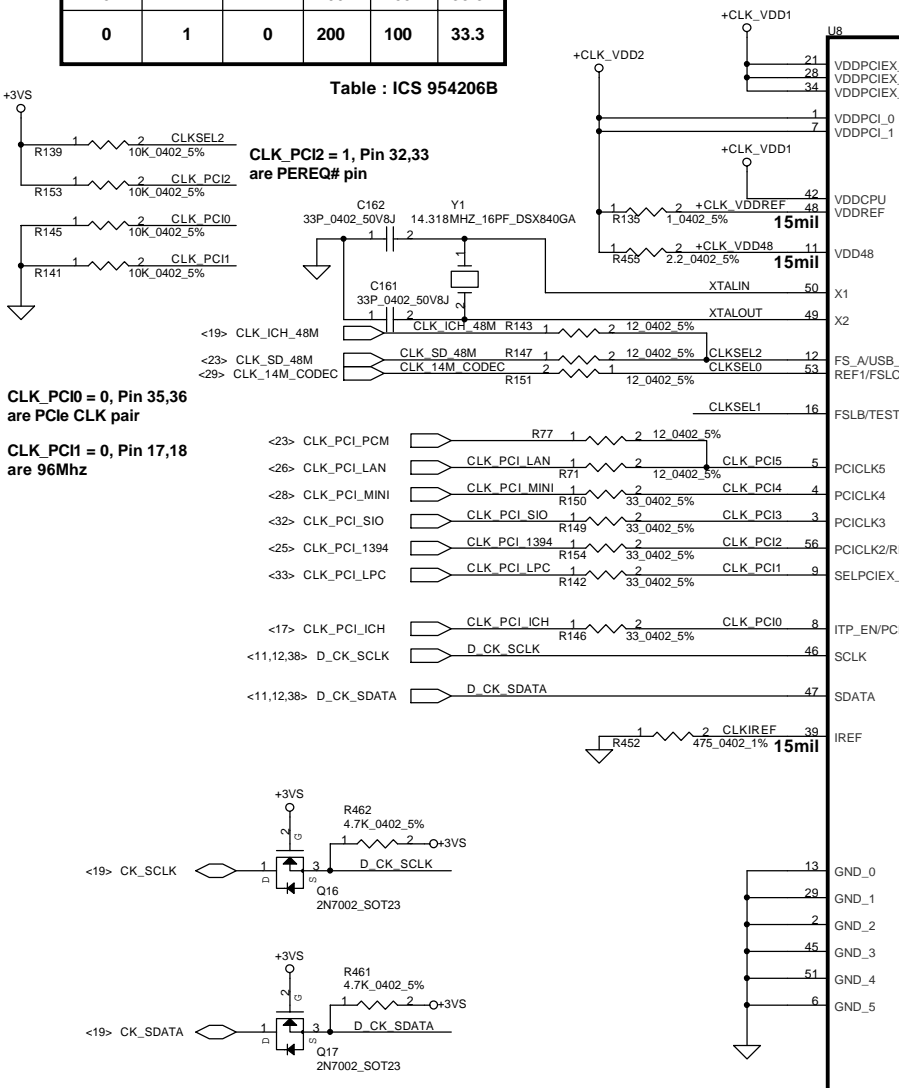
CLK_PCI2 = 1, Pin 32,33 are PEREQ# pin

CLK_PCIO = 0, Pin 35,36 are PCIe CLK pair

CLK_PC1 = 0, Pin 17,18 are 96MHz



Clock Generator



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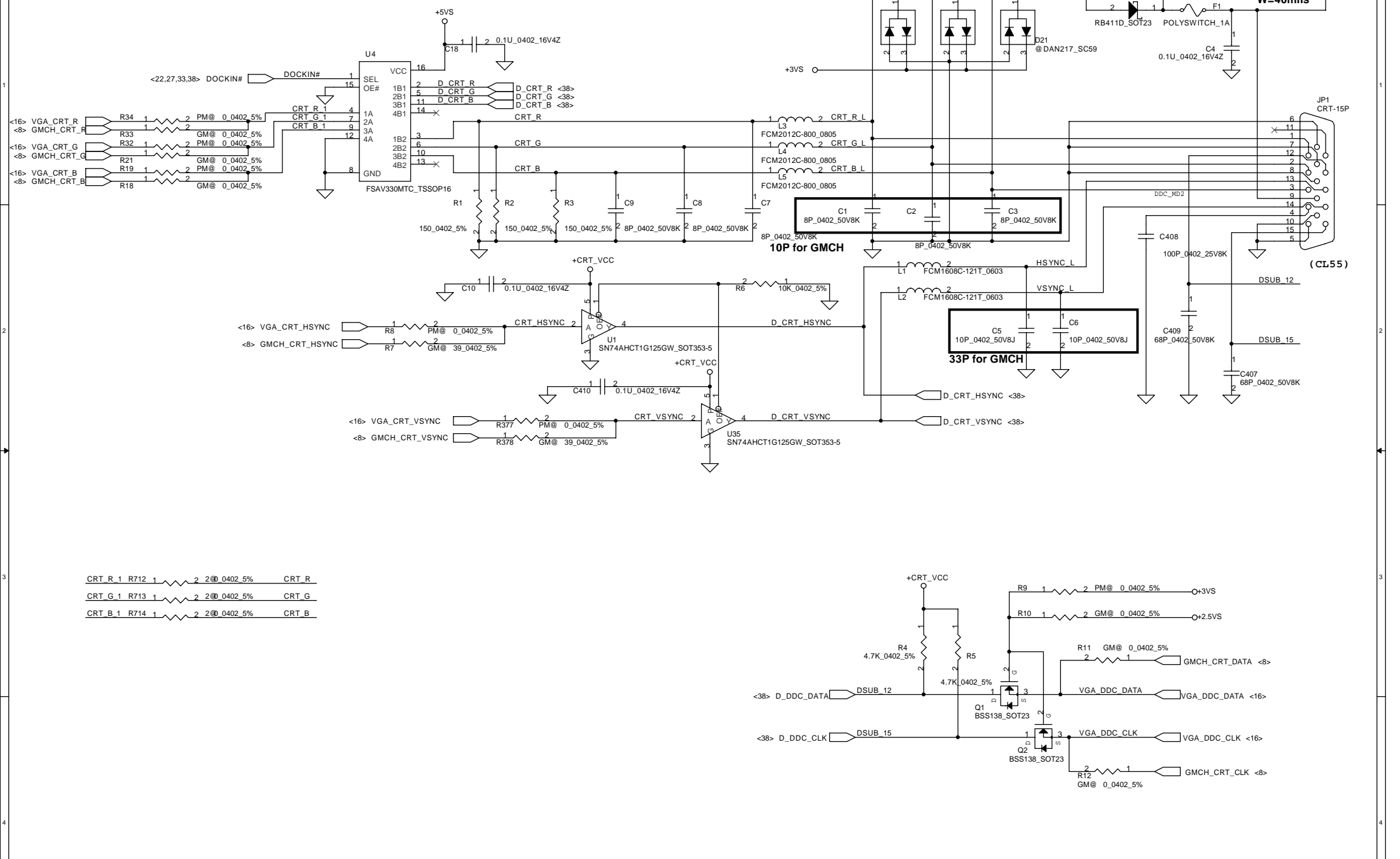
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CRT Connector

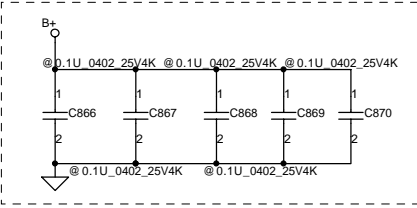
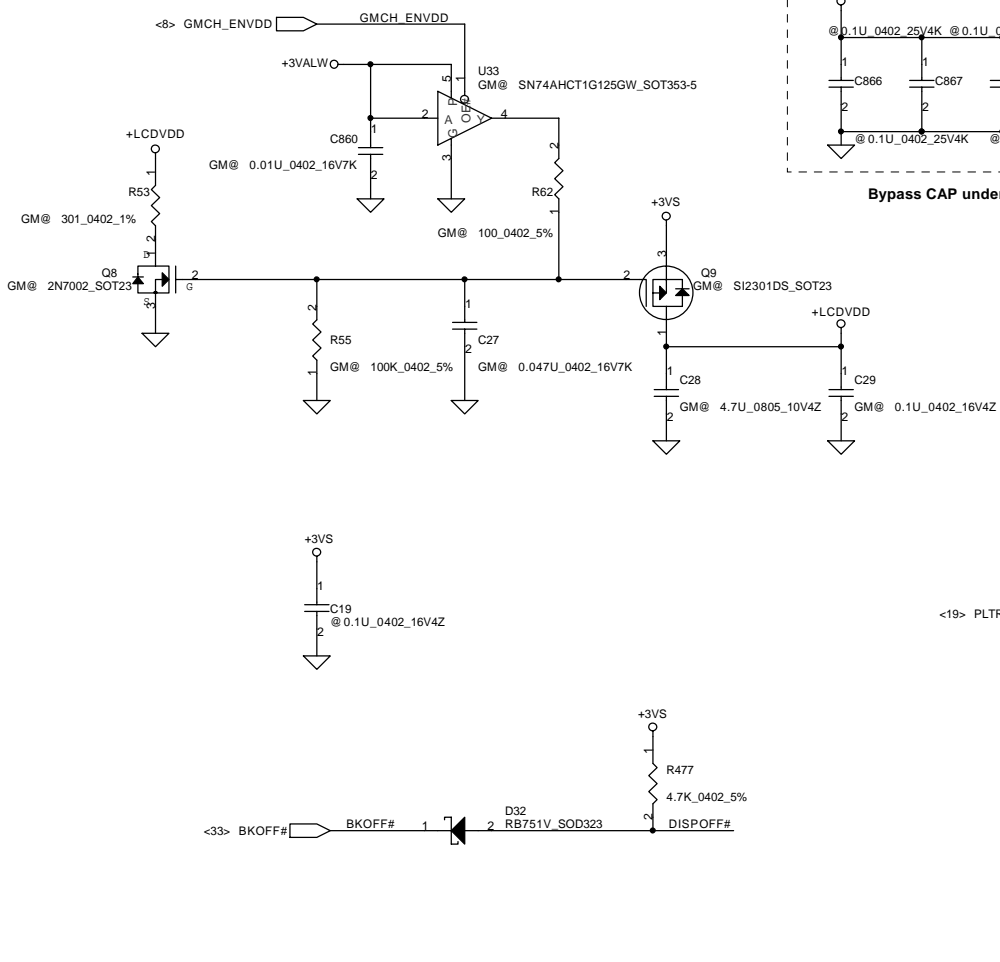


- CRT_R_1 R712 1 2 2@ 0.0402_5% CRT_R
- CRT_G_1 R713 1 2 2@ 0.0402_5% CRT_G
- CRT_B_1 R714 1 2 2@ 0.0402_5% CRT_B

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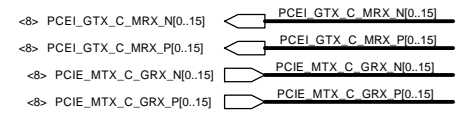
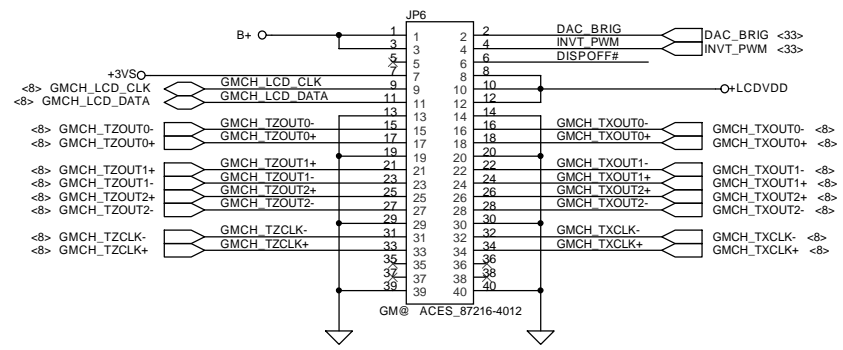
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LCD POWER CIRCUIT

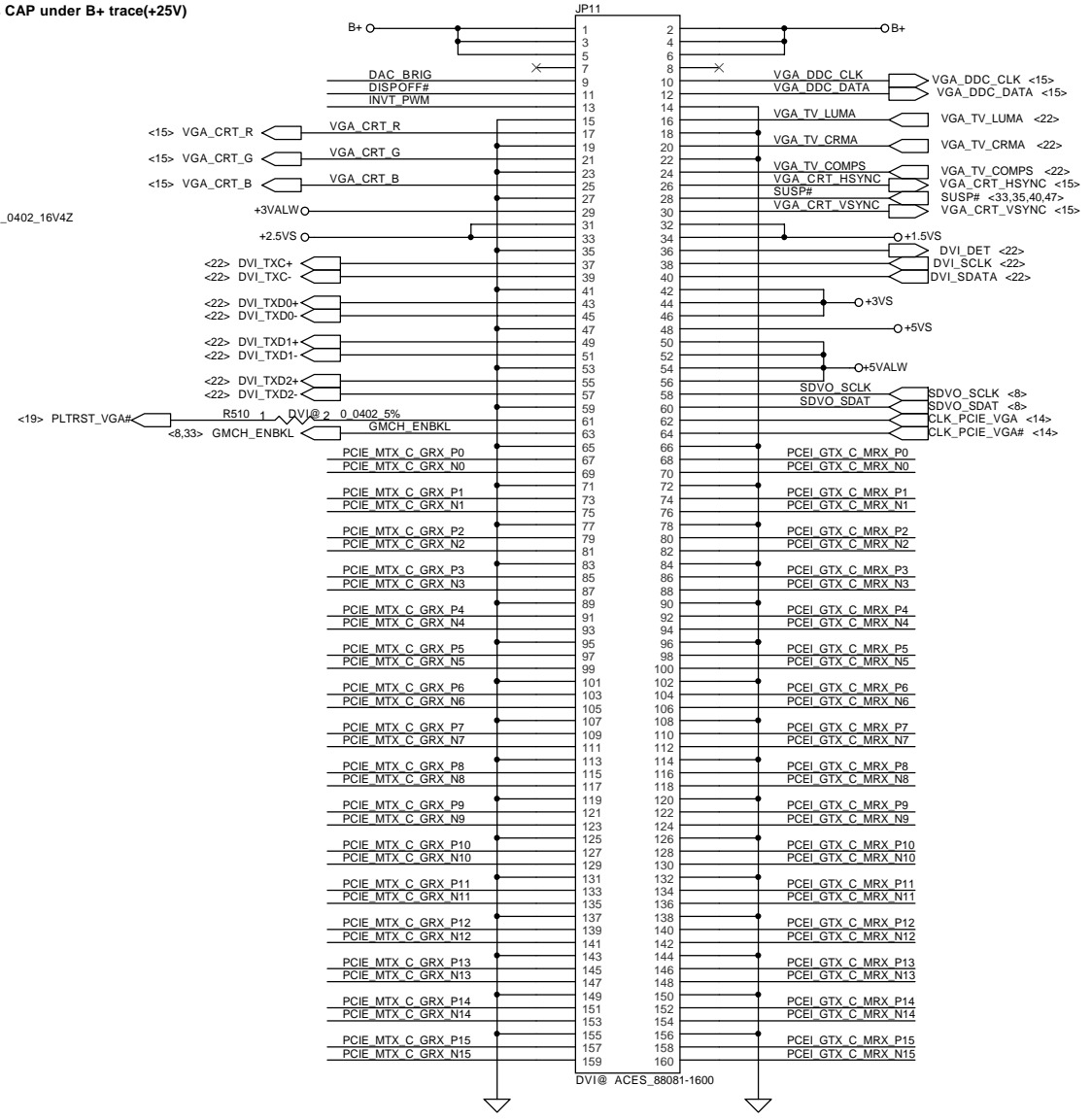


Bypass CAP under B+ trace(+25V)

LCD/PANEL BD. Conn.

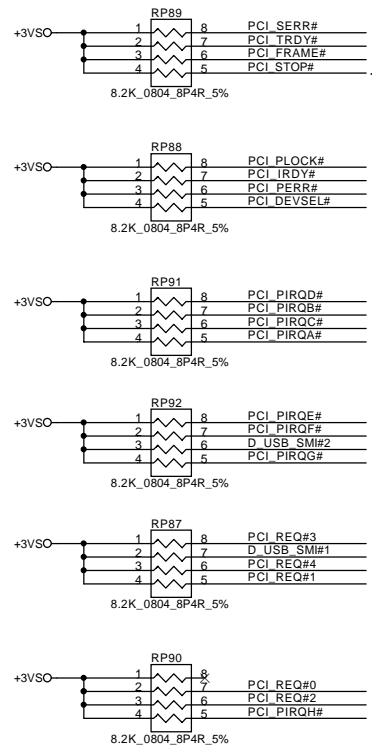


VGA BOARD Conn.



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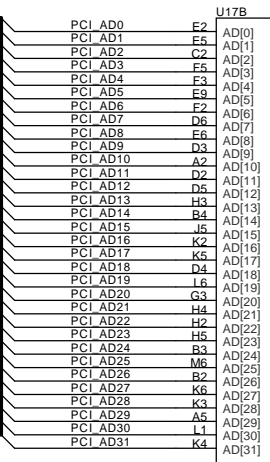
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<23,25,26,28> PCI_AD[0..31]

<23,25,26,28> PCI_FRAME#

<23> PCI_PIRQA#
<23> PCI_PIRQB#



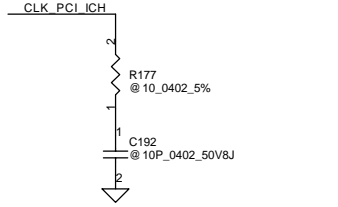
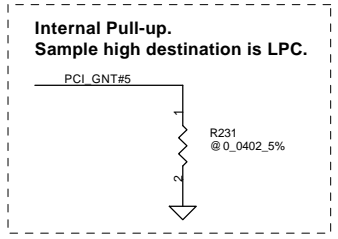
PCI

Interrupt I/F

RESERVED

- × AC5 SATA[1]RXN/RSVD[1]
- × AD5 SATA[1]RXP/RSVD[2]
- × AF4 SATA[1]TXN/RSVD[3]
- × AG4 SATA[1]TXP/RSVD[4]
- × AC9 SATA[3]RXN/RSVD[5]
- × AD9 SATA[3]RXP/RSVD[6]
- × AF8 SATA[3]TXN/RSVD[7]
- × AG8 SATA[3]TXP/RSVD[8]
- × U3 TP[3]/RSVD[9]

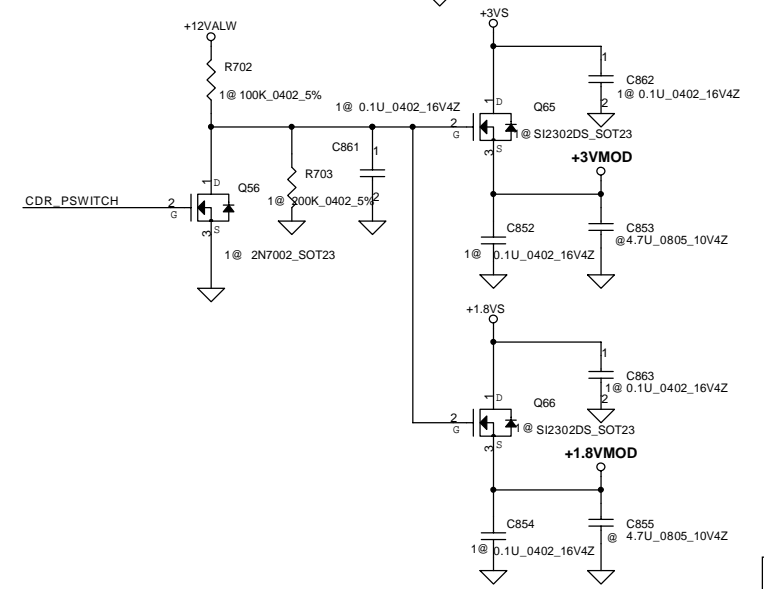
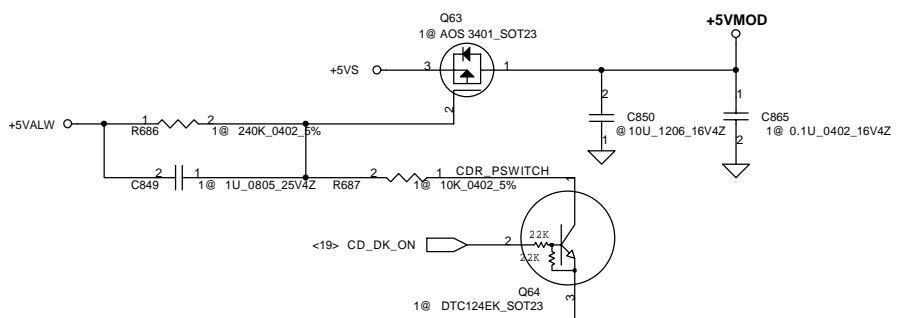
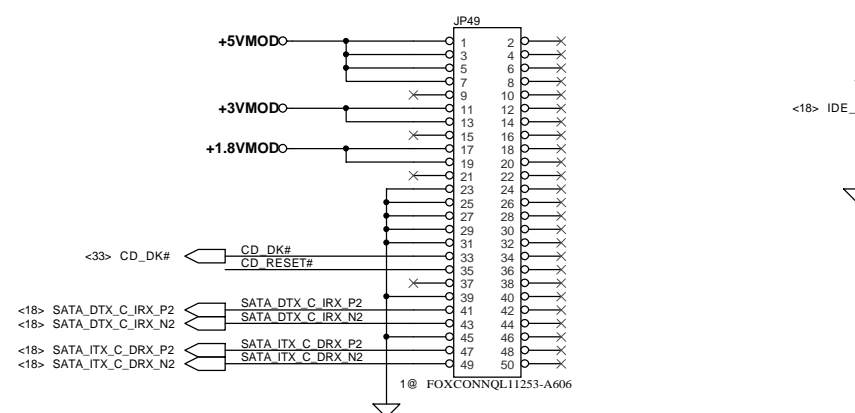
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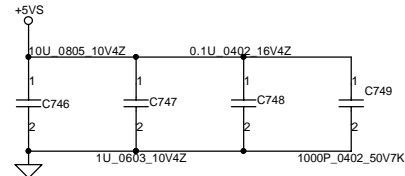
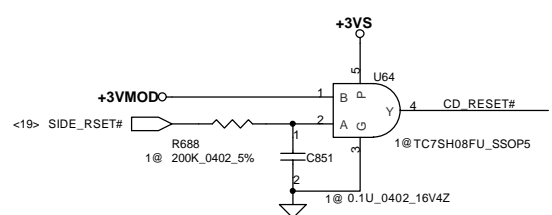
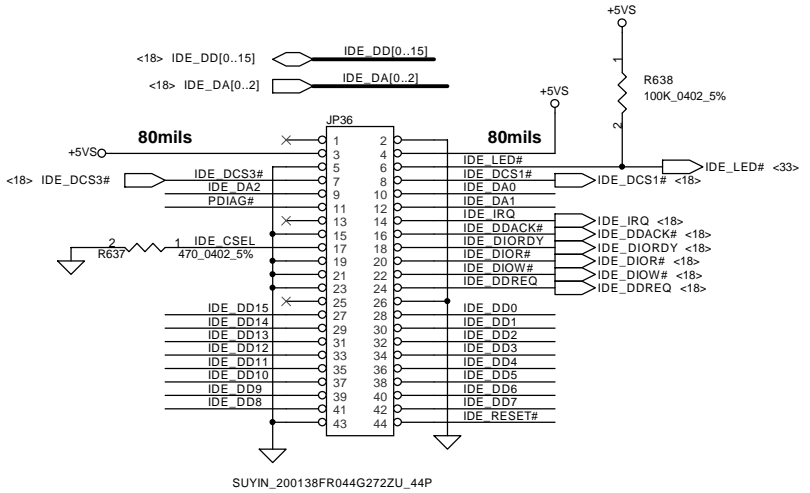
Compal Electronics, Inc.		
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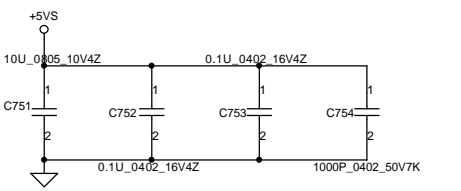
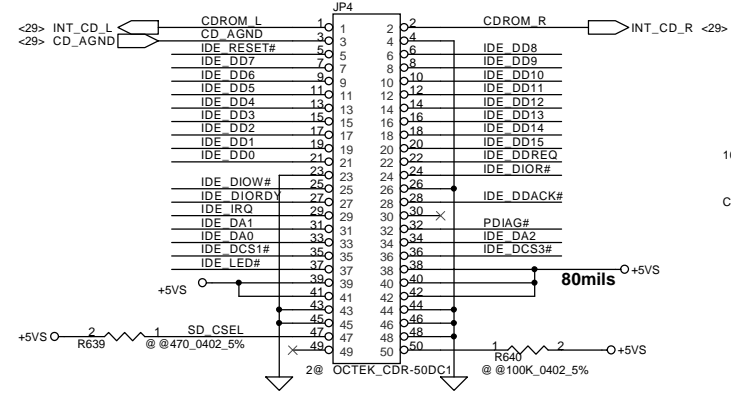
HOT CDROM CONN(DCL55)



HDD CONN



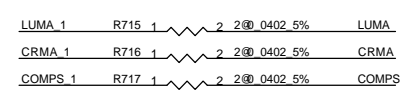
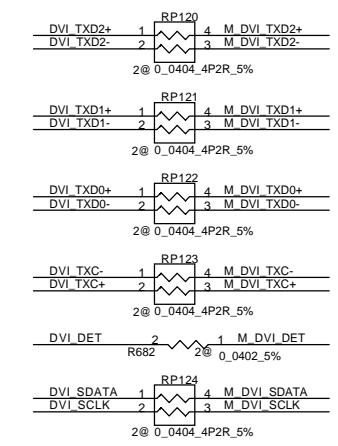
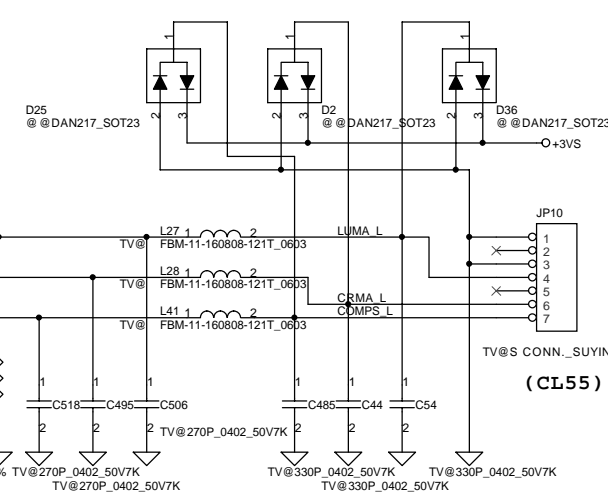
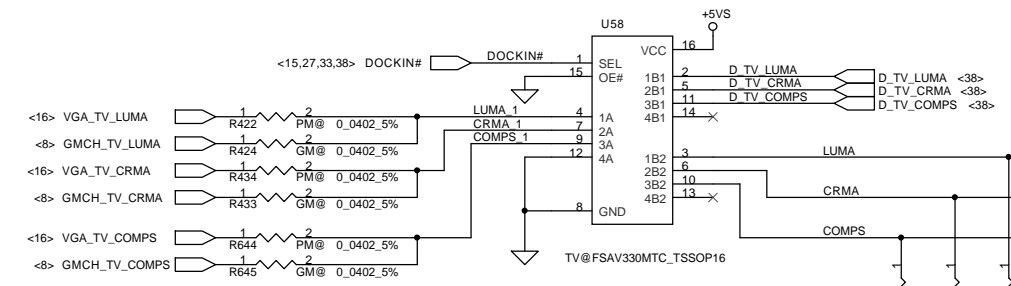
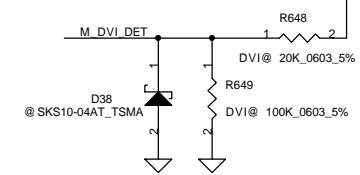
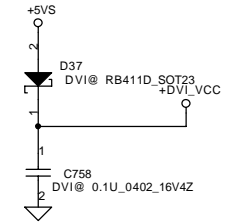
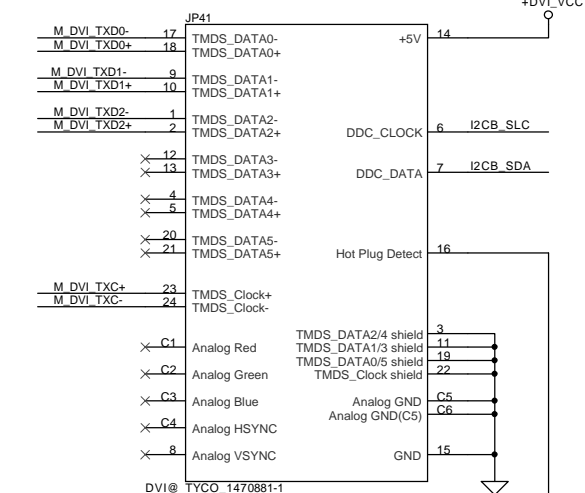
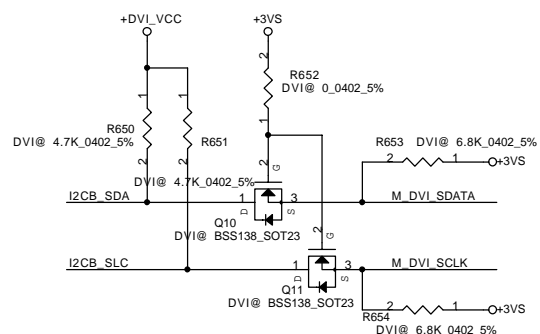
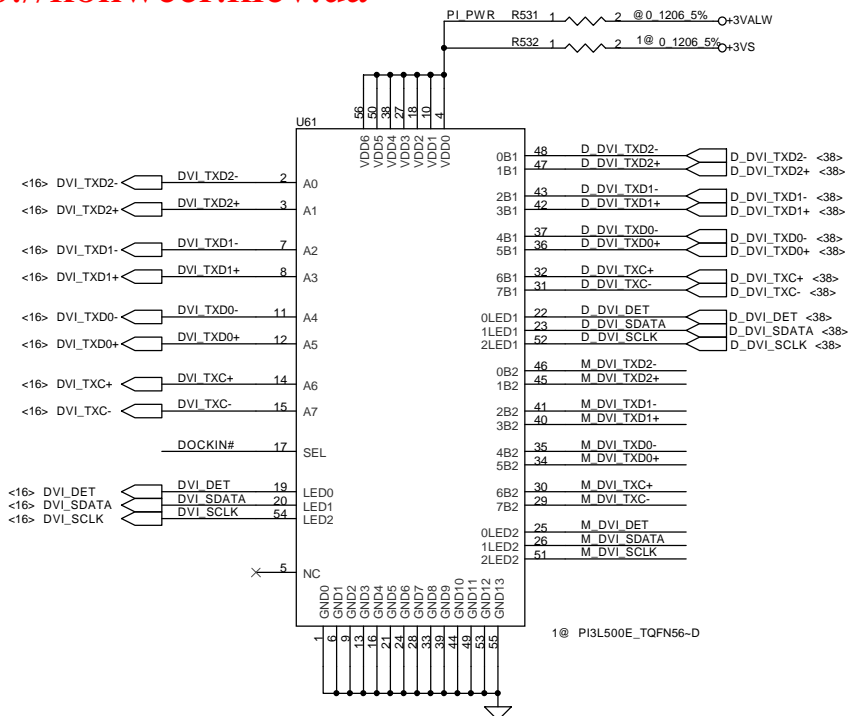
CDROM CONN



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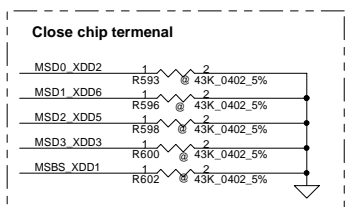
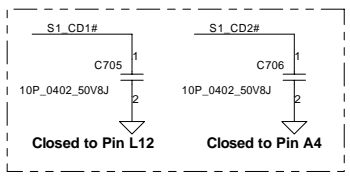
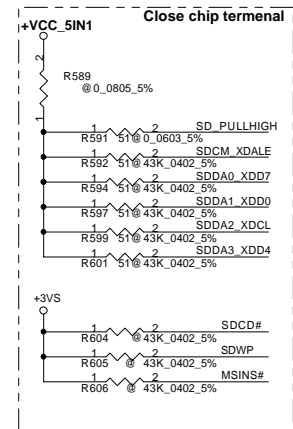
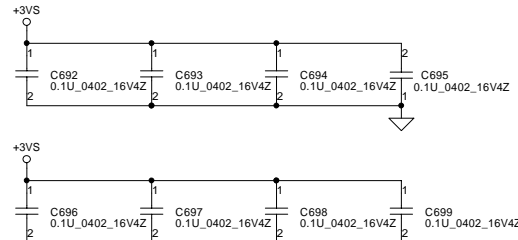
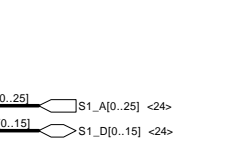
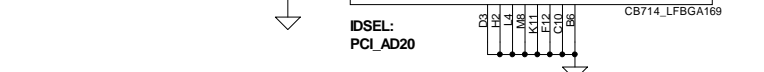
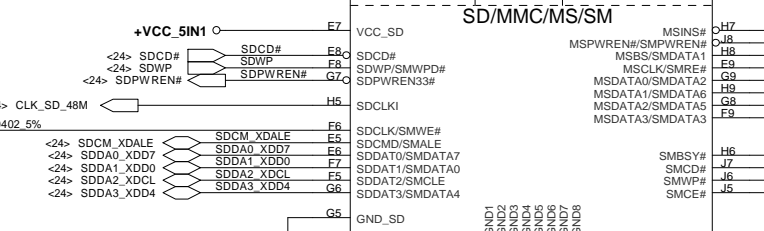
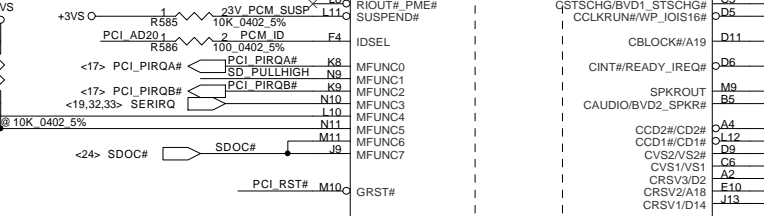
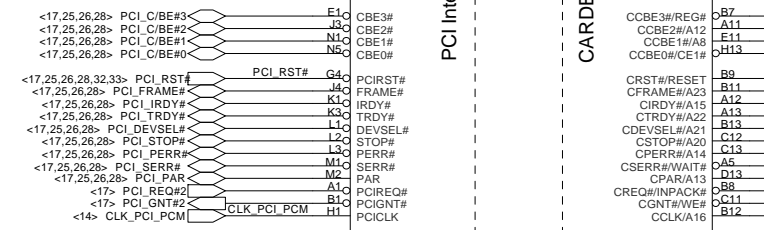
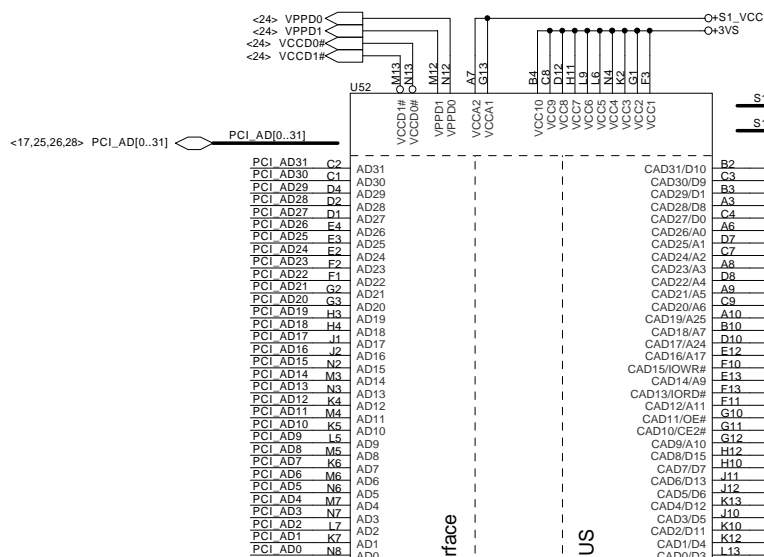
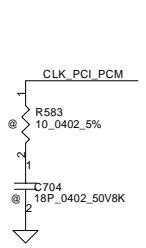
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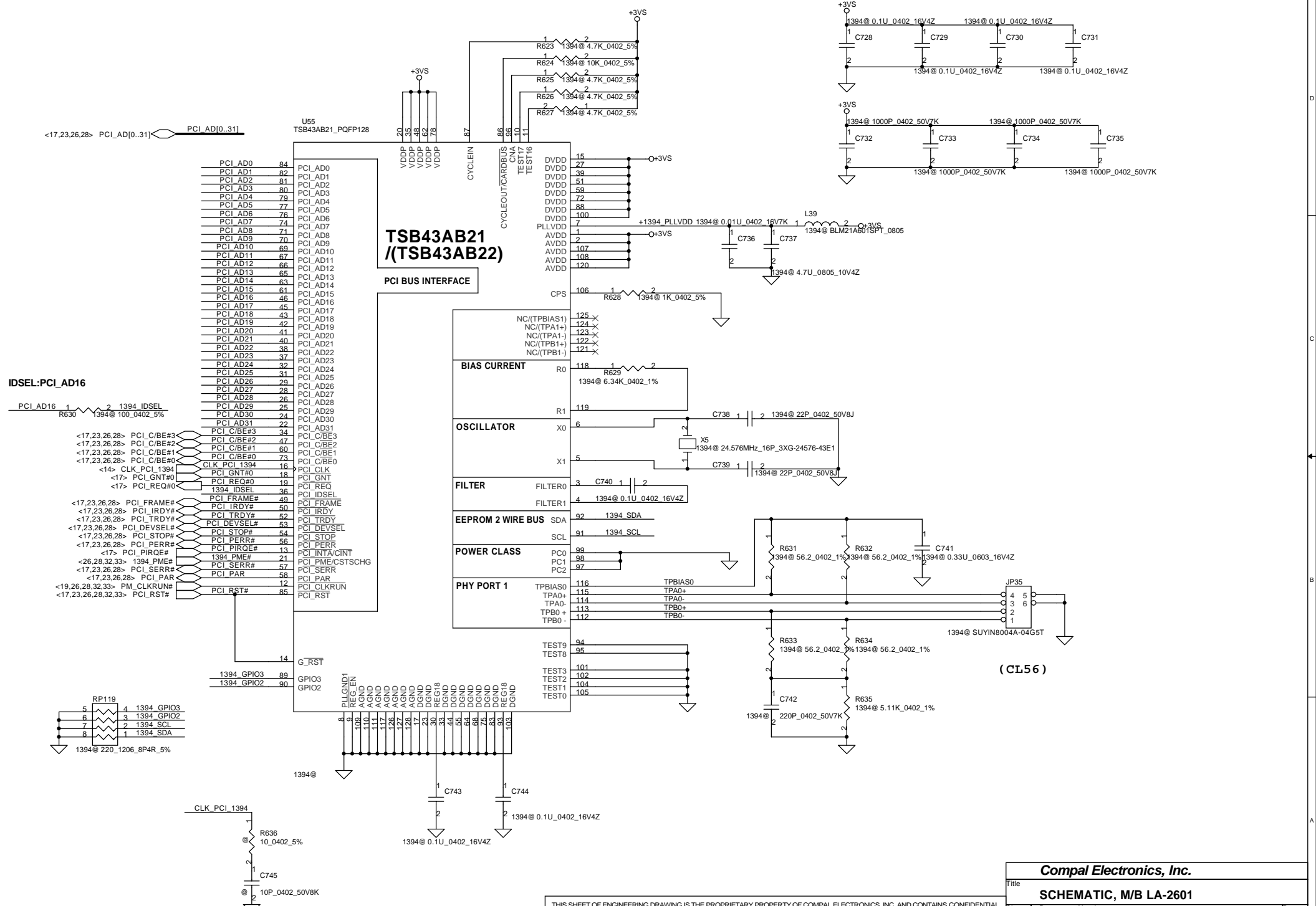


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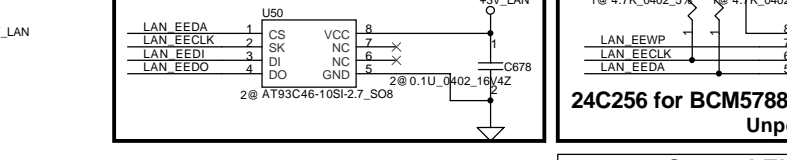
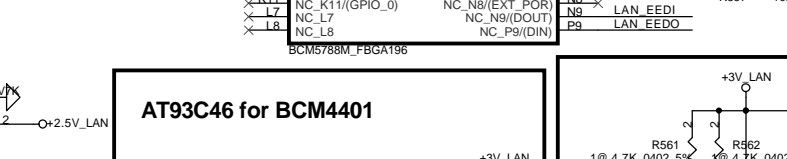
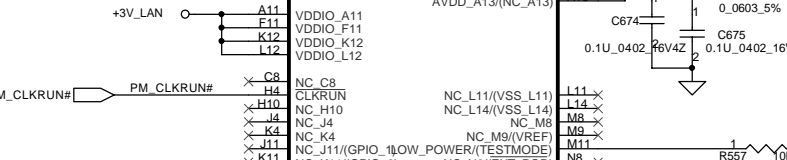
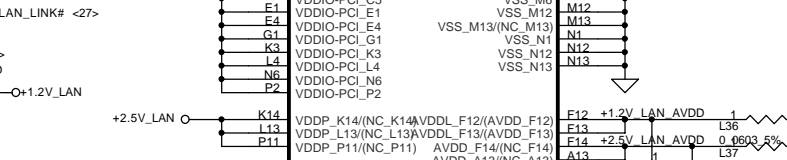
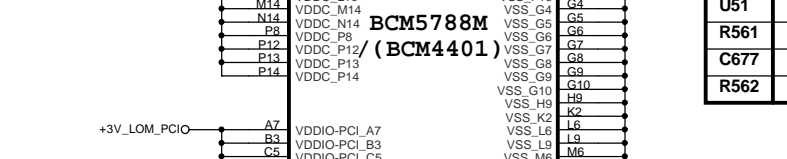
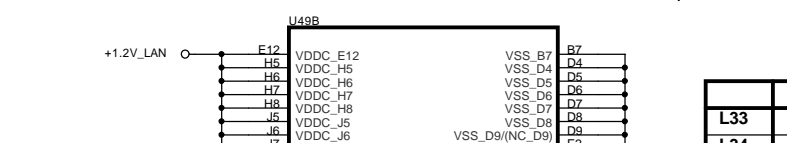
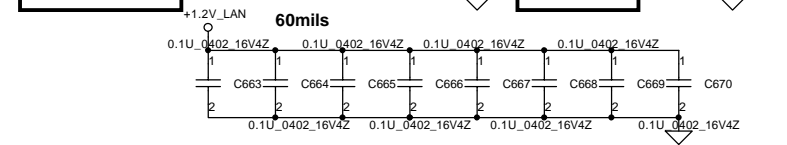
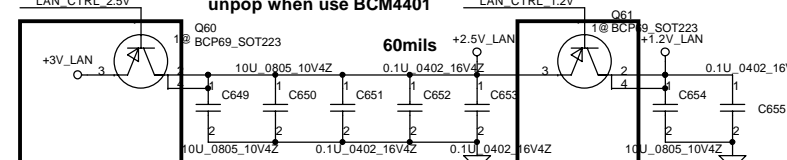
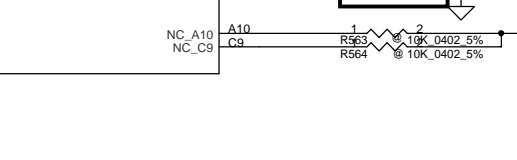
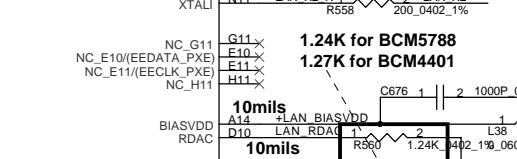
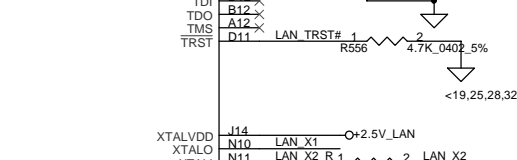
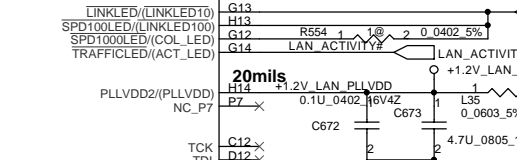
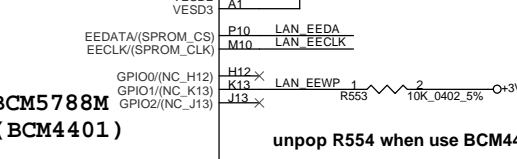
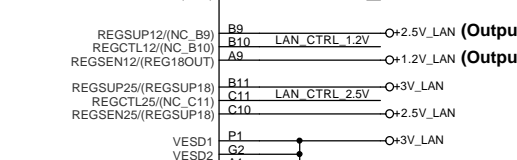
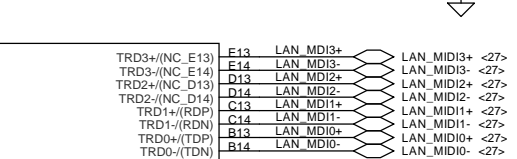
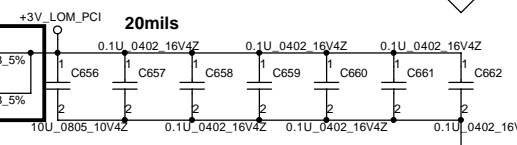
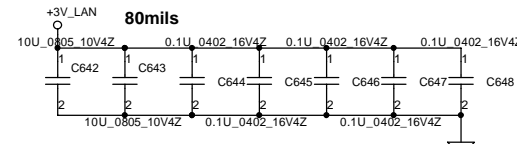
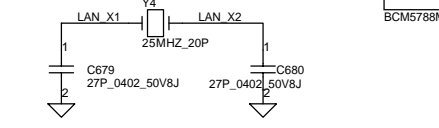
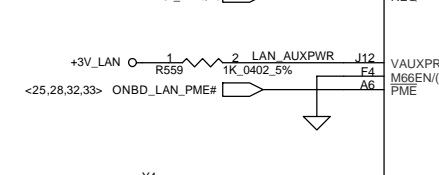
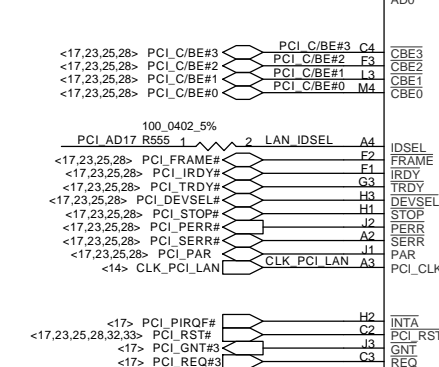
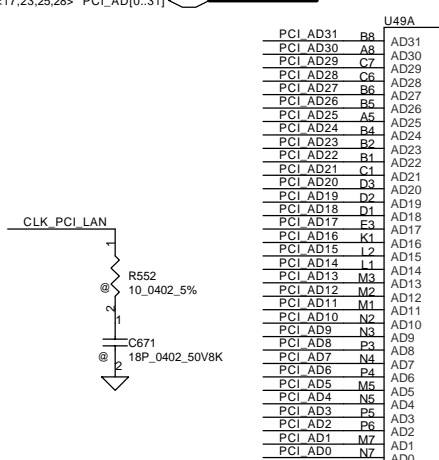
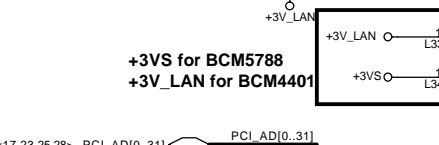


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EN_WOL# = Low,
System can wake on LAN
(keep Low when Power On)

VGS(th) = -0.45V
IDmax = 2.3A



	5788	4401
L33		Pop
L34	Pop	
Q61	Pop	
Q60	Pop	
R560	1.24K	1.27K
U50		Pop
C678		Pop
U51	Pop	
R561	Pop	
C677	Pop	
R562	Pop	

BCM5788M / (BCM4401)

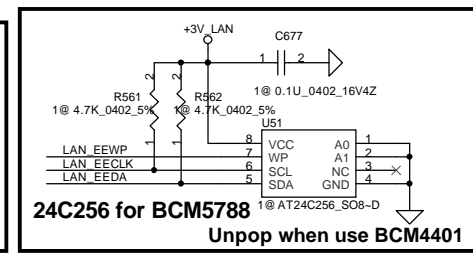
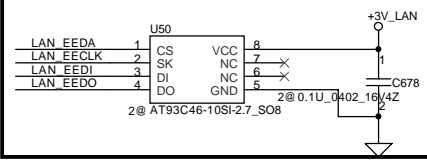
unpop R554 when use BCM4401

20mils

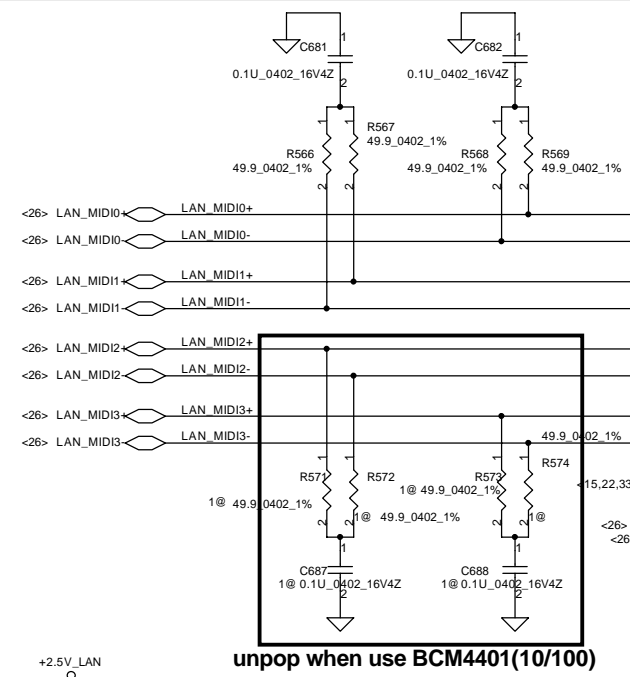
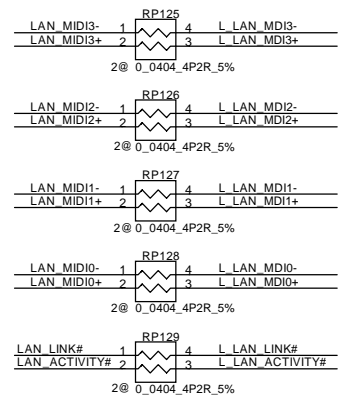
1.24K for BCM5788
1.27K for BCM4401

10mils

AT93C46 for BCM4401

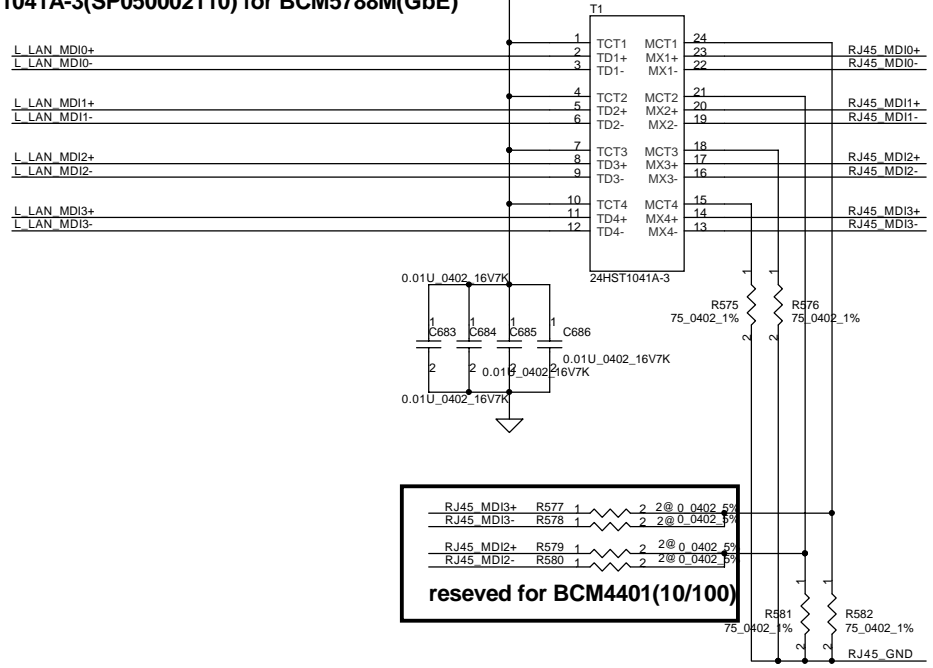


24C256 for BCM5788
Unpop when use BCM4401

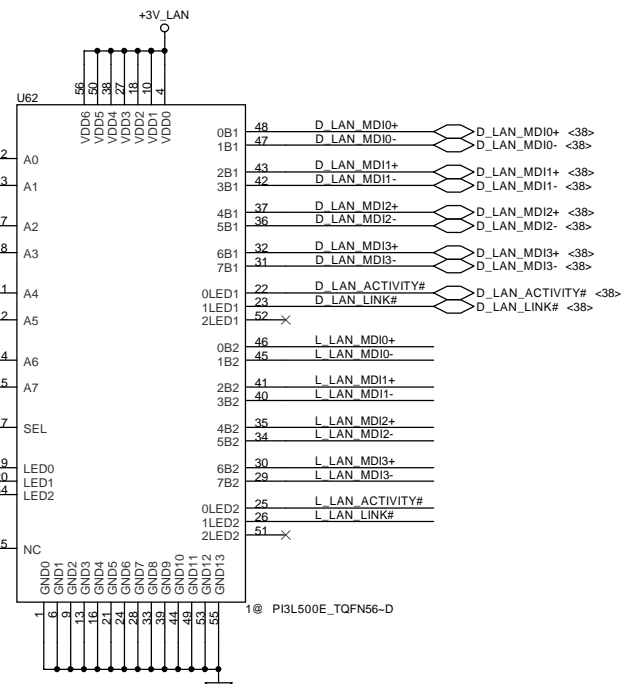


unpop when use BCM4401(10/100)

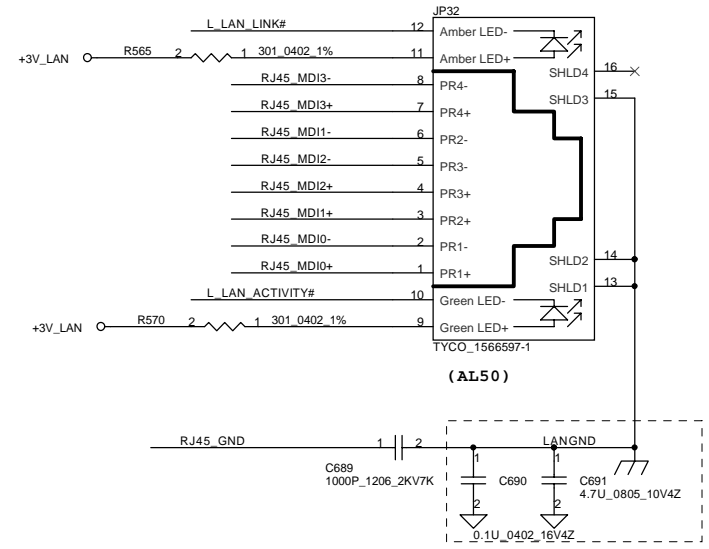
24ST0023-3(SP050004200) for BCM4401(10/100)
24HST1041A-3(SP050002110) for BCM5788M(GbE)



revised for BCM4401(10/100)



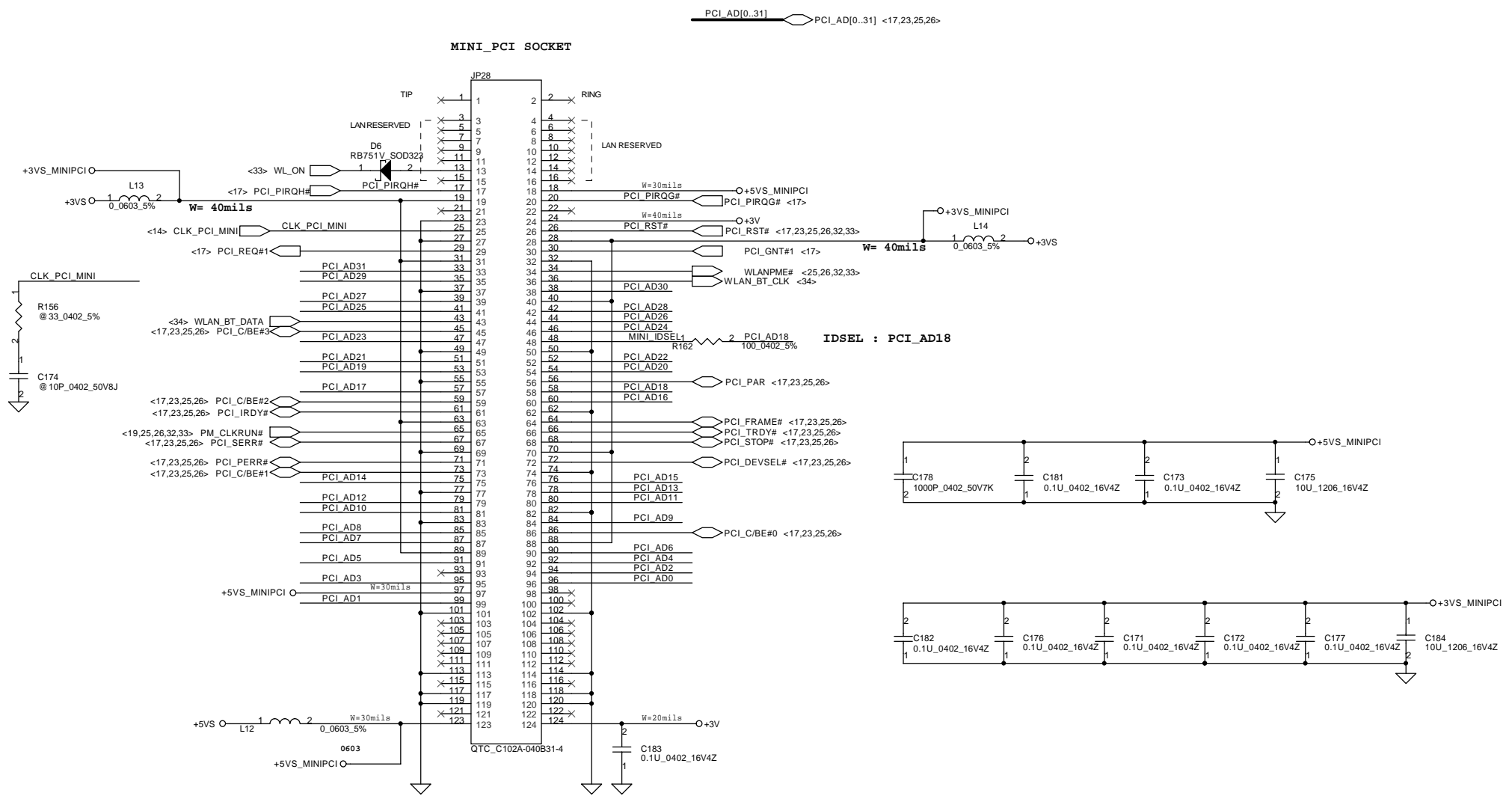
零件有誤, LED 燈要對調



LAN BCM5788M/BCM4401KFB

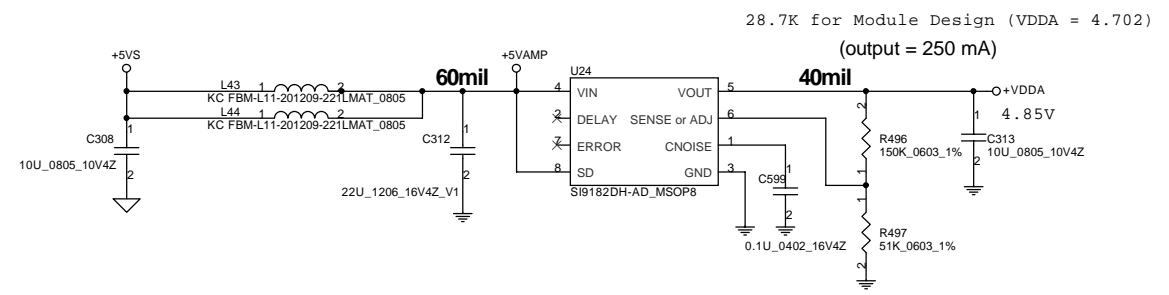
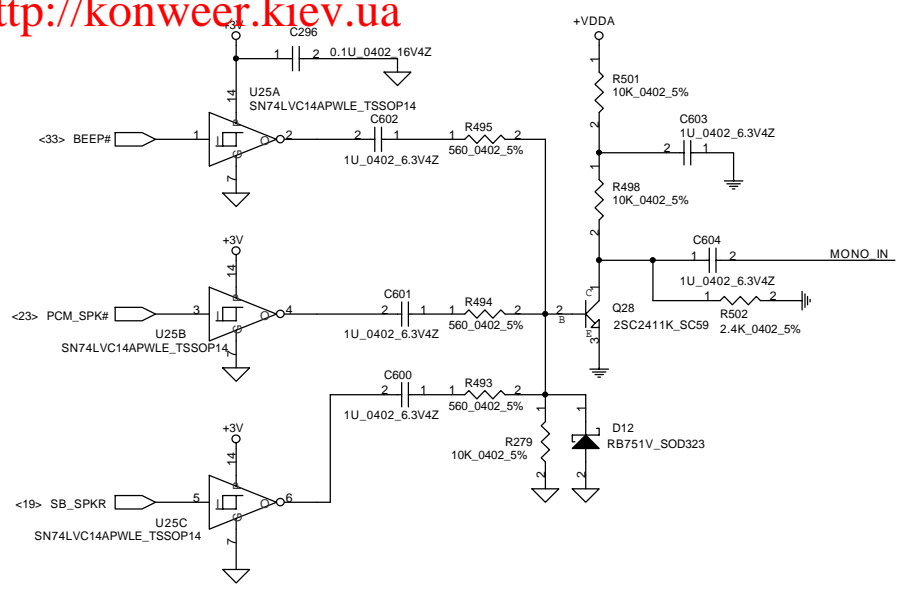
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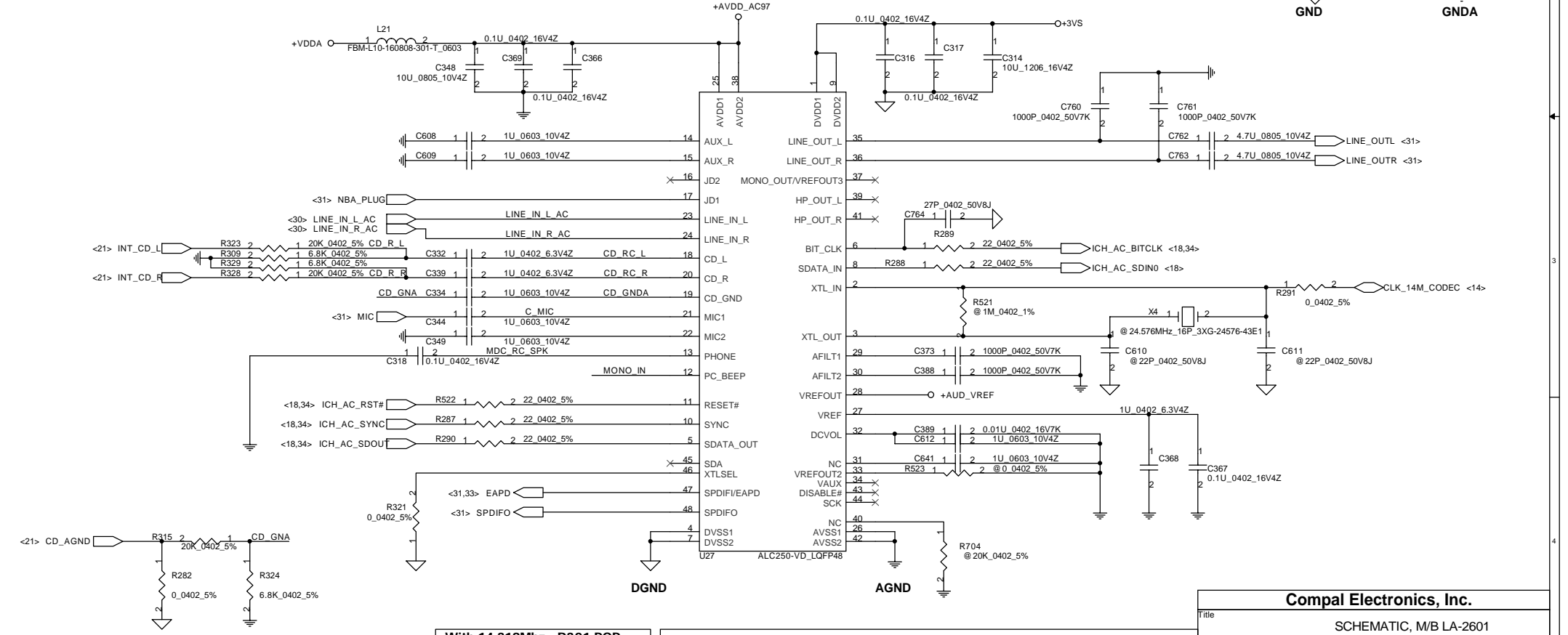
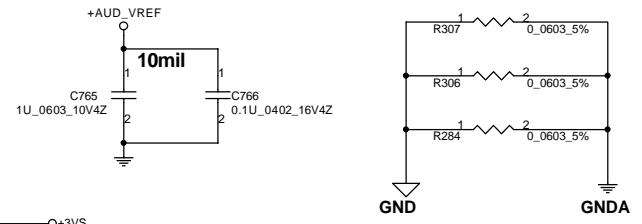
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28.7K for Module Design (VDDA = 4.702)
(output = 250 mA)

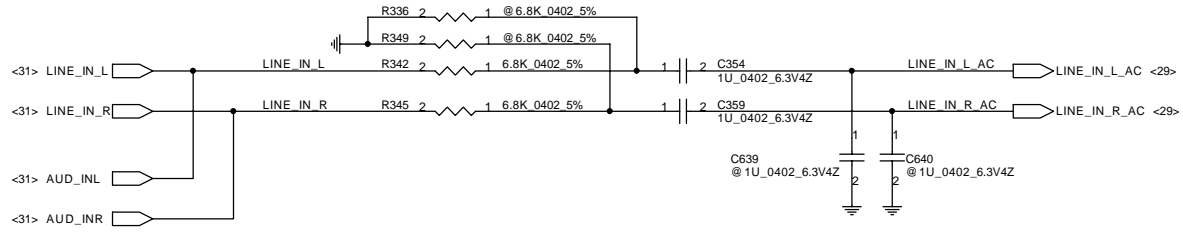
AC97 Codec



With 14.318Mhz : R321 POP
With 24.576Mhz : R321 DEPOP

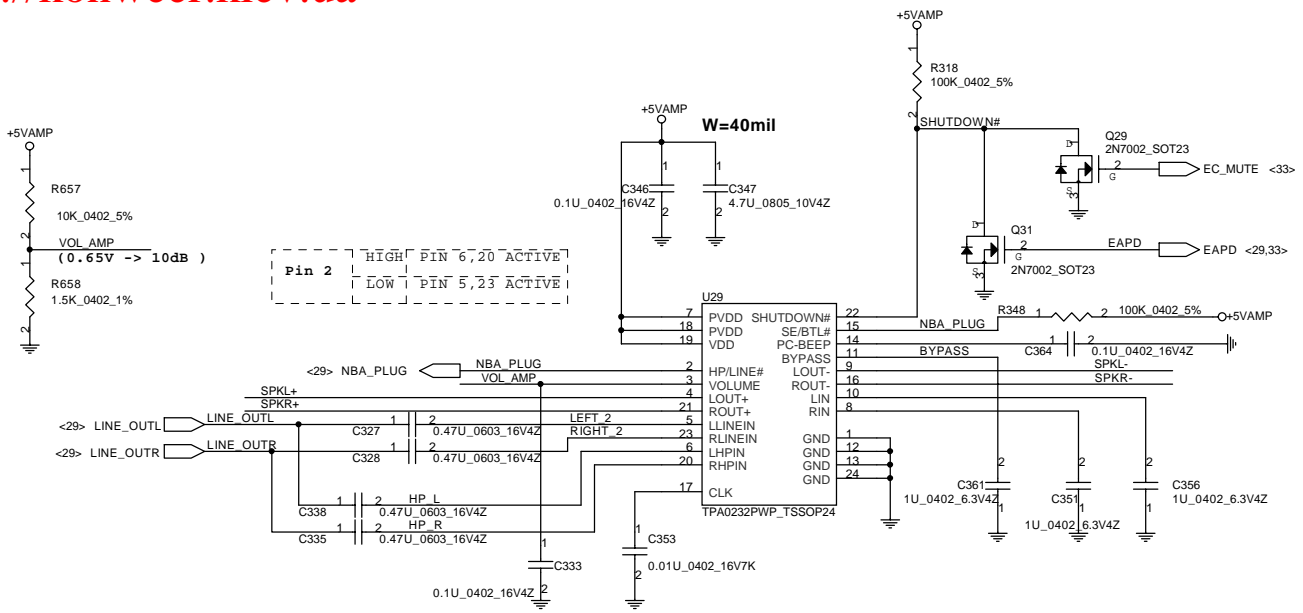
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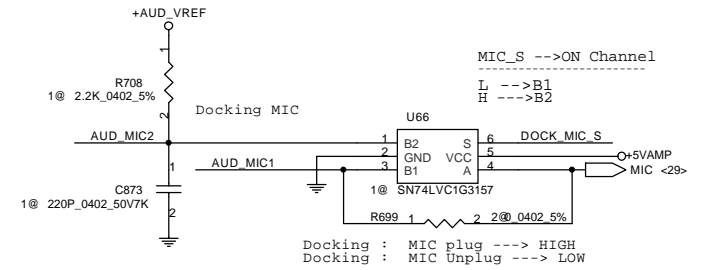
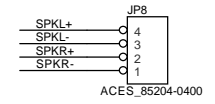


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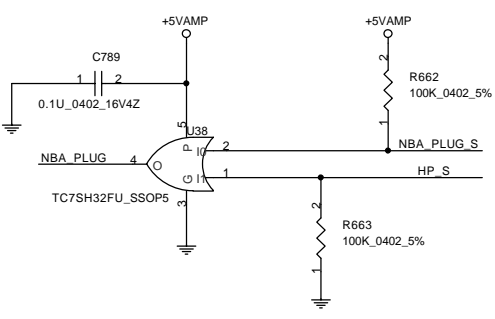
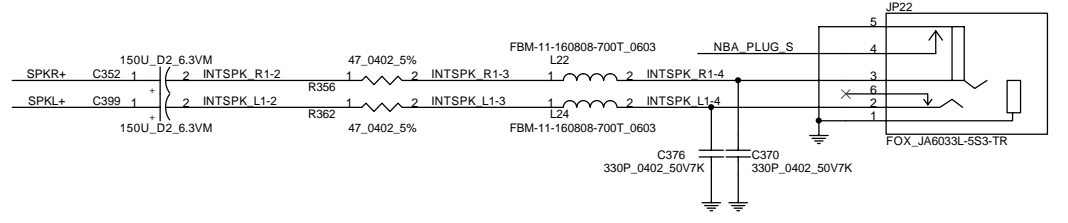
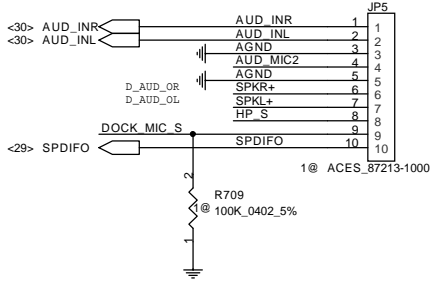
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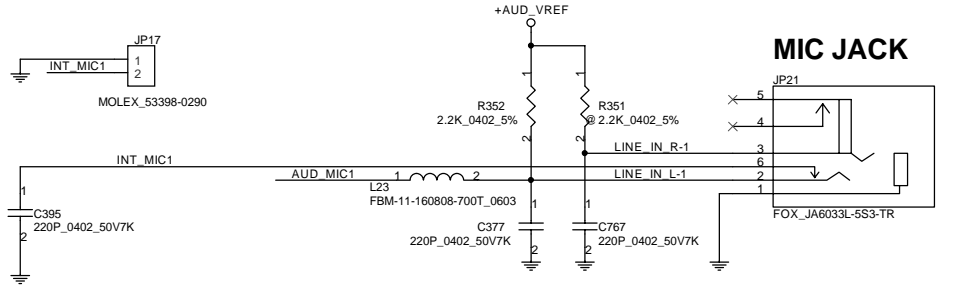
Speaker Conn.



HeadPhone JACK

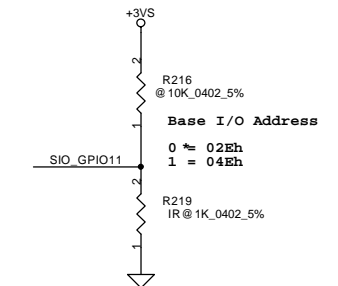
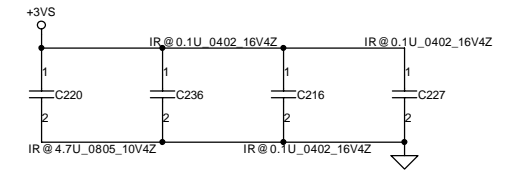
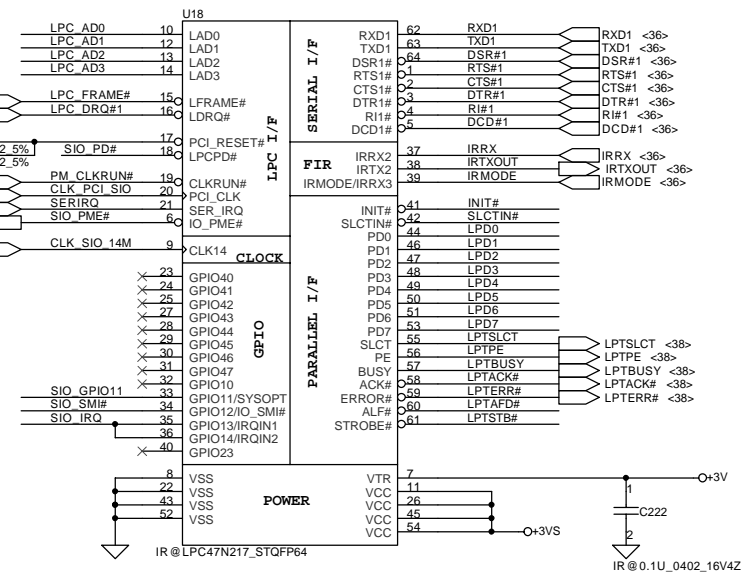
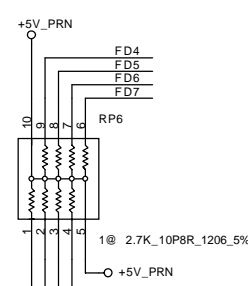
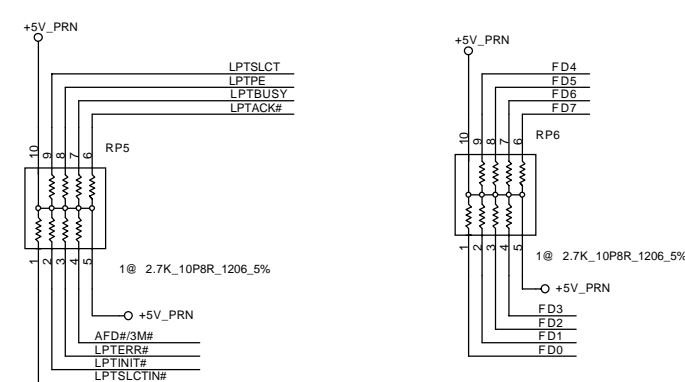
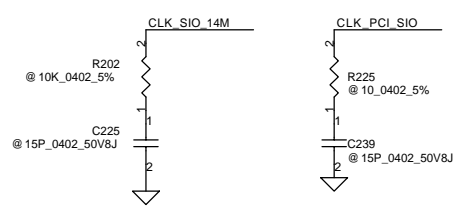
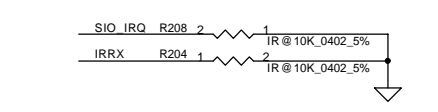
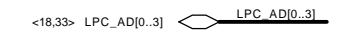
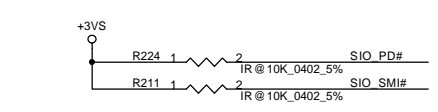


MIC JACK

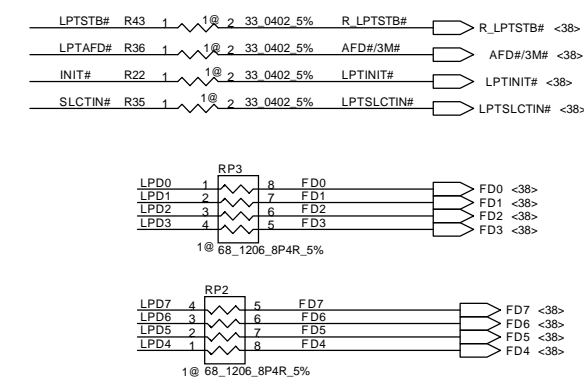


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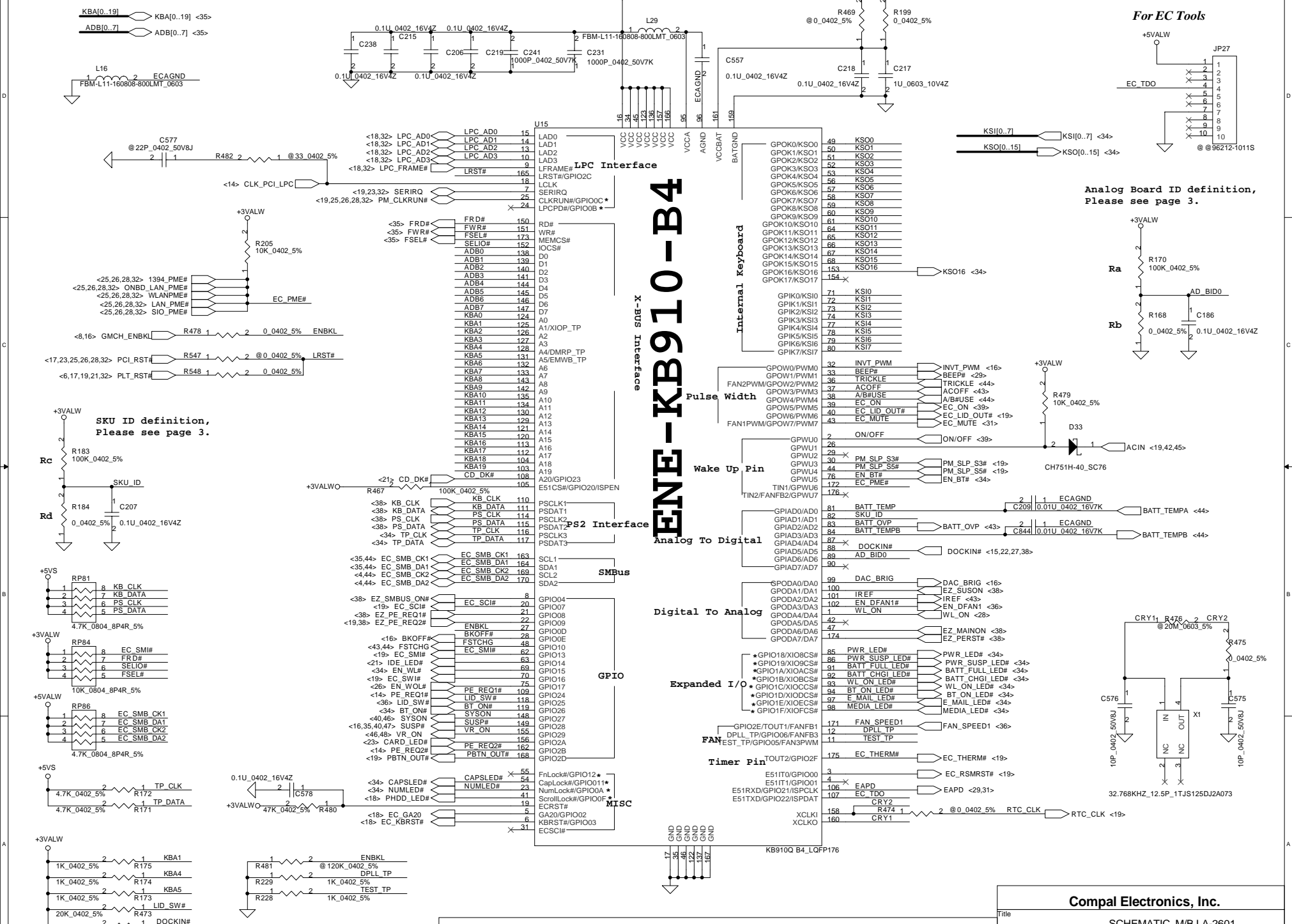


Close to Docking

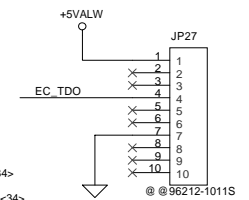


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Size:	Document Number: 401336	Rev: C
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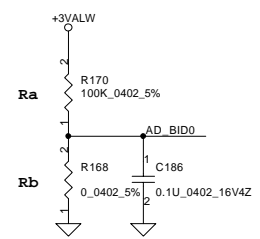
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For EC Tools



Analog Board ID definition, please see page 3.



ENE-KB910-B4

X-BUS Interface

Internal Keyboard

PS2 Interface

SMBus

GPIO

Expanded I/O

MISC

Timer Pin

FAN

Wake Up Pin

Analog To Digital

Digital To Analog

Pulse Width

ACIN

BATT_TEMP

BATT_OVP

BATT_TEMPB

DOCKIN#

DAC_BRIG

IEZ_SUSON

IEF

EN_DFAN1

WL_ON

EZ_MAION

EZ_PERST#

PWR_LED#

PWR_SUSP_LED#

BATT_FULL_LED#

BATT_CHGI_LED#

WL_ON_LED#

BT_ON_LED#

E_MAIL_LED#

BT_ON_LED#

MEDIA_LED#

FAN_SPEED1

EC_THERM#

EC_RSMRST#

EAPD

EC_TDO

RTC_CLK

EC_LOCK#

CapLock#/GPIO12*

NumLock#/GPIO11*

ScrollLock#/GPIO0F*

ECRST#

GA20/GPIO2

KBRST#/GPIO03

ECSCI#

FnLock#/GPIO12*

CapLock#/GPIO11*

NumLock#/GPIO10*

ScrollLock#/GPIO0F*

ECRST#

GA20/GPIO2

KBRST#/GPIO03

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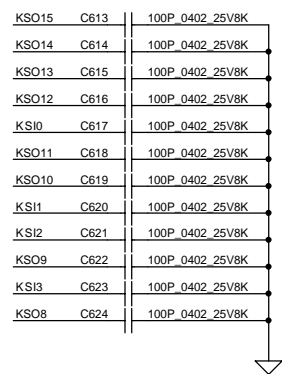
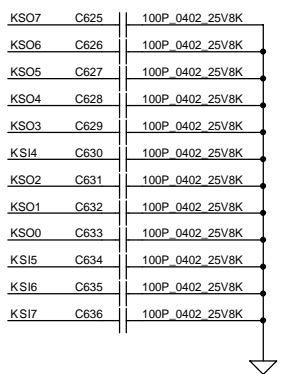
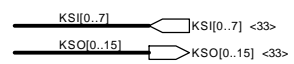
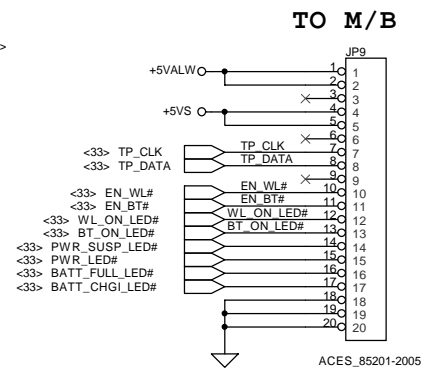
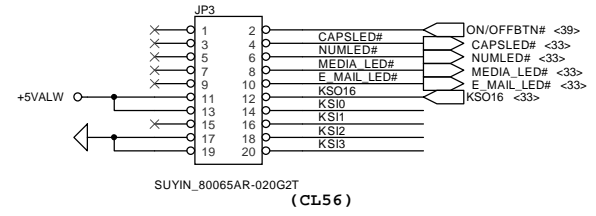
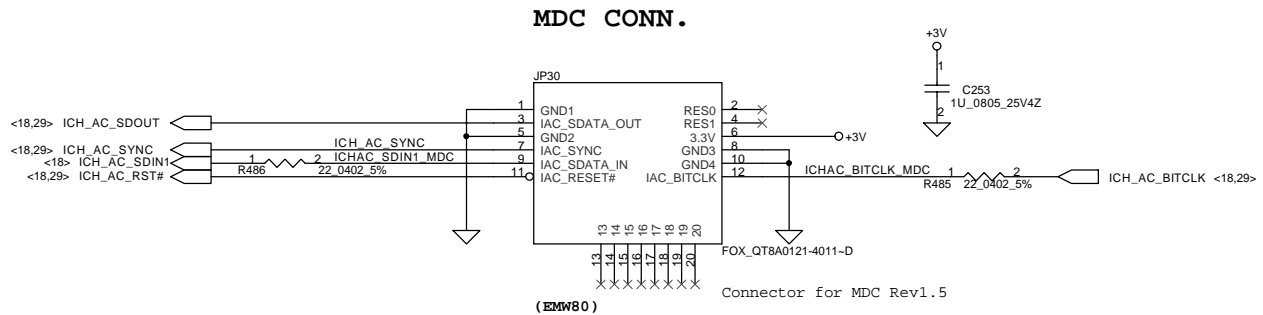
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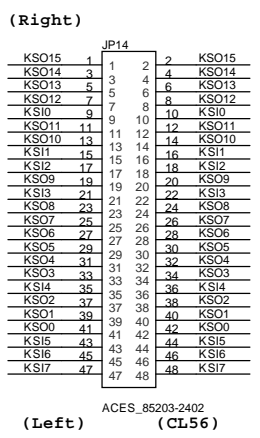
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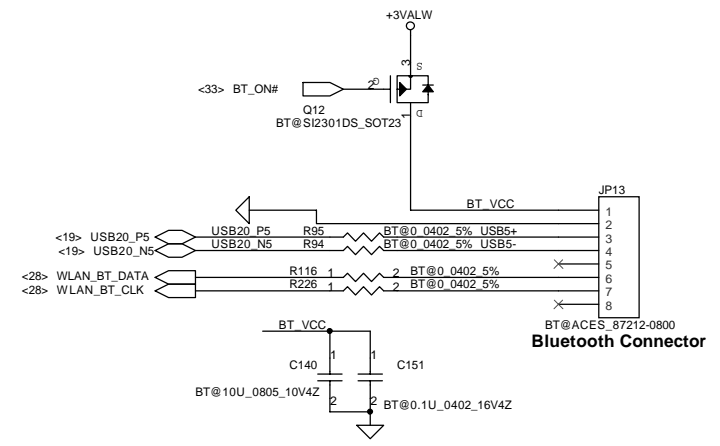
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INT_KBD CONN.



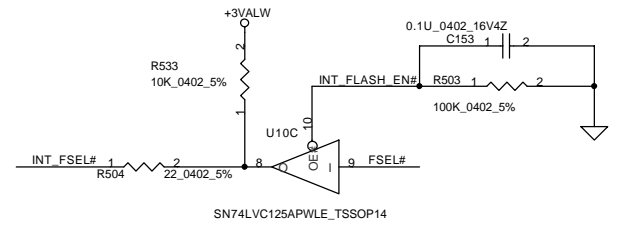
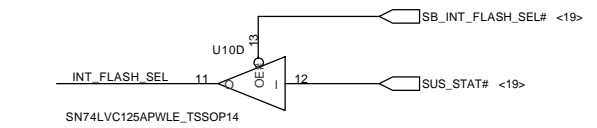
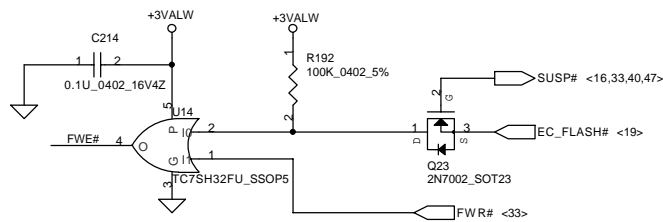
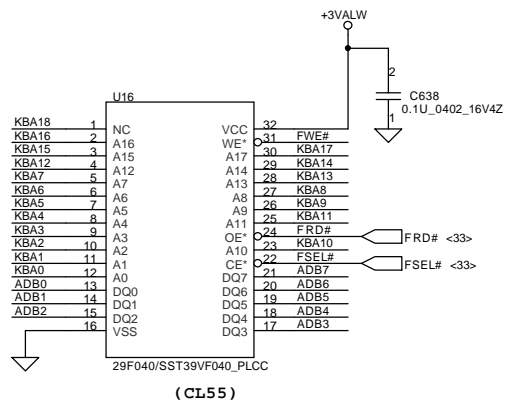
BlueTooth Interface



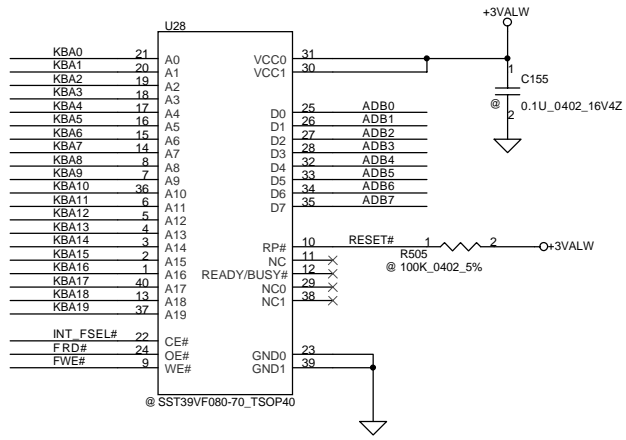
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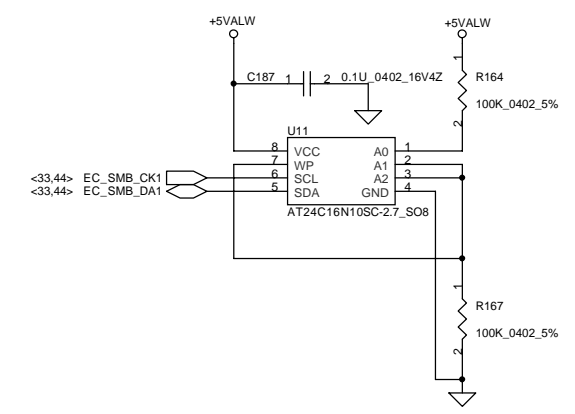
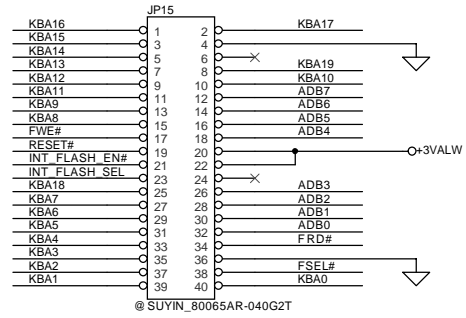
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 <33> ADB[0..7] ← ADB[0..7]



1MB Flash ROM

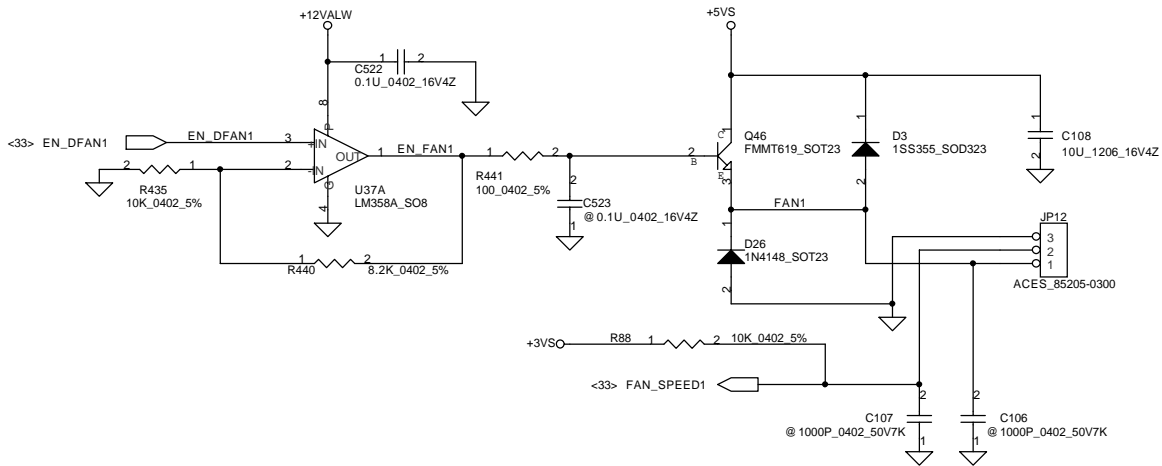


1MB ROM Socket

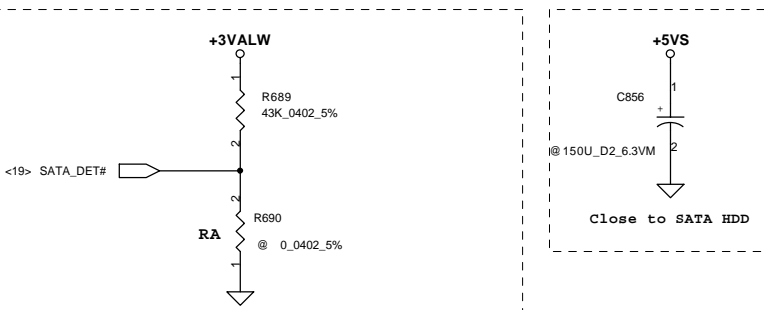
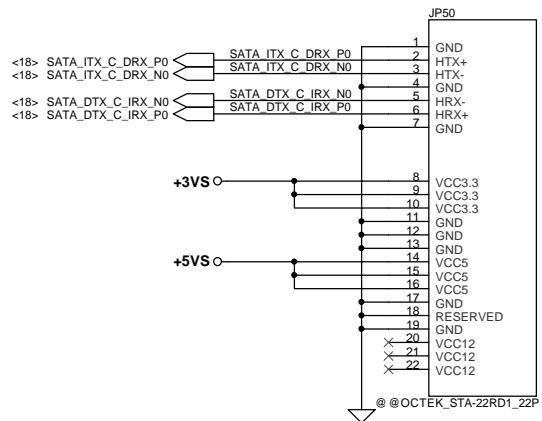


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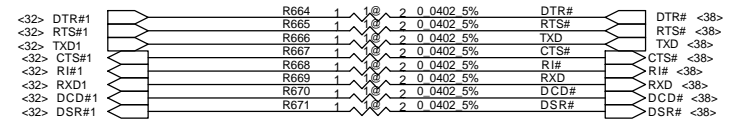


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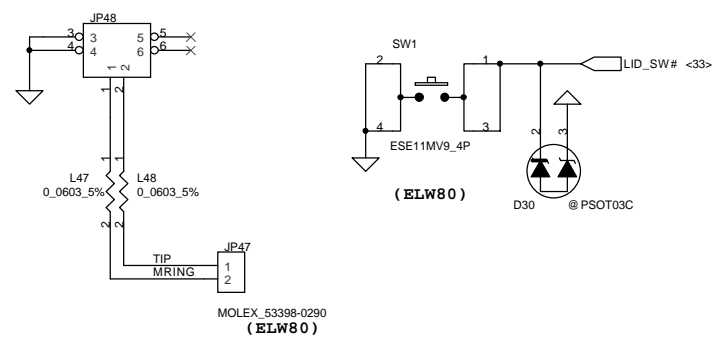


SATA Device Status	RA
Presence	POP
Removed	NO POP

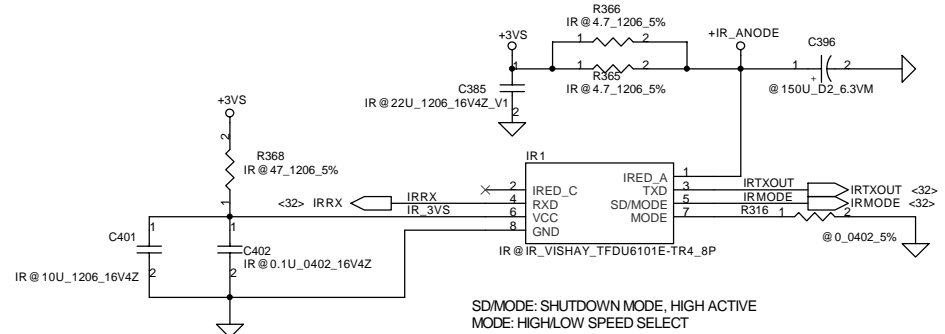
Close to Docking



FOXCONN_JM34613-L002-TR



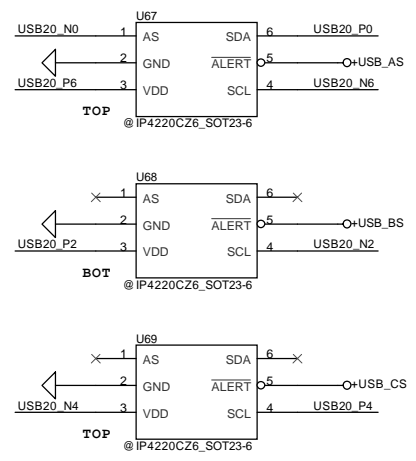
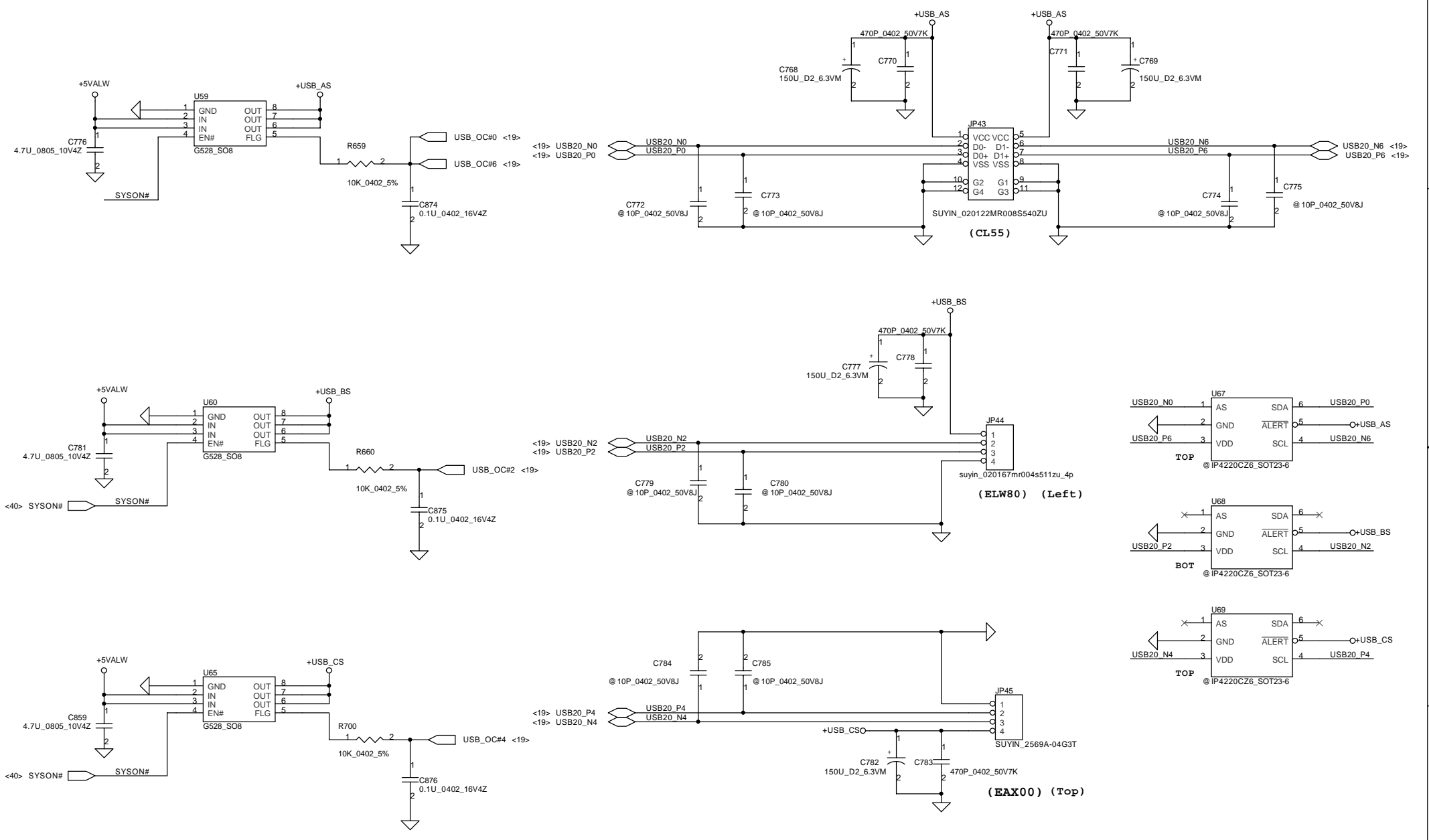
FIR Module



SD/MODE: SHUTDOWN MODE, HIGH ACTIVE
MODE: HIGH/LOW SPEED SELECT

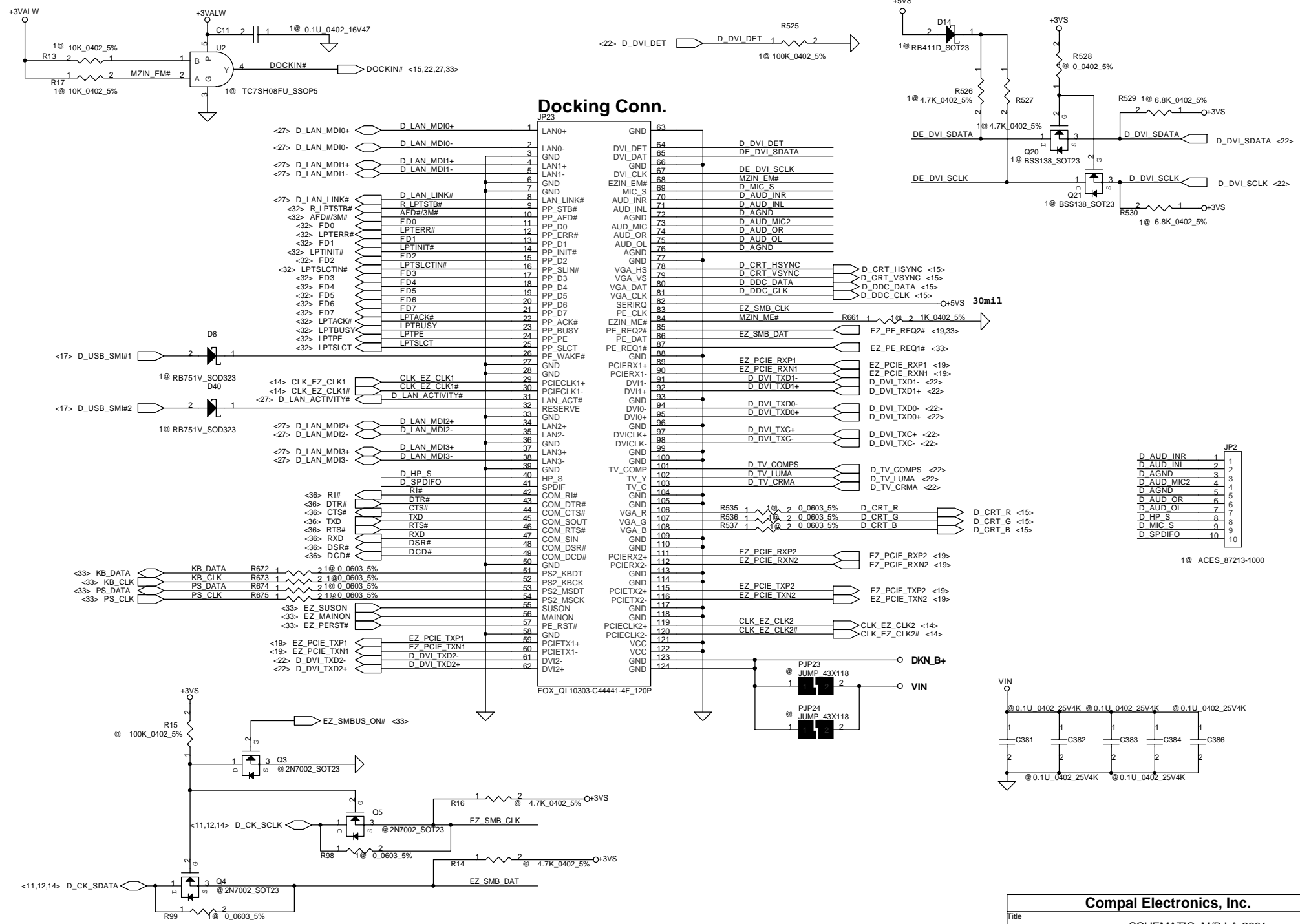
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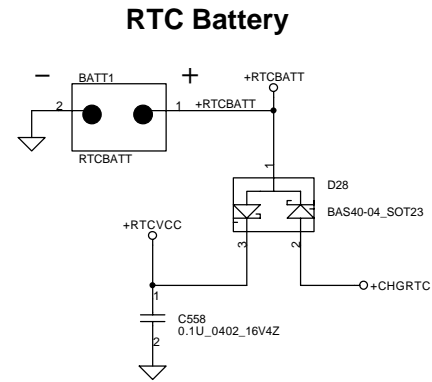
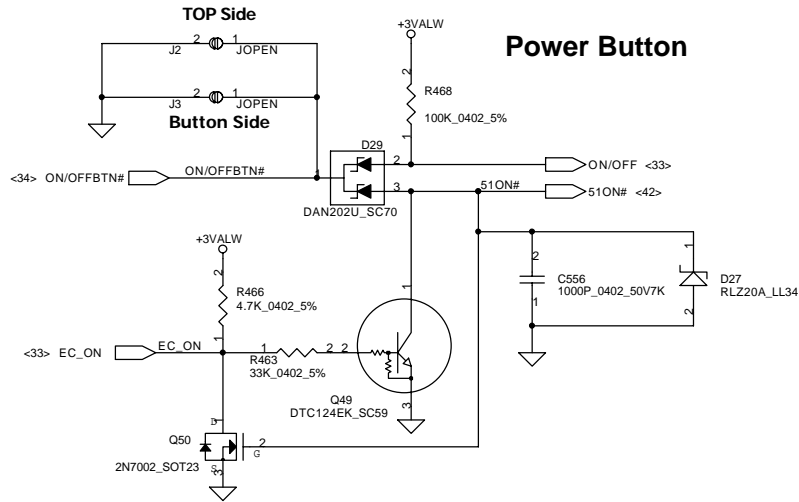
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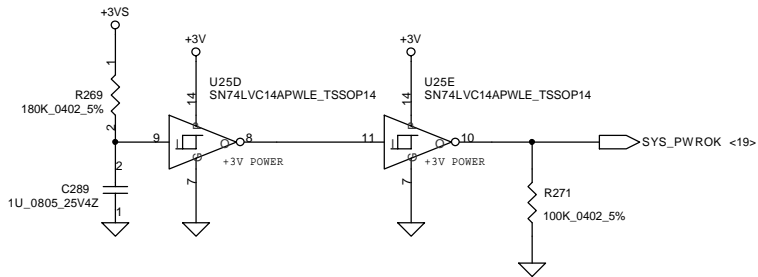
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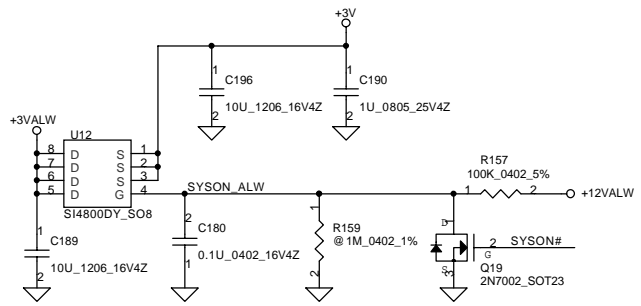
Power ON Circuit



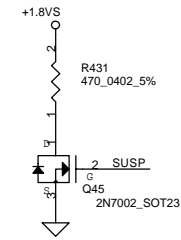
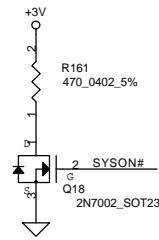
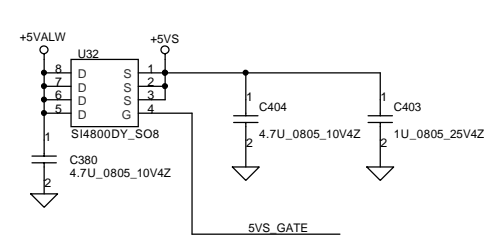
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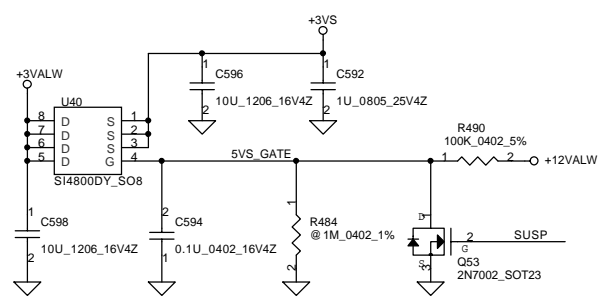
+3VALW TO +3V



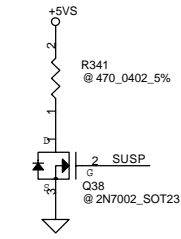
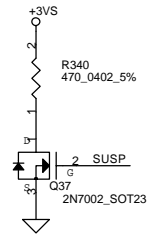
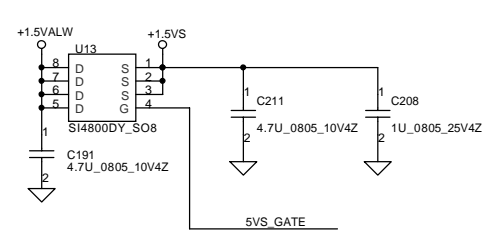
+5VALW TO +5VS



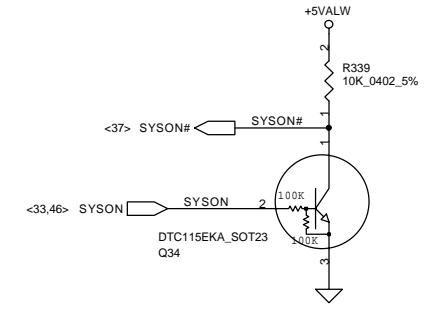
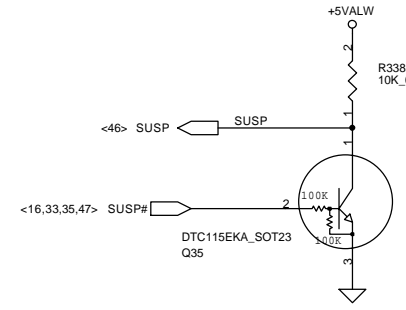
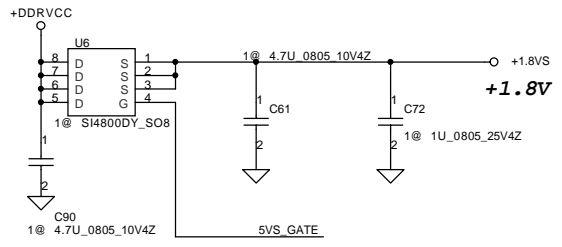
+3VALW TO +3VS



+1.5VALW TO +1.5VS

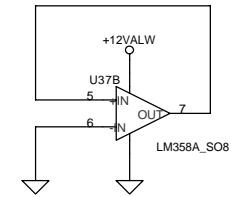
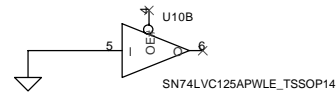
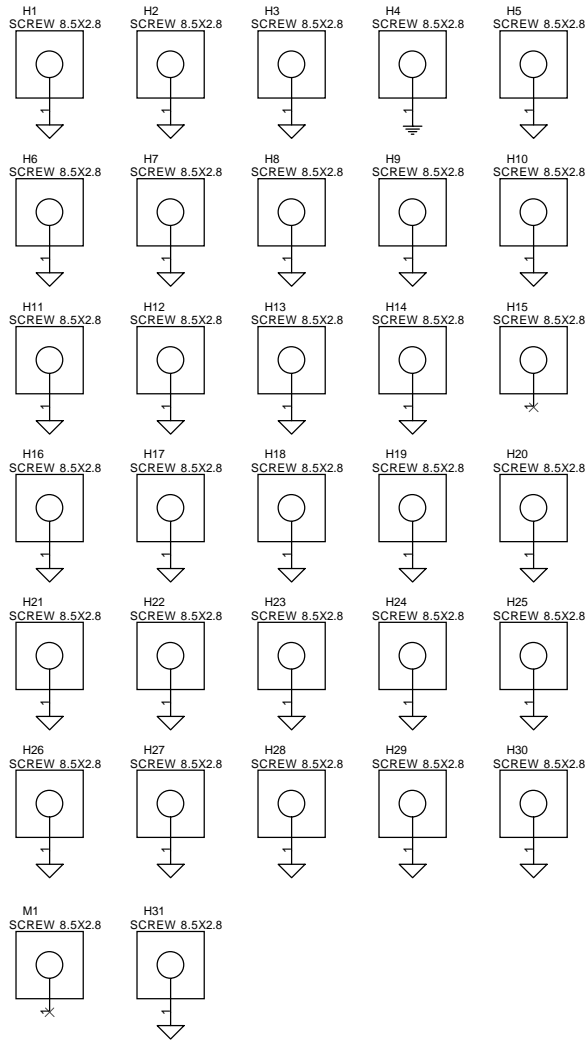
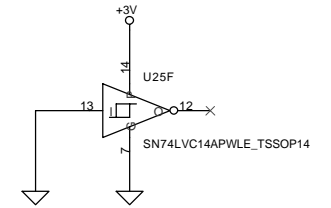
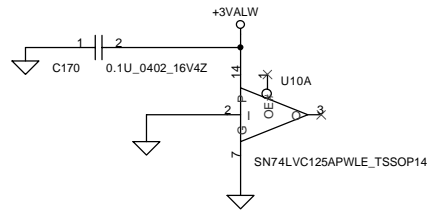
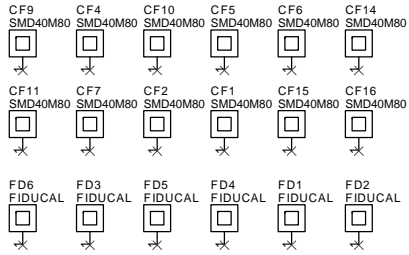


+1.8V TO +1.8VS (DDR2)



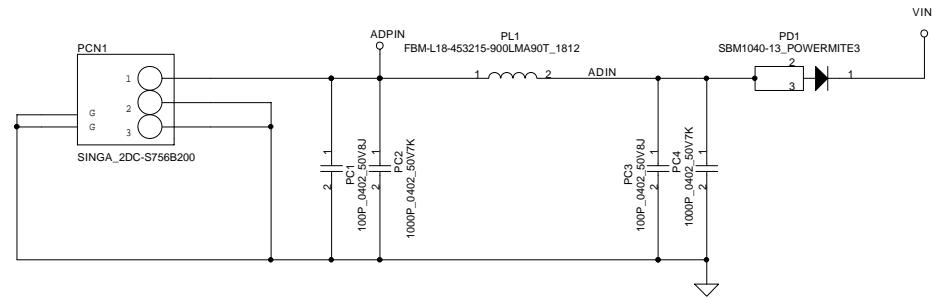
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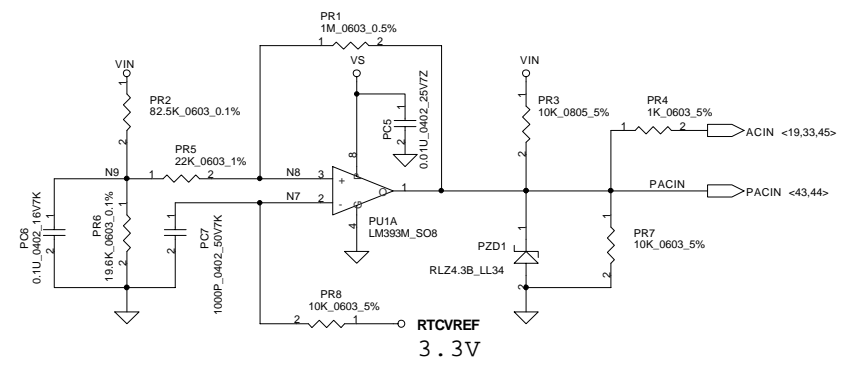


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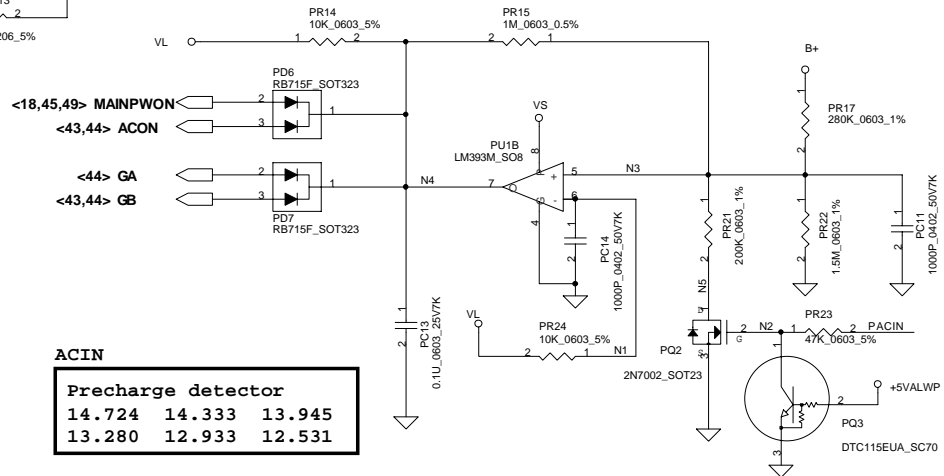
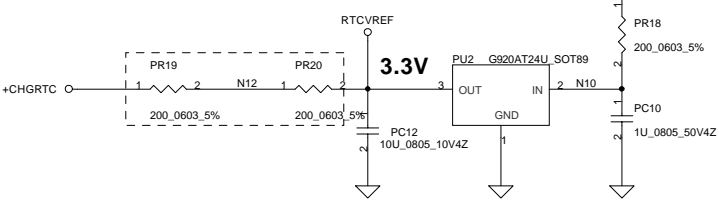
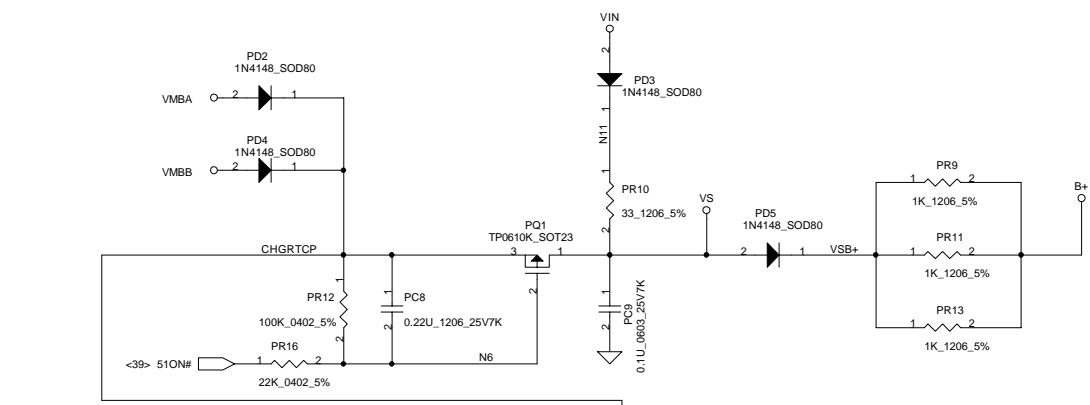
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Vin Detector		
18.234	17.841	17.449
17.597	17.210	16.813

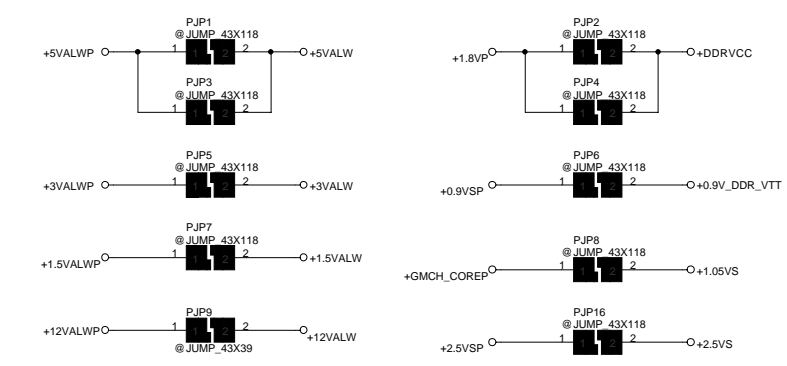


RTCVREF
3.3V



ACIN Precharge detector		
14.724	14.333	13.945
13.280	12.933	12.531

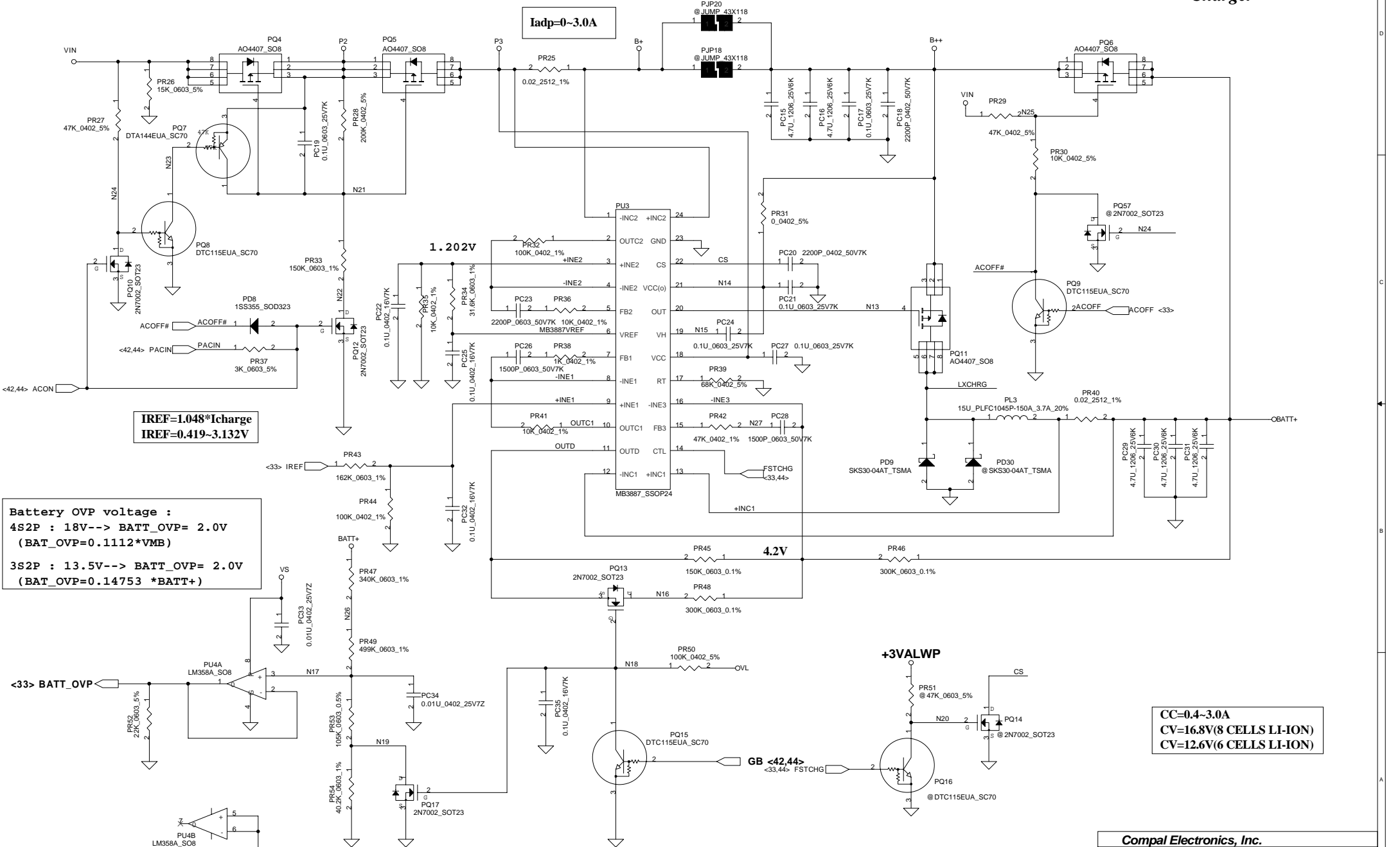
BATT Precharge detector		
7.558	7.333	7.112
6.108	5.933	5.704



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Charger



Battery OVP voltage :
 4S2P : 18V--> BATT_OVP= 2.0V
 (BAT_OVP=0.1112*VMB)
 3S2P : 13.5V--> BATT_OVP= 2.0V
 (BAT_OVP=0.14753 *BATT+)

IREF=1.048*Icharge
 IREF=0.419~3.132V

Iadp=0~3.0A

1.202V

4.2V

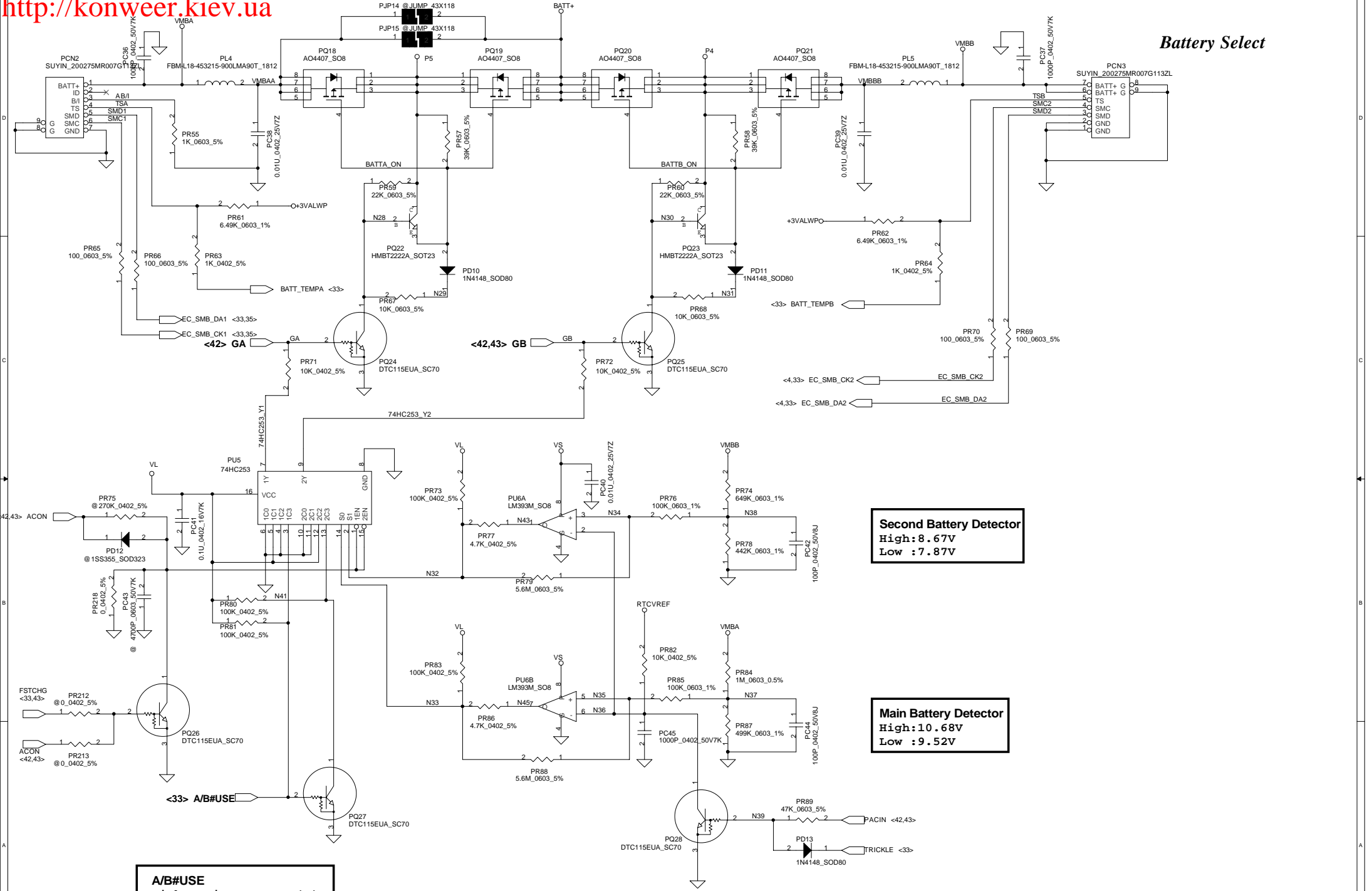
+3VALWP

CC=0.4~3.0A
 CV=16.8V(8 CELLS LI-ION)
 CV=12.6V(6 CELLS LI-ION)

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Battery Select



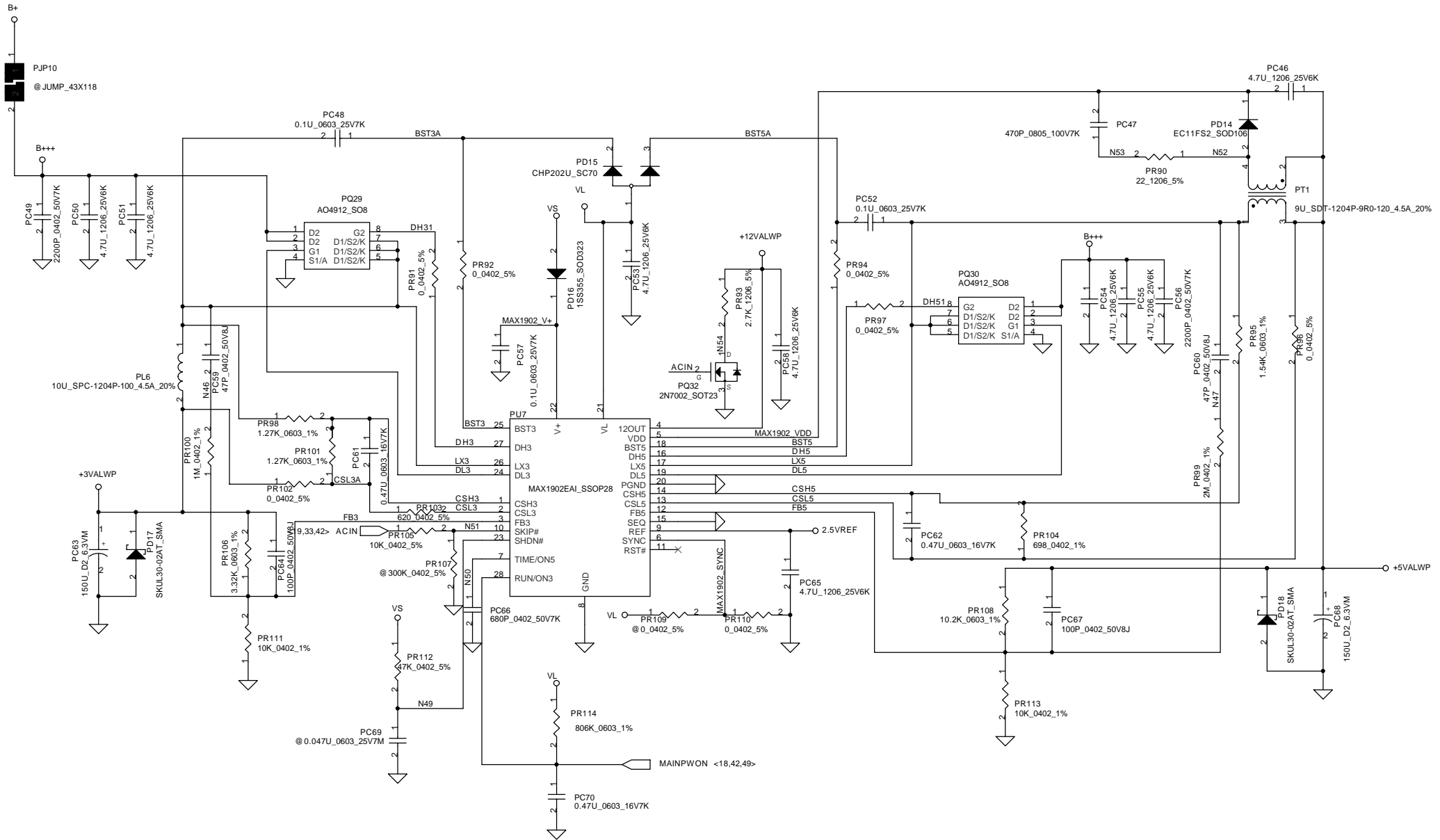
Second Battery Detector
 High: 8.67V
 Low : 7.87V

Main Battery Detector
 High: 10.68V
 Low : 9.52V

A/B#USE
 High: Main Battery (A)
 Low : Second Battery (B)

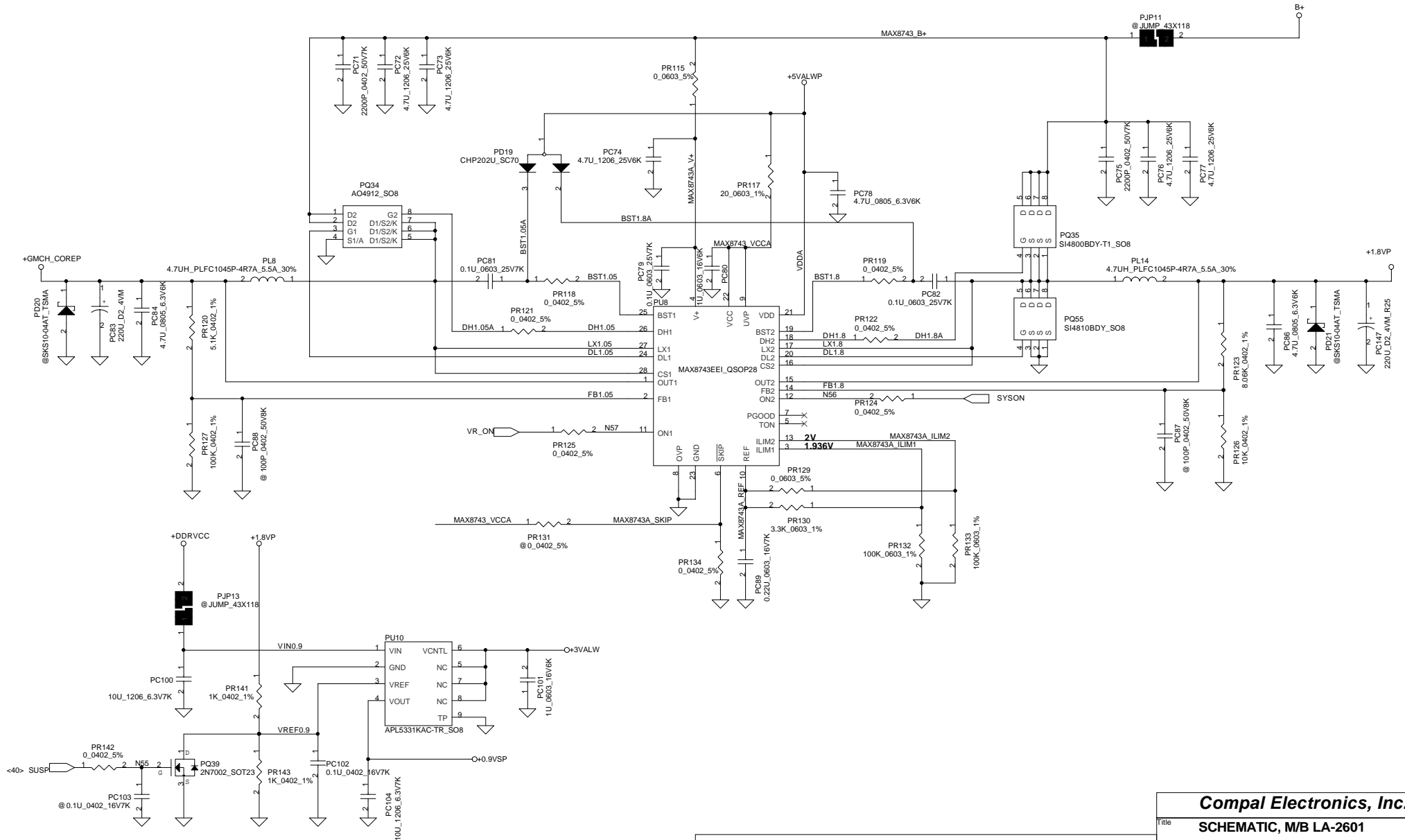
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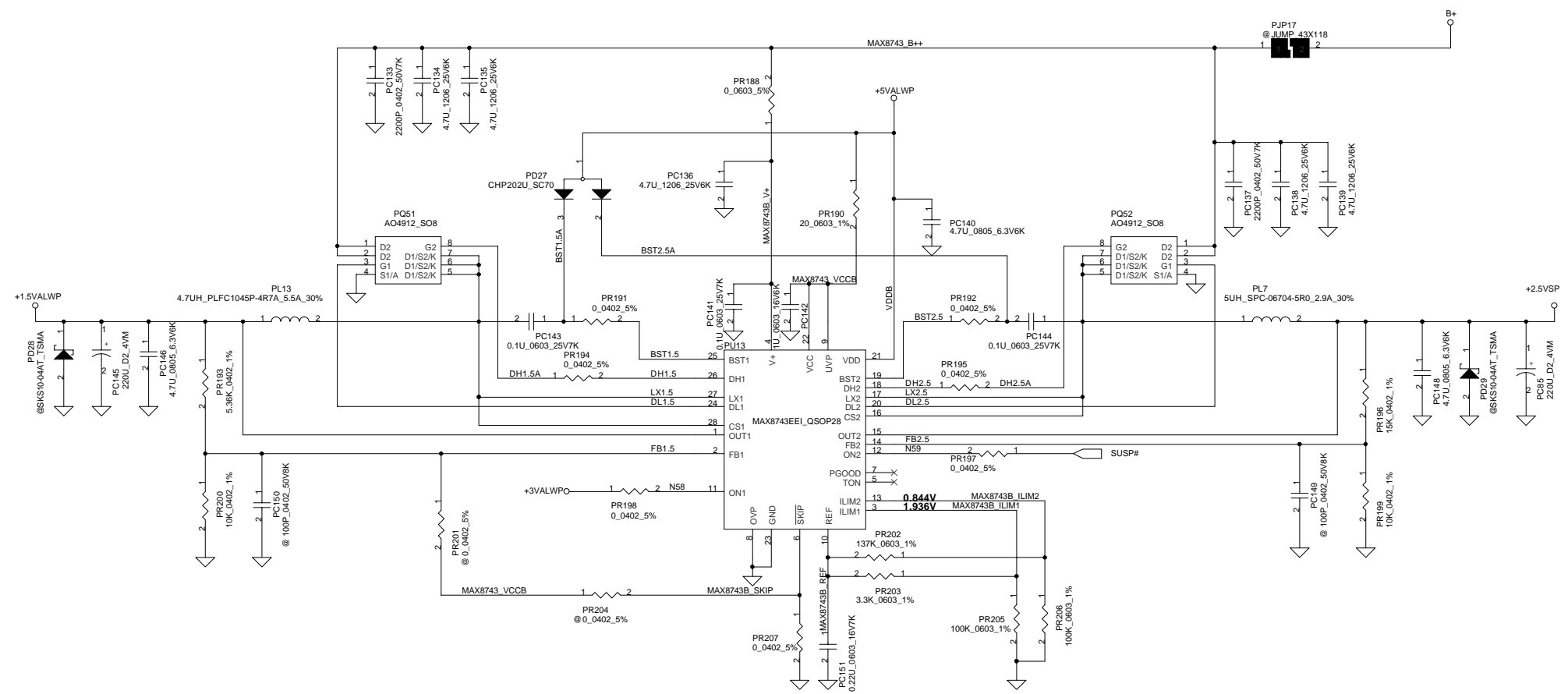
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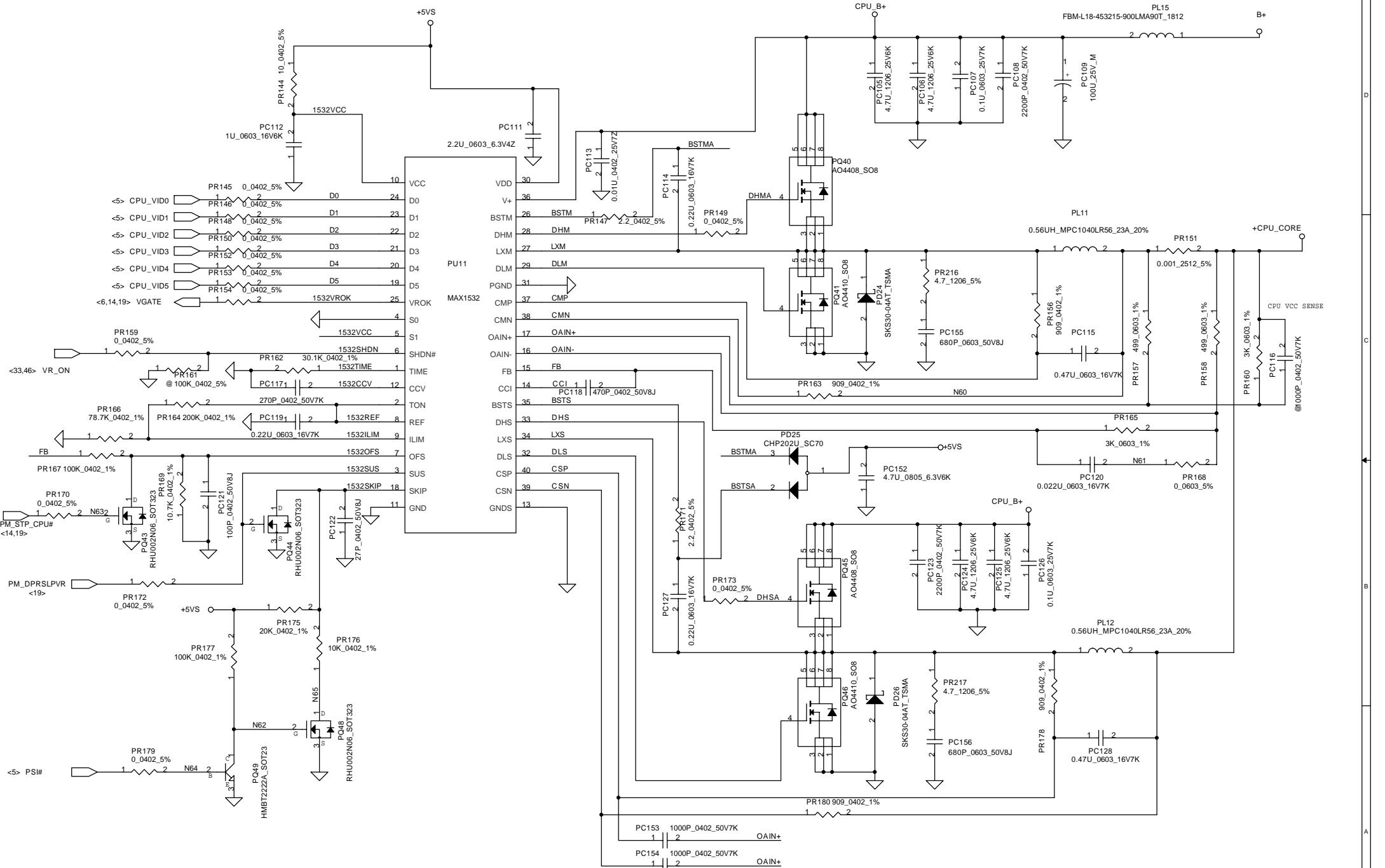
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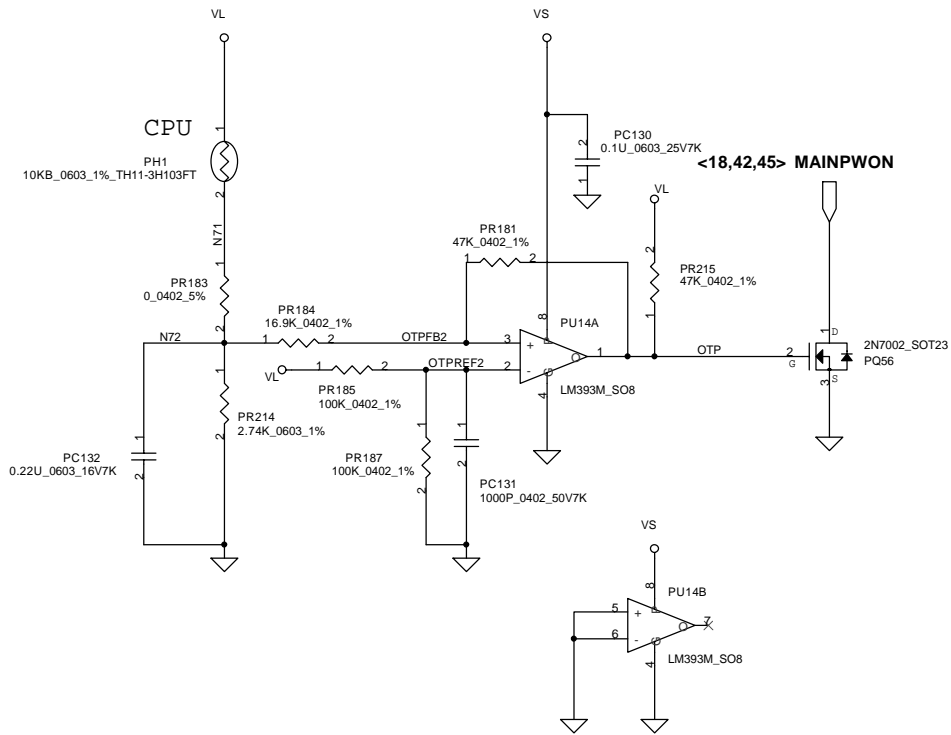
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PH1 under CPU botten side :
CPU thermal protection at 90 +-3 degree C
Recovery at 50 +-3 degree C



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Item	Reason for change	PG#	Modify List	Date	B.Ver#
1	Change DC JAcK and second battery connect.	42,44	Change DC JAcK from DC231105200 to DC231000500. Change second battery connect from	2004.10.28	
2	Modify OTP schematic.	45,49	Change PR114 from 47K_0402_5% to 806K_0603_1%; Change PR183 from 0.0402_5% to 1.37K_0402_1%; Change PR184 from 16.9K_0402_1% to 10K_0402_1%; Change PC132 from 0.22u_0603_16V to 1U_0603_16V; Change PC70 from 0.047uF to 0.47uF; Delete PR186, PR182, PU12 and PQ50.	2004.11.30	
3	To increase the pre-charge current.	42	Change PR21 from 169K_0603_1% to 200K_0603_1%; Change PR10 from 47_1206_5% to 33_1206_5%; Change PR9,PR11 and PR13 from 1.5K_1206_5% to 1K_1206_5%.	2004.12.28	
4	Change OTP schematic to EVT design.	49	Change PR183 from 1.37K_0402_1% to 0.0402_5%; Change PR184 from 10K_0402_1% to 16.9K_0402_1%; Change PC132 from 1U_0603_16V to 0.22u_0603_16V; Add PR215 47K_0402_1%; Add PR214 2.74K_0603_1%; Add PU14 LM393 and PQ56 2N7002.	2005.01.05	
5	For EMI request.	48	Add PR216 and PR217, 4.7_1206_5%; Add P155 and PC156, 680P_0603_50V; Add PL15, FBM-L18-453215-900LMA90T.	2005.01.10	
6	To increase 1.5V power plan to 1.57V by H/W request.	47	Delete PR201; Add PR200 10K_0402_1%; Add PR193 5.36K_0402_1%.	2005.01.10	

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***** Rev:0.2 PIR List 2004/08/10 Writer by Jason *****

P05 : Add @ in R63
Del @ in R56

P06 : Del @ in R54
Del JP9

P18: New add R512

P21 : New add R513
New add X3
New add C607

P21 : New add R534, R535

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